

L0M18 Half-Module

20190419

L0M18 half-module with pinholes removed

High Voltage Test

(V) (uA)	
V:D/I:D	
50	0.34
100	0.39
200	0.51
300	0.81
400	1.78
450	2.61
465	>20, decayed to less than 3 in ~2 minutes

Low Voltage Test

(V)	(A)
V:D/I:D	

Pre-Config

AVDD:	2.50	0.106
DVDD:	2.50	0.245
V125:	1.25	0.000

Post-Config

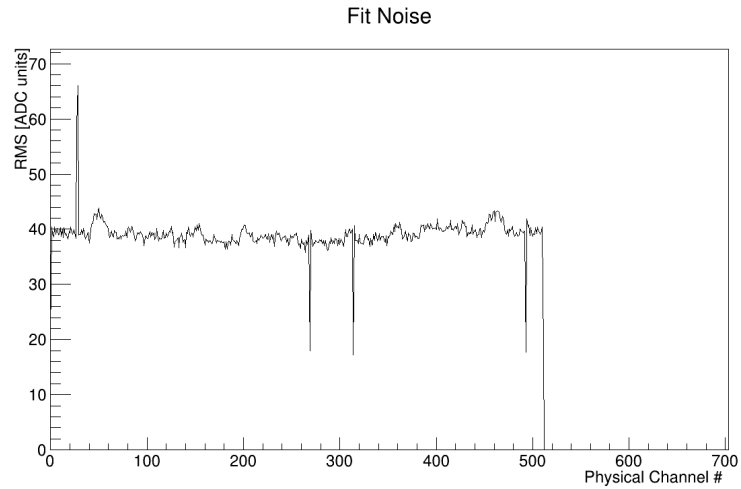
AVDD:	2.50	0.262
DVDD:	2.50	0.245
V125:	1.25	0.232

Bad Channel Table

Sector B		Sector A	
pCH	apvCH	128+apvCH	2*128-apvCH-1 128-apvCH-1
APV0			
0	0	128	255 127
APV1			
APV2			
269	13	141	242 114
314	58	186	197 69
APV3			
493	109	237	146 18
511	127	255	128 0

Nlow: 5

0V Bias:



50V Bias:

