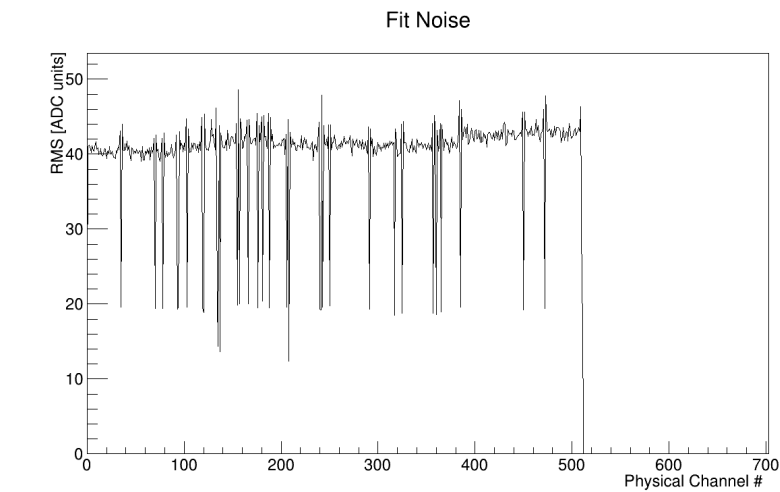
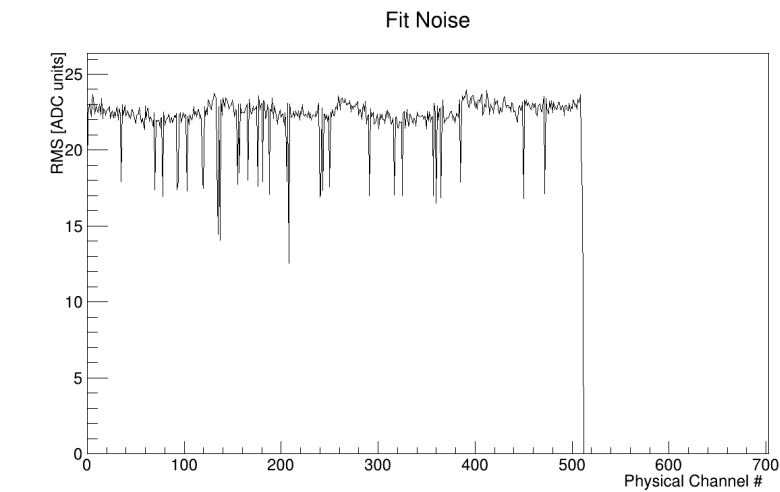
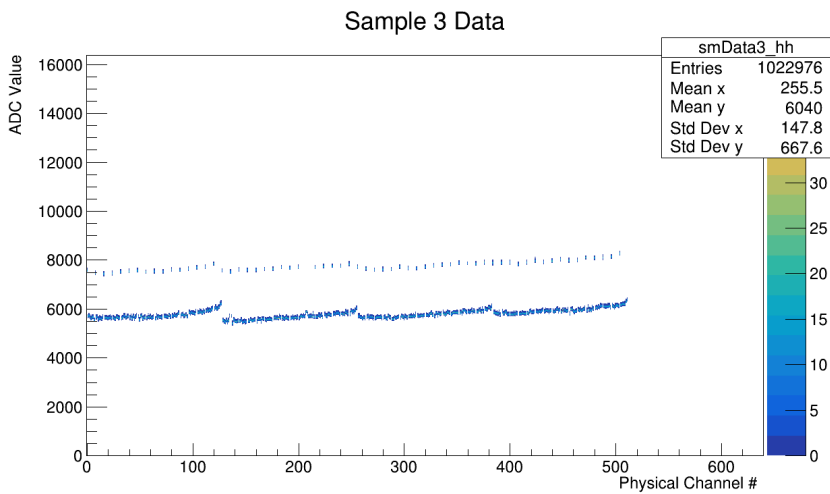


LOM10 Half-Module

2019/03/22 half module 0V bias baseline:



2019/03/22 half module 50V bias:



2019/03/22
LOM10 half module. Pinholes have been pulled.

High Voltage Test

V/D:I/D
(V) (uA)
50 5.38
100 5.50
200 5.82
300 6.23
400 22.20

Low Voltage Test

Pre-Config

V/D:I/D
(V) (A)
AVDD: 2.50 0.107
DVDD: 2.50 0.241
V125: 1.25 0.000

Post-config

V/D:I/D
(V) (A)
AVDD: 2.50 0.304
DVDD: 2.50 0.245
V125: 1.25 0.226

Bad Channels Table

Sector B Sector A

pCH | apvCH | 128+apvCH | 2*128-apvCH-1| 128-apvCH-1
APV0

0 | 0 | 128 | 255 | 127
35 | 35 | 163 | 220 | 92
70 | 70 | 198 | 185 | 57
78 | 78 | 206 | 177 | 49
93 | 93 | 221 | 162 | 34
94 | 94 | 222 | 161 | 33
103 | 103 | 231 | 152 | 24
119 | 119 | 247 | 136 | 8
120 | 120 | 248 | 135 | 7
APV1

134 | 6 | 134 | 249 | 121
135 | 7 | 135 | 248 | 120
137 | 9 | 137 | 246 | 118
155 | 27 | 155 | 228 | 100
157 | 29 | 157 | 226 | 98
166 | 38 | 166 | 217 | 89
176 | 48 | 176 | 207 | 79
181 | 53 | 181 | 202 | 74
188 | 60 | 188 | 195 | 67
206 | 78 | 206 | 177 | 49
208 | 80 | 208 | 175 | 47
240 | 112 | 240 | 143 | 15
241 | 113 | 241 | 142 | 14
243 | 115 | 243 | 140 | 12
250 | 122 | 250 | 133 | 5

APV2

291 | 35 | 163 | 220 | 92
317 | 61 | 189 | 194 | 66
325 | 69 | 197 | 186 | 58
357 | 101 | 229 | 154 | 26
360 | 104 | 232 | 151 | 23
365 | 109 | 237 | 146 | 18

APV3

385 | 1 | 129 | 254 | 126
450 | 66 | 194 | 189 | 61
472 | 88 | 216 | 167 | 39
510 | 126 | 254 | 129 | 1
511 | 127 | 255 | 128 | 0
Nlow: 35