

LOM14 Hybrid

2019/03/21

LOM14 Pretest

High Voltage

V/D:I/D

(V) (uA)

50 0.18

100 0.33

200 0.62

300 0.91

400 1.13

500 1.37

Low Voltage Test

Pre-Config

V/D:I/D

(V) (A)

AVDD: 2.50 1.06

DVDD: 2.50 0.245

V125: 1.25 0.000

Post-Config

V/D:I/D

(V) (A)

AVDD:

A System Error Has Occured!

Please HardReset and then configure!

System is in run state 'Stopped'

Fpga 0 Hybrid 0 Apv 2 Not synced

Fpga 0 Hybrid 0 Apv 3 Not synced

2 Out Of 4 Apvs Are Synced!