

COB RCE Reset

Usage is: `cob_rce_reset <rce address> [<rce_address> ...]`

Send hardware reset the RCEs specified by `<rce_address>`.
This will pull the hardware reset line on the RCEs' FPGA.

Each `<rce address>` is of the form:

`shelfmanager_ip[/slot[/bay[/rce]]]`

Where:

`shelfmanager_ip`: the hostname of the shelf manager or its IP address
WARNING: THIS IS NOT THE SHELF NAME!

`slot`: the physical slot number (1-16)
or the case-insensitive keyword ALL

`bay`: the bay number (0-7)
or one of the case-insensitive keywords:
ALL, CMB, DTM, DPM, RTM, CEN

`rce`: the RCE number (0-3)
or the case-insensitive keyword ALL.

Leaving off the end of the address is the same
as using the keyword ALL for the omitted arguments.

Examples:

```
egbert-sm/4/3/2
egbert-sm/4/3
egbert-sm/4/
egbert-sm/
egbert-sm/4/ALL/2
egbert-sm/4/dpm
egbert-sm/4/DPM/0
172.21.6.112/4/3/2
```

Return Value:

```
0 if OK
1 Error
```