

# CSpad2.3M configuration from CXI

Run Delay	58000	Quad	0	1	2	3	Quad Registers
Event Code	40	Shift Sel	4	4	4	4	4
Inact Run Mode	RunButDrop	Edge Sel	1	1	1	1	1
Activ Run Mode	RunAndSendTriggeredByTTL	Read Clk Set	2	2	2	2	
Internal Trigger Delay	0	Read Clk Hold	1	1	1	1	
Test Data Indx	4	Data Mode	0	0	0	0	
ADC Threshold quad 0	12000	PRst Sel	0	0	0	0	
Pixel Count Threshold quad 0	1000	Acq Delay	350	350	350	350	
ADC Threshold quad 1	12000	Int Time	5000	5000	5000	5000	
Pixel Count Threshold quad 1	1000	Dig Delay	1000	1000	1000	1000	
ADC Threshold quad 2	12000	Amp Idle	e0e	e0e	e0e	e0e	
Pixel Count Threshold quad 2	1000	Inj Total	0	0	0	0	
ADC Threshold quad 3	12000	Row/Col Shift	3	3	3	3	
Pixel Count Threshold quad 3	1000	Amp Reset	0	0	0	0	
Bad ASIC Mask (hex)	0	Dig Count (hex)	3fff	3fff	3fff	3fff	
Sector Mask (hex)	ffffffff	Dig Period	21	21	21	21	
Digital Pots Fields							
Vref	175	175	175	175			
Vin	250	250	250	250			
RampCurrR1	4	4	4	4			
RampCurrR2	40	40	40	40			
RampCurrRef	0	0	0	0			
RampVolRef	122	122	122	122			
CompBias1	255	255	255	255			
CompBias2	255	255	255	255			
lss2	176	176	176	176			
lss5	176	176	176	176			
Analog Prst	16	16	16	16			

GainMap

Import Text File
Export Text File

Set ASIC
Clear ASIC

Set ASIC
Clear ASIC

Save
Cancel