

Flange Boards

Documentation:

	Signal flange board	LV Power flange board	HV bias flange board
Board schematics	board page	board page	board page
Board layout	conceptual layout	board page	board page

Punch lists

LV Power flange board v1

- Make ANAP and ANAP_GND as beefy as possible and run on top of each other
- Make DIG and DIG_GND as beefy as possible
- Add plated holes between each row of pads including at edges

HV bias board v1

- Add plated holes at corners of board for strain relief system access.