

Run Info

CAL Calibrations - electrons

700000691 is the first run of position scan cal-calibration runs. The position scans:

ydir:

700000703-712 (tower 2),
700000713-724 (tower 3),

x-dir:

700000725-737 (tower 2),
700000738-790 (tower 3)
(JC)

(Dave Smith comments:-)It was the night of 27 to 28 July. CAL HI trigger never fired for the whole night, because the thresholds, which were supposed to be set to 1000 MeV (about 20 FHE DAC counts) were all set to 127 counts. This is being fixed right now (afternoon of 28th).

To see the FHE DAC settings: from the eLog, for a given run, click on the "config" report link, and way down in the .pdf file there are tables of CAL GCFE settings.

Baseline configuration - electrons

These runs were taken with baseline configuration. Time settings for those may not be optimal, please verify that before you do your analysis. We need to verify if the configurations agree with the [description](#) . CAL_HI may not be enabled for these runs

700000695 to 700000700
BT1,BT2,BT3,BT13,BT15

ACD timed in with CAL and TKR - electrons

These runs were used to time in the ACD. CAL_HI now is correctly enabled from run 700000820.

700000821 : 90 degrees - delay is 8 ticks for external trigger

700000822 : 90 degrees - delay is 4 ticks for external trigger

ACD timed in with CAL and TKR - pions

These runs were used to time in the ACD.

700000821 and 700000822

Run 700000825

Many packet errors (597/164930) at a rate of an average of 500 Hz and instant rate of 3kHz. Run crashed

- hitmap width 15
- hitmap delay 6
- hitmap_deadtime 0
- hold_delay 34
- veto_width 5
- veto_delay 0

Run 700000826

- hitmap width 15
- hitmap delay 6
- hitmap_deadtime 0
- hold_delay 34
- veto_width 5
- **veto_delay 16**

Packet error events

These runs need to be studied to verify events before and after errors

700000611, 700000825

700000829 to 700000829

ACD Calibrations with ACD timed in

Run 700000829 BT12 ACD pedestal run Tile 4 far from the table - **this pedestal reference run**
Run 700000830 BT12 ACD run Tile 4 according to calibration setting

Run 700000831 BT12 ACD run Tile 3 according to calibration setting has 10% packet errors

Run 700000832 BT12 ACD run Tile 2 according to calibration setting has 30% packet errors

Run 700000833 BT12 ACD run Tile 1 according to calibration setting has 30% packet errors

Run 700000834 BT12 ACD run Tile 0 according to calibration setting has 30% packet errors

High Voltage Set to 760 - too high pulse heights from tiles 0 and 1

Created BT12 which is same as BT1 but with Zero suppression off for Alex to see pedestals. See configuration for details.

These are calibration runs for ACD tiles

Run 700000837 BT12 ACD run Tile 4 according to calibration setting

Run 700000838 BT12 ACD run Tile 3 according to calibration setting has 10% packet errors

Run 700000839 BT12 ACD run Tile 2 according to calibration setting has 30% packet errors

Run 700000840 BT12 ACD run Tile 1 according to calibration setting has 30% packet errors

Run 700000841 BT12 ACD run Tile 0 according to calibration setting has 30% packet errors