

Hardware

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Experimental HW database dump (see The COB board database in Oracle)

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Shelves

ID	Slot 1	Slot 2	Slot 3	Slot 4	Slot 5	Date	Notes
Shasta	COB 2-1	X			COB 2-2	120606	
Freel	COB 2-3	X			COB 3-2	120607	
Olancha		X			COB 3-4	120607	
Keynot		X			COB 3-1	120607	
Darwin		X				120607	At BNL or Penn
Ritter		X			COB 3-5	120607	At BNL or Penn
Whitney							14 slots

COBs

V4 JTAG switch settings

Value	Device
0000	Base
0001	DPM-0
0010	DPM-1
0011	DPM-2
0100	DPM-3
0101	DTM

Inventory

ID	Site 0	Site 1	Site 2	Site 3	Site 4	Date	Notes
2-1						120605	Back from JLAB 120525 – switch broken 120605
2-2	DTM 2-7		DPM 2-2		DPM 2-7	120605	Back from JLAB 120525, green health LED not lighting (120605); Fixed (120606)
2-3	DTM 2-8		DPM 4			120605	
3-1	DTM 2-6		DPM 2-6			120503	Blue LED doesn't light (120605); Fixed (120606)
3-2	DTM 2-3		DPM 2-1			120607	
3-4	DTM 2-2		DPM 2-12			120605	
3-5	DTM 2-4		DPM 2-5			120605	

DTM405s

[illegible]

2-1		08:00:56:00:41:38	868	0.8.0	prod 2.2, 8??	rescue 2.2, 8xx	2012-05-02	With Ryan for Heavy Photon
2-2	000000445F63	08:00:56:00:41:32	868	0.8.0	prod 2.2, 927M	rescue 2.2, 927M	2012-05-03	OK
2-3	00000044895C	08:00:56:00:41:33	868		prod 2.2, 1088:1104	rescue 2.2, 1088:1104	120607	Filed down power connectors: don't screw down front side
2-4	000000445FC5	08:00:56:00:41:34	868		prod 2.2, 927M	rescue 2.2, 927M	2012-05-03	OK
2-5			868	0.8.0	prod 2.2, 927M	rescue 2.2, 927M	2012-05-02	EEPROM Read errors; Disrupts JTAG to the DPMs
2-6	000000445D84	08:00:56:00:41:36	868		prod 2.2, 1088:1104	rescue 2.2, 1088:1104		
2-7	00000044555C	08:00:56:00:43:06	868					
2-8	000000445F63	08:00:56:00:41:38	868		prod 2.2, 1088:1104	rescue 2.2, 1088:1104	2012-05-31	

DPM 405s

ID	Ser. No.	MAC	Firmware	EEPROM	System 0	System 1	Date	Notes
2-1	000000197519	08:00:56:00:42:fc	917		prod 2.2, 1088:1104	rescue 2.2, 1088:1104	120531	OK
2-2	00000019c057	08:00:56:00:42:f7	917		prod 2.2, 1088:1104	rescue 2.2, 1088:1104	120531	OK
2-3	000000197DAD	08:00:56:00:42:f9	917	0.8.0			120503	Fails: peb 0 exception 0x8baec1 edw 0x5f transferred 0; EEPROM Read errors. XMD gives: ERROR: Unable to STOP PowerPC Processor Check: (1) If the FPGA is Configured Correctly (or) (2) If Processor Reset and Clock Ports are Connected Correctly C103 scraped off.
2-4	00000019B108	08:00:56:00:42:XX	917		prod 2.2, 927M	rescue 2.2, 927M	120503	Programming f/w took two attempts; XMD 'dow'ing devel and 'con'ing causes crash to 0x700. XMD gives: ERROR: Unable to STOP PowerPC Processor Check: (1) If the FPGA is Configured Correctly (or) (2) If Processor Reset and Clock Ports are Connected Correctly C76 and R22 scraped off.
2-5	00000019A827	08:00:56:00:42:f5	917	0.8.0	prod 2.2, 927M	rescue 2.2, 927M	120503	OK
2-6	00000019C145	08:00:56:00:42:fa	917		prod 2.2, 1088:1104	rescue 2.2, 1088:1104	120531	OK
2-7	00000019D013	08:00:56:00:42:f8	917		prod 2.2, 1088:1104	rescue 2.2, 1088:1104	120531	OK
2-8	000000196E67							Bad. XMD gives: ERROR: Unable to STOP PowerPC Processor Check: (1) If the FPGA is Configured Correctly (or) (2) If Processor Reset and Clock Ports are Connected Correctly
2-9		08:00:56:00:42:f4	917		prod 2.2, 927M	rescue 2.2, 927M	120503	OK
2-10	00000019DC05	08:00:56:00:42:fb	917		prod 2.2, 927M	rescue 2.2, 927M	120503	OK
2-11	000000197D2F	08:00:56:00:42:XX					120503	Mark says board is bad. XMD gives: ERROR: Unable to STOP PowerPC Processor Check: (1) If the FPGA is Configured Correctly (or) (2) If Processor Reset and Clock Ports are Connected Correctly

2-12	000000198 C40	08:00:56:00:42:f6	917		prod 2.2, 927M	rescue 2.2, 927M	120503	OK
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DPM 440s

ID	Ser. no.	MAC	Firmware	MemTest	EEPROM	System 0	System 1	Date	Notes
02	000000457EXX	08:00:56:00:43:XX	DpmTest_DB0000XX						
03	000000457EXX	08:00:56:00:43:XX	DpmTest_DB0000XX						
04	000000457E18	08:00:56:00:43:01	DpmTest_DB000018	Passed				120529	@ Bruce
06	000000459BC C	08:00:56:00:43:0e	DpmTest_DB000018	Passed				120529	
07	00000045403A	08:00:56:00:43:00	DpmTest_DB000018	Passed				120529	
08	000000457EXX	08:00:56:00:43:XX	DpmTest_DB0000XX						
09	0000004538B3	08:00:56:00:43:04	DpmTest_DB000018	Won't load				120530	impact works, xmd doesn't
12	000000455233	08:00:56:00:43:0d	DpmTest_DB000018	Passed				120530	
13	000000457EXX	08:00:56:00:43:XX	DpmTest_DB0000XX						
14	000000456FB5	08:00:56:00:43:03	DpmTest_DB000018	Passed				120530	

DTM Zynqs

ID	Ser. no.	MAC	MemTest	Network	Date	Notes
01		08:00:56:00:43:1C	Not Tested	Passed	130918	
02		08:00:56:00:43:17	Not Tested	Passed	130918	
03		08:00:56:00:43:14	Not Tested	Passed	130918	

DPM Zynqs

ID	Ser. no.	MAC	MemTest	Network	Date	Notes
01-0		08:00:56:00:43:15	Failed	Not Tested	130918	
01-2		08:00:56:00:43:16	Failed	Not Tested	130918	
02-0		08:00:56:00:43:18	Passed	Passed	130918	
02-2		08:00:56:00:43:19	Won't load	Not Tested	130918	
03-0		08:00:56:00:43:1A	Failed	Not Tested	130918	
03-2		08:00:56:00:43:1B	Passed	Failed	130918	

RTMs

Type	Ser. no.	EEPROM	Date	Notes
Old	2-1		1205xx	
Old	2-2		1205xx	
STP	00000063488A		120607	
STP	00000044C85 C		120607	
STP	0000006362BE		120607	
STP	00000044BB35		120607	
STP	00000044C289		120607	
STP	000000450F2F		120607	
STP	00000044BE81		120607	
SNAP-12	000000474DE0		120607	

SNAP-12	000000632FD9		120607	
SNAP-12	000000			
SNAP-12	000000			
SNAP-12	000000			
SNAP-12	000000			
SNAP-12	000000			

Experimental HW database dump (see [The COB board database in Oracle](#))

COB modules

Supertype	Type	Serial no.	MAC Addr	Good?	Retired?	Comment
rtm	snap-12	000000474de0		✓		2012-06-07 14:00:00 EEPROM written. SN#04.
rtm	snap-12	000000632e76		✓		2012-06-14 17:02:12 LEDs OK. 2012-06-14 17:02:12 EEPROM written. SN#05.
rtm	snap-12	000000632fd9		✓		2012-06-07 14:00:00 EEPROM written. SN#03.
rtm	snap-12	000000633007		✓		2012-06-14 17:19:57 LEDs OK. 2012-06-14 17:19:57 EEPROM written. SN#08.
rtm	snap-12	0000006362e9		✓		2012-06-07 14:00:00 EEPROM written. SN#02.
rtm	snap-12	0000006362f4		✓		2012-08-29 18:39:06 EEPROM written. Backwards I2C chip reinstalled. 2012-08-29 18:39:06 LEDs OK.
rtm	snap-12	000000637191		✓		2012-06-14 17:07:38 LEDs OK. 2012-06-14 17:07:38 EEPROM written. SN#06.
rtm	sfp	00000044bb35		✓		2012-06-07 14:00:00 EEPROM written. SN#06.
rtm	sfp	00000044be81		✓		2012-06-07 14:00:00 EEPROM written. SN#08.
rtm	sfp	00000044c289		✓		2012-06-07 14:00:00 EEPROM written. SN#05.
rtm	sfp	00000044c85c		✓		2012-06-07 14:00:00 EEPROM written. SN#07.
rtm	sfp	000000450f2f		✓		2012-06-07 14:00:00 EEPROM written. SN#03.
rtm	sfp	00000063488a		✓		2012-05-02 00:31:47 EEPROM written. SN#02.
rtm	sfp	0000006362be		✓		2012-06-07 14:00:00 EEPROM written. SN#04.
cmb1	dpm405	000000196e67	080056004308	✗		2012-05-02 00:31:47 EEPROM written. REV-2-08. 2012-06-05 22:14:00 XMD unable to stop processor.
cmb1	dpm405	000000197519	0800560042fc	✓		2012-05-02 00:31:47 EEPROM written. 2-1. 2012-05-31 00:00:00 Firmware 917. 2012-05-31 00:00:00 Sys images 0: prod 2.2 SVN 1088:1104, 1: rescue ditto
cmb1	dpm405	000000197d2f	080056004307	✗		2012-05-02 00:31:47 XMD unable to stop processor. 2012-05-02 00:31:47 EEPROM written. REV-2-11.
cmb1	dpm405	000000197dad	0800560042f9	✗		2012-05-31 00:00:00 Firmware 917. EEPRPM 0.8.0. 2012-06-02 00:31:47 EEPROM written. REV-2-03. 2012-06-05 22:14:00 PEB 0 exception 0x8baec1 edw 0x5f. 2012-06-05 22:14:00 C103 has been scraped off. 2012-06-05 22:14:00 XMD unable to stop processor. 2012-06-05 22:14:00 EEPROM read errors.
cmb1	dpm405	000000198c40	0800560042f6	✗		2012-05-02 00:31:47 EEPROM written. 2-12. 2012-05-03 00:00:00 Firmware 917. 2012-05-03 00:00:00 Sys images 0: prod 2.2 SVN 927M, 1: rescue ditto. 2012-12-08 00:54:26 Good 1 -> 0. 2012-12-08 00:54:26 memTest fails within first MB
cmb1	dpm405	00000019a827	0800560042f5	✓		2012-05-02 00:31:47 EEPROM written. REV-2-05. 2012-05-03 00:00:00 Firmware 917. EEPROM 0.8.0. 2012-05-03 00:00:00 Sys images 0: prod 2.2 SVN 927M, 1: rescue ditto.
cmb1	dpm405	00000019b108	0800560042f2	✗		2012-05-02 00:31:47 EEPROM written. 2-04. 2012-05-03 00:00:00 Firmware 917. 2012-05-03 00:00:00 Sys images 0: prod 2.2 SVN 927M, 1: rescue ditto. 2012-05-03 00:31:47 Programming firmware took two attempts. 2012-05-03 00:31:47 XMD: "con" after "dow" caused crash at 0x700. 2012-05-03 22:14:00 XMD unable to stop processor. 2012-05-03 22:14:00 C76 and R22 have been scraped off.
cmb1	dpm405	00000019bbb3	0800560042f4	✓		2012-05-02 00:31:47 EEPROM written. 2-09. 2012-05-03 00:00:00 Firmware 917. 2012-05-03 00:00:00 Sys images 0: prod 2.2 SVN 927M, 1: rescue ditto.
cmb1	dpm405	00000019c145	0800560042fa	✓		2012-05-02 00:31:47 EEPROM written. 2-06. 2012-05-31 00:00:00 Firmware 917. 2012-05-31 00:00:00 Sys images 0: prod 2.2 SVN 1088:1104, 1: rescue ditto.
cmb1	dpm405	00000019cd57	0800560042f7	✓		2012-05-02 00:31:47 EEPROM written. REV-2-02. 2012-05-03 00:00:00 Sys images 0: prod 2.2 SVN 1088:1104, 1: rescue ditto 2012-05-03 00:00:00 Firmware 917.

cmb1	dpm405	00000019d013	0800560042f8	✓		2012-05-02 00:31:47 EEPROM written. 2-07. 2012-05-03 00:00:00 Firmware 917. 2012-05-03 00:00:00 Sys images: 0: prod 2.2 SVN 1088:1104, 1: rescue ditto.
cmb1	dpm405	00000019dc05	0800560042fb	✓		2012-05-02 00:31:47 EEPROM written. 2-10. 2012-05-03 00:00:00 Sys images 0: prod 2.2 SVN 927M, 1: rescue ditto. 2012-05-03 00:00:00 Firmware 917.
cmb1	dpm440	0000004538b3	080056004304	✗		2012-05-30 00:00:00 Firmware DpmTest_DB000018. 2012-06-05 22:14:00 EEPROM written. SN009. 2012-06-05 22:14:00 Impact works, xmd doesn't. Can't load MemTest. 2012-11-19 23:01:42 EEPROM modified. Change the first port from xaui to 1000basex_lane0
cmb1	dpm440	00000045403a	080056004300	✓		2012-05-29 00:00:00 Firmware DpmTest_DB000018. 2012-06-05 22:14:00 EEPROM written. SN*07. 2012-08-09 19:37:39 MGT voltage changed from 1.2V to 1.0V. 2012-08-09 19:37:44 SD rework done. 2012-08-09 19:42:03 Has Matt's DTM fixes.
cmb1	dpm440	000000454296	080056004309	✓		2012-08-08 23:17:29 EEPROM written. SN#2. From Ryan. 2012-08-08 23:23:18 MGT voltage changed from 1.2V to 1.0V. 2012-08-08 23:23:43 SD rework done. 2012-09-13 21:35:31 EEPROM modified. Configured eth port 0 as 1000basex_lane0.
cmb1	dpm440	000000454f4f	08005600430b	✓		2012-08-08 23:32:58 EEPROM written. SN#8. From Ryan. 2012-08-08 23:33:12 MGT voltage changed from 1.2V to 1.0V. 2012-08-08 23:33:17 SD rework done. 2012-09-13 21:45:19 EEPROM modified. Configured eth port 0 as 1000basex_lane0.
cmb1	dpm440	000000454f52	08005600430a	✓		2012-08-08 23:26:31 EEPROM written. SN#3. From Ryan. 2012-08-08 23:26:40 MGT voltage changed from 1.2V to 1.0V. 2012-08-08 23:26:56 SD rework done.
cmb1	dpm440	000000455233	08005600430d	✓		2012-05-30 00:00:00 Firmware DpmTest_DB000018. 2012-06-05 22:14:00 EEPROM written. SN012. 2012-09-12 18:03:52 Altered EEPROM setting for port 0 ethernet to 1000basex_lane0. 2012-09-12 18:06:11 Altered firmware yesterday to DpmTest_DB00002B.
cmb1	dpm440	000000456fb5	080056004303	✓		2012-05-30 00:00:00 Firmware DpmTest_DB000018. 2012-06-05 22:14:00 EEPROM written. SN014. 2012-09-13 21:48:10 EEPROM modified. Configured eth port 0 as 1000basex_lane0.
cmb1	dpm440	00000045788e	08005600430c	✓		2012-08-09 23:22:52 EEPROM written. SN013. From Ben Reese.
cmb1	dpm440	000000457e18	080056004301	✓		2012-05-29 00:00:00 Firmware DpmTest_DB000018. 2012-06-05 22:14:00 EEPROM written. SN#4.
cmb1	dpm440	000000459bcc	08005600430e	✓		2012-05-29 00:00:00 Firmware DpmTest_DB000018. 2012-06-05 22:14:00 EEPROM written. SN*06. 2012-08-09 19:35:35 MGT voltage changed from 1.2V to 1.0V. 2012-08-09 19:35:54 SD rework done. 2012-09-12 23:17:25 Altered EEPROM setting for port 0 ethernet to 1000basex_lane0.
cmb1	dtm405	000000000000		✗	☹	2012-05-06 00:00:00 Broken beyond repair. 2012-05-06 00:00:00 EEPROM written. R2-1.
cmb1	dtm405	0000000000001		✗		2012-05-02 00:00:00 Sys images 0: prod 2.2 SVN 927M, 1: rescue ditto. 2012-05-02 00:00:00 Firmware 868. EEPROM 0.8.0. 2012-05-06 00:00:00 EEPROM read errors. 2012-05-06 00:00:00 Disrupts JTAG to DPMs. 2012-05-06 00:00:00 EEPROM written. R2-5.
cmb1	dtm405	00000044555c	080056004138	✓		2012-05-03 00:00:00 Sys images 0: prod 2.2 SVN 927M, 1: rescue ditto. 2012-06-02 00:00:00 Firmware 868. EEPROM 0.8.0. 2012-06-05 22:14:00 From Jefferson Lab. 2012-06-05 22:14:00 EEPROM written. REV-2#8.
cmb1	dtm405	000000445d84	080056004136	✓		2012-06-05 22:14:00 Firmware 868. 2012-06-05 22:14:00 Sys images 0: prod 2.2 SVN 1088:1104, 1: rescue ditto. 2012-06-05 22:14:00 EEPROM written. REV2#6.
cmb1	dtm405	000000445f63	080056004306	✓		2012-06-05 22:14:00 EEPROM written. REV-2-7.
cmb1	dtm405	000000445fc5	080056004134	✓		2012-05-03 00:00:00 Sys images 0: prod 2.2 SVN 927M, 1: rescue ditto. 2012-05-03 00:00:00 Firmware 868. 2012-06-05 22:14:00 EEPROM written. REV2#4.
cmb1	dtm405	00000044810b	080056004132	✓		2012-05-03 00:00:00 Sys images 0: prod 2.2 SVN 927M, 1: rescue ditto. 2012-05-03 00:00:00 Firmware 868. EEPROM 0.8.0. 2012-06-05 22:14:00 EEPROM written. R2-2.
cmb1	dtm405	00000044895c	080056004133	✓		2012-06-05 22:14:00 Broken. 2012-06-05 22:14:00 Repaired, filed down power connectors. 2012-06-05 22:14:00 Don't screw down the front. 2012-06-05 22:14:00 EEPROM written. R2-3. 2012-06-07 00:00:00 Firmware 868. 2012-06-07 00:00:00 Sys images 0: prod 2.2 SVN 1088:1104, 1: rescue ditto.

General (non-board) log entries.

2012-06-13 18:00:00 Database schema v2.

2012-06-15 01:04:23 Implemented dump-as-wiki.

2012-06-15 21:22:00 Added firmware and sys-image info. from Confluence page.