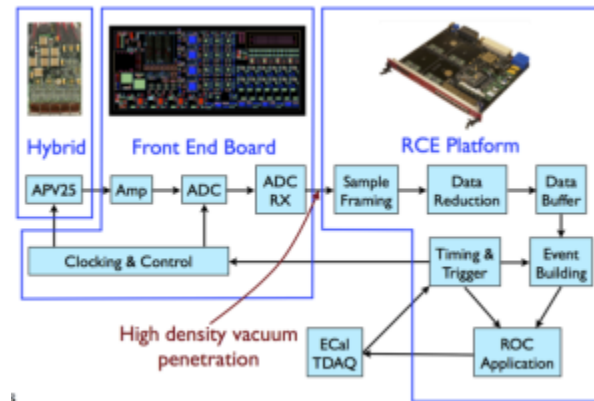# SVT DAQ

The HPS Tracker Readout Electronics

The SVT data acquisition (DAQ) system is a SLAC built system based on the Advanced Telecom Computing Architecture (ATCA). The design is meant to continuous readout the SVT silicon detector modules, store data in pipeline buffers and transfer data to the JLab DAQ system after a trigger signal is received.

Components:

- Advanced Telecommunications Computing Architecture (ATCA) System
- Hybrid Readout Board
- Front End Board
- Flange Boards
- APV25 Readout Chip
- Power and HV cabling

Links

JLab DAQ & Trigger

10/2/2014 SVT DAQ System Layout

Talks:

- Overview of the Test Run SVT DAQ (R. Herbst) - pdf