

EPICS Aug 2010 Day 2

EPICS Asyn Class Day 2

Schedule

0900-1230	Area Detector
	ASYN Port Driver
1230-1315	Lunch Break
1315-1700	Acromag Digital
	HMS
	Test Asyn Port Driver

Quicktime Videos of Lectures

- [Part 4](#) (runtime 01:00:36)
- [Part 5](#) (runtime 00:51:20)
- [Part 6](#) (runtime 01:02:50)

Lecture Agenda

- asynPortDriver - C++ class for simplifying writing drivers
- Example drivers: MCA, areaDetector

Lab Agenda

Lab TAs: Zen Szalata (ARD-TestFacilities-CTRLS),Kukhee Kim (SLAC-CTRLS)

- Communication with register-based devices using asynInt32, asynFloat64, etc. interfaces VME crate with ACROmag digital I/O IP modules
areaDetector simulation driver and Prosilica cameras

Lab Material

- (Python scripts that emulate hardware)
- [Hardware Manager Simulator \(HMS\) Script File](#)
- [server.txt](#)
- [UDP client python script \(apMgr.txt\)](#)

Attendees

1. Dayle Kotturi (SLAC-CTRLS)
2. Harvey Rarback (SSRL-CTRLS)
3. Shantha Condamoore (SLAC-CTRLS)
4. Arturo Alarcon (SLAC-CTRLS)
5. Alison Chaiken (ARD-TestFacilities-CTRLS)
6. Matt Boyes (SLAC-CTRLS)
7. Ravi Mirakhur (SLAC-PPA)
8. Richard Neill Dabney (SLAC-PPA)
9. John Wagner (SLAC-PPA)
10. Stephanie Allison (SSRL-CTRLS)
11. Kristi Luchini (SLAC-CTRLS)
12. Sandeep Babel (PCD)
13. Qiao Geng (SLAC-CTRLS)
14. Charlie Xu (SLAC-CTRLS)
15. Bruce Hill (SLAC-PPA)