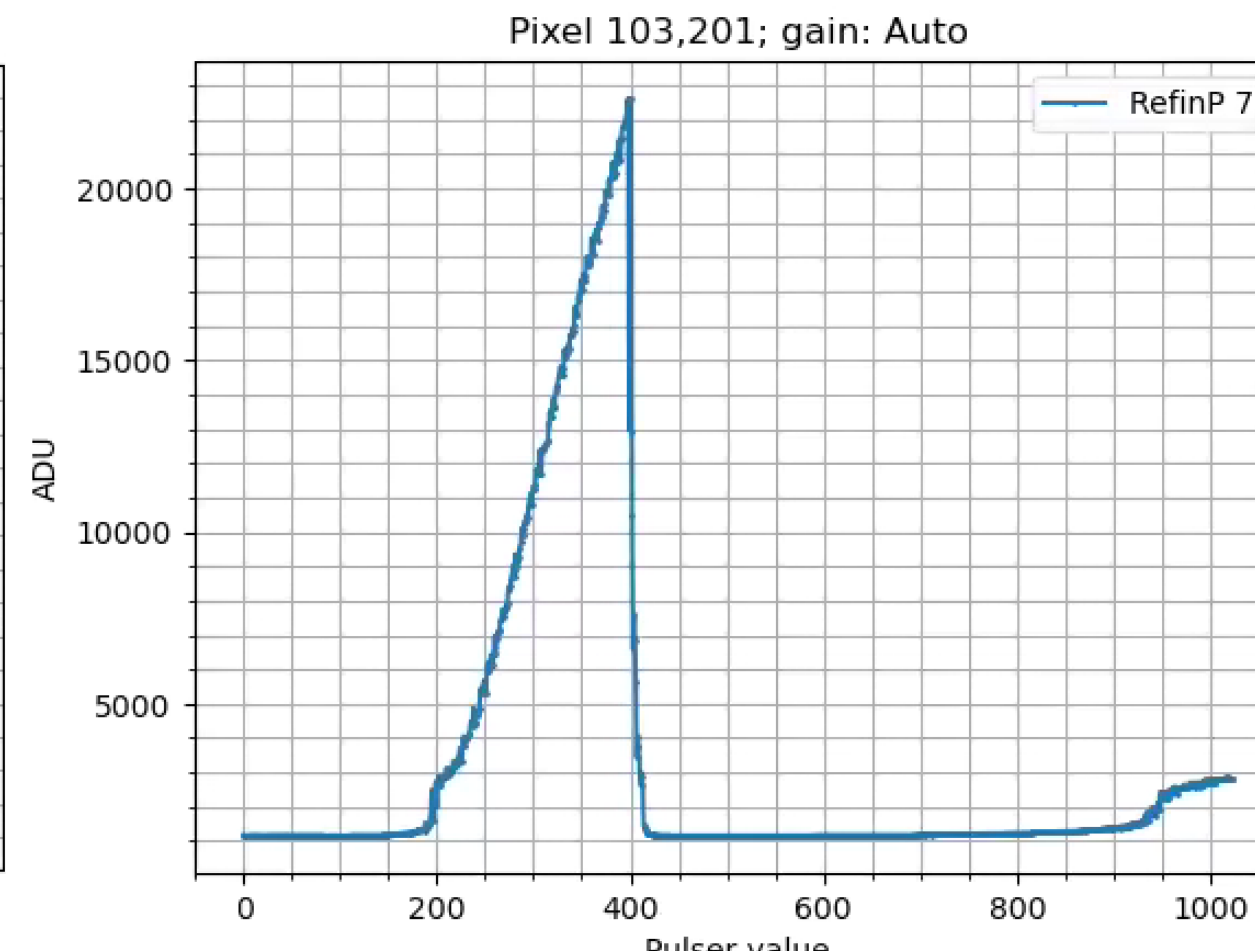
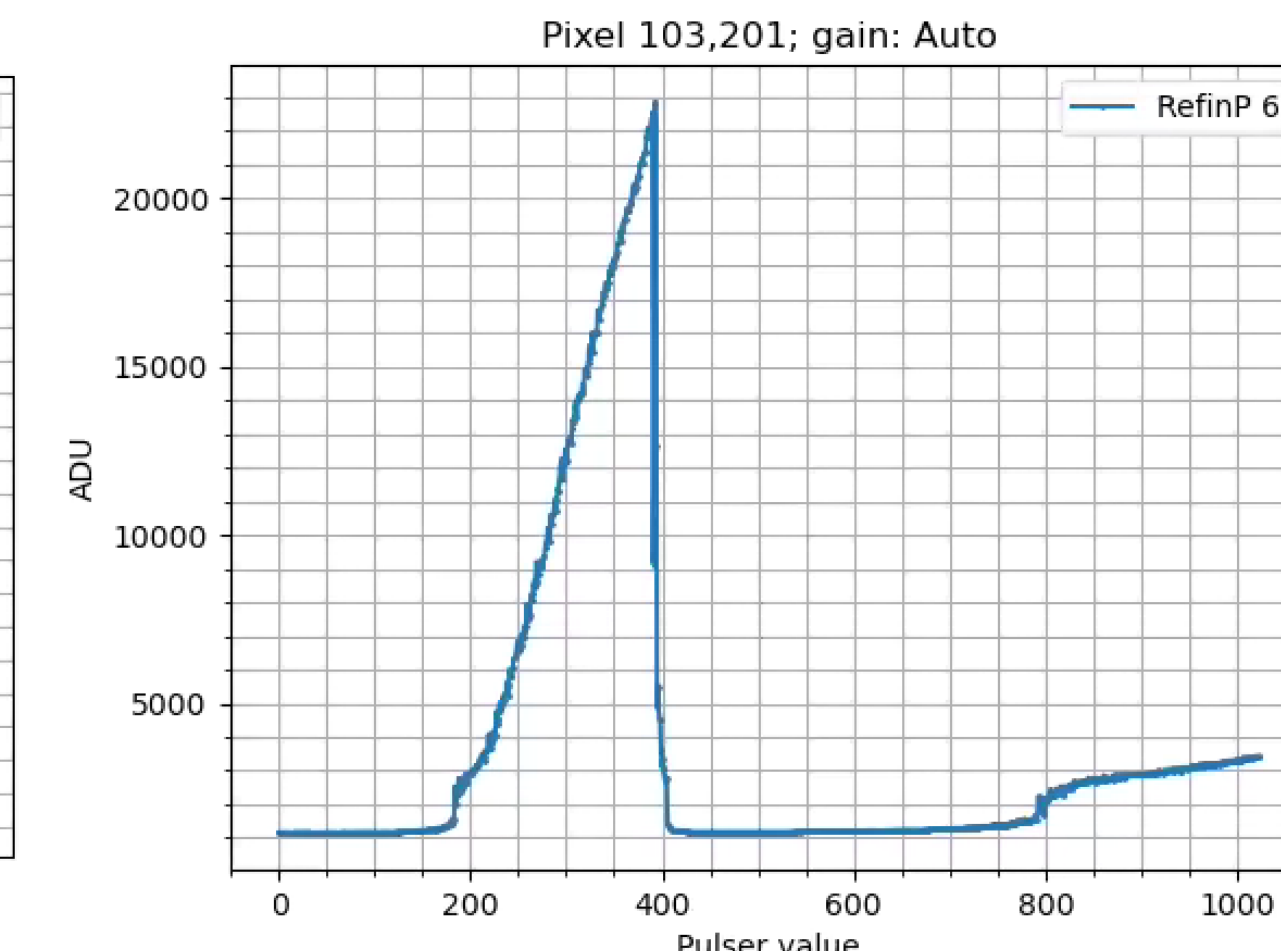
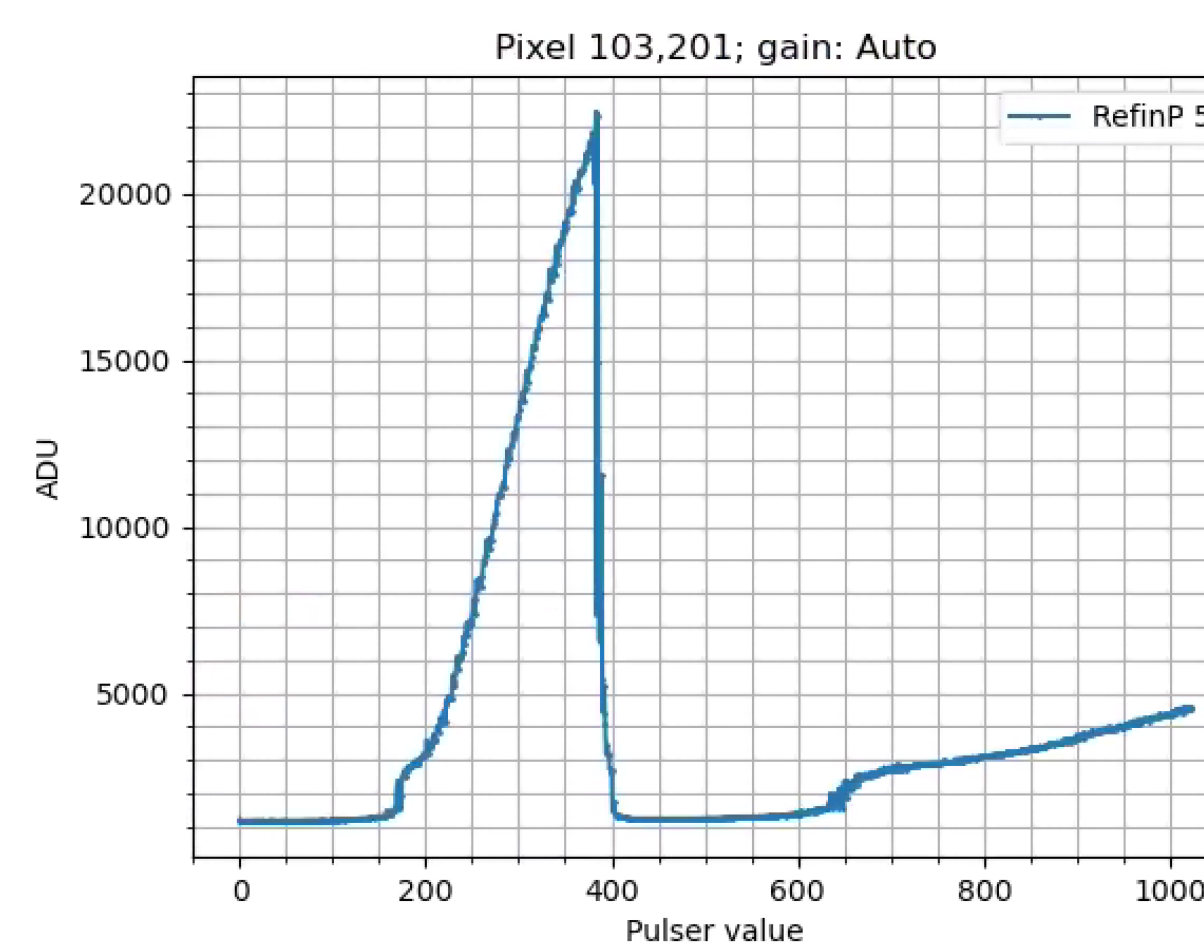
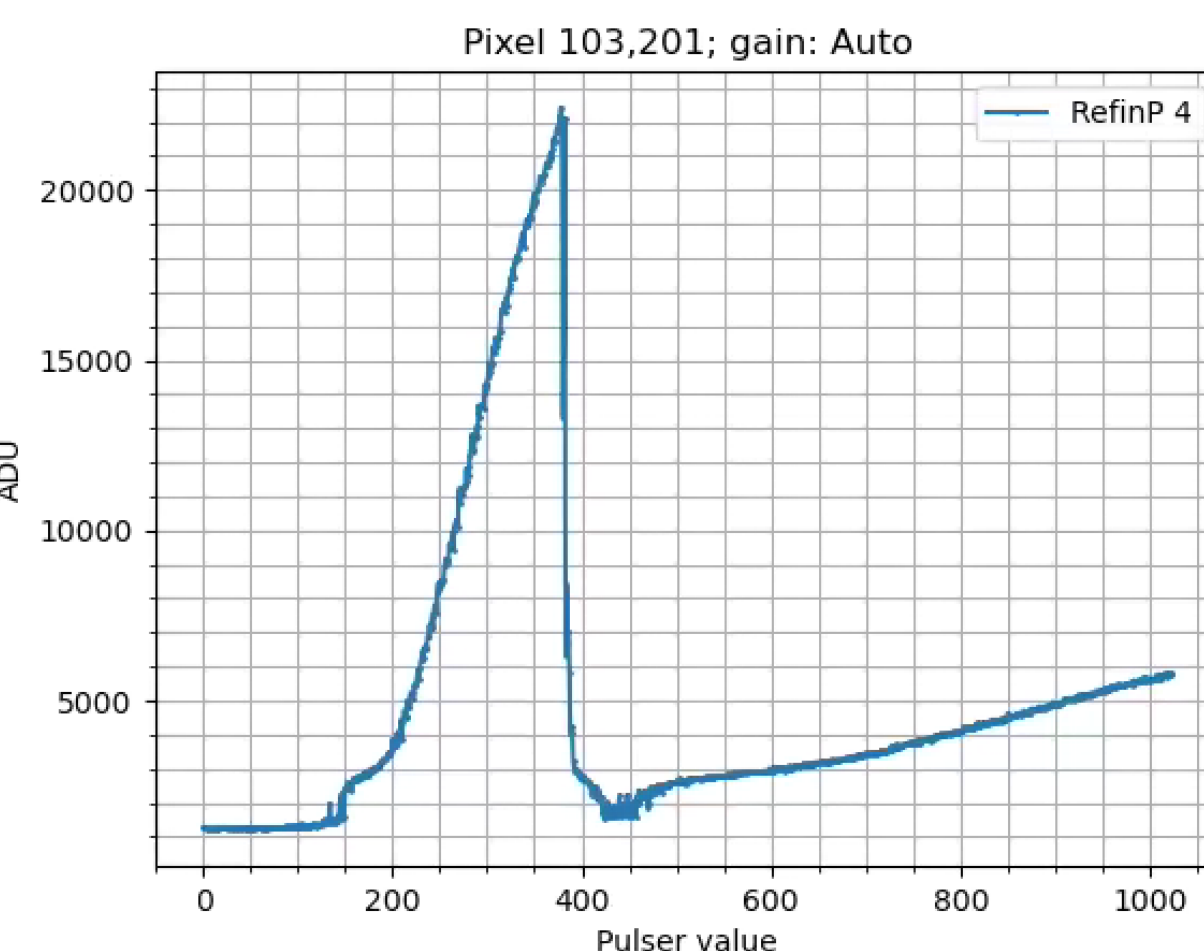
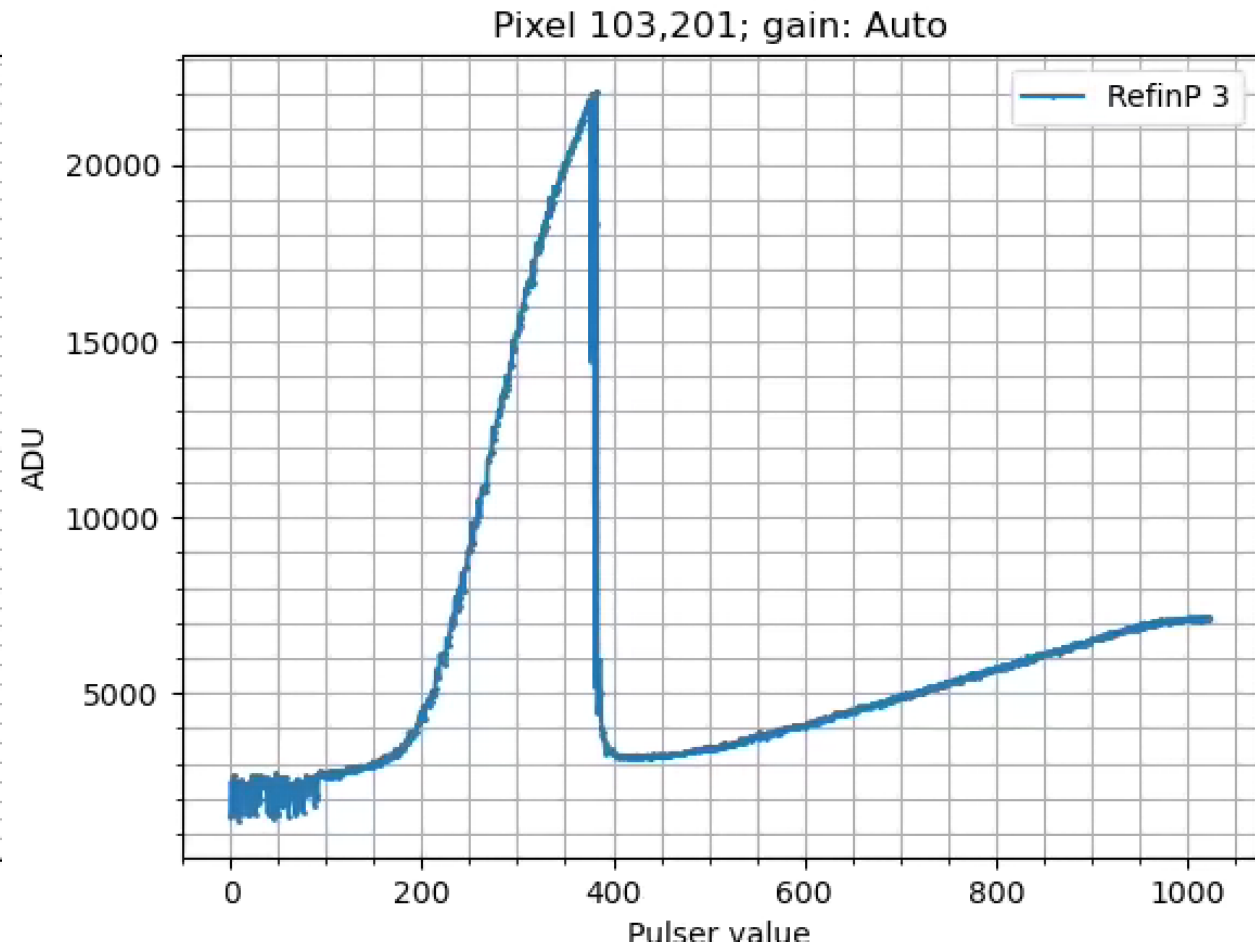
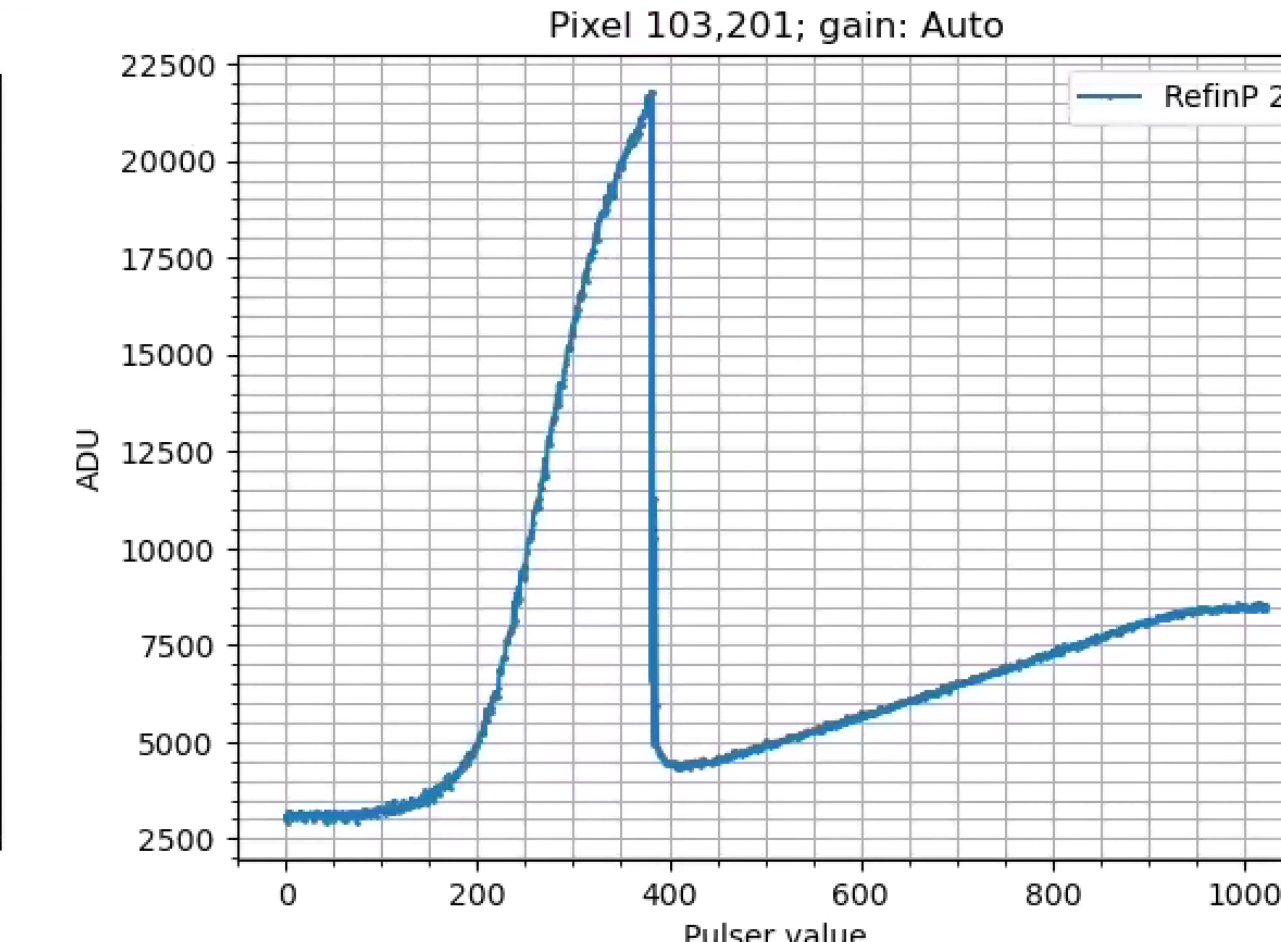
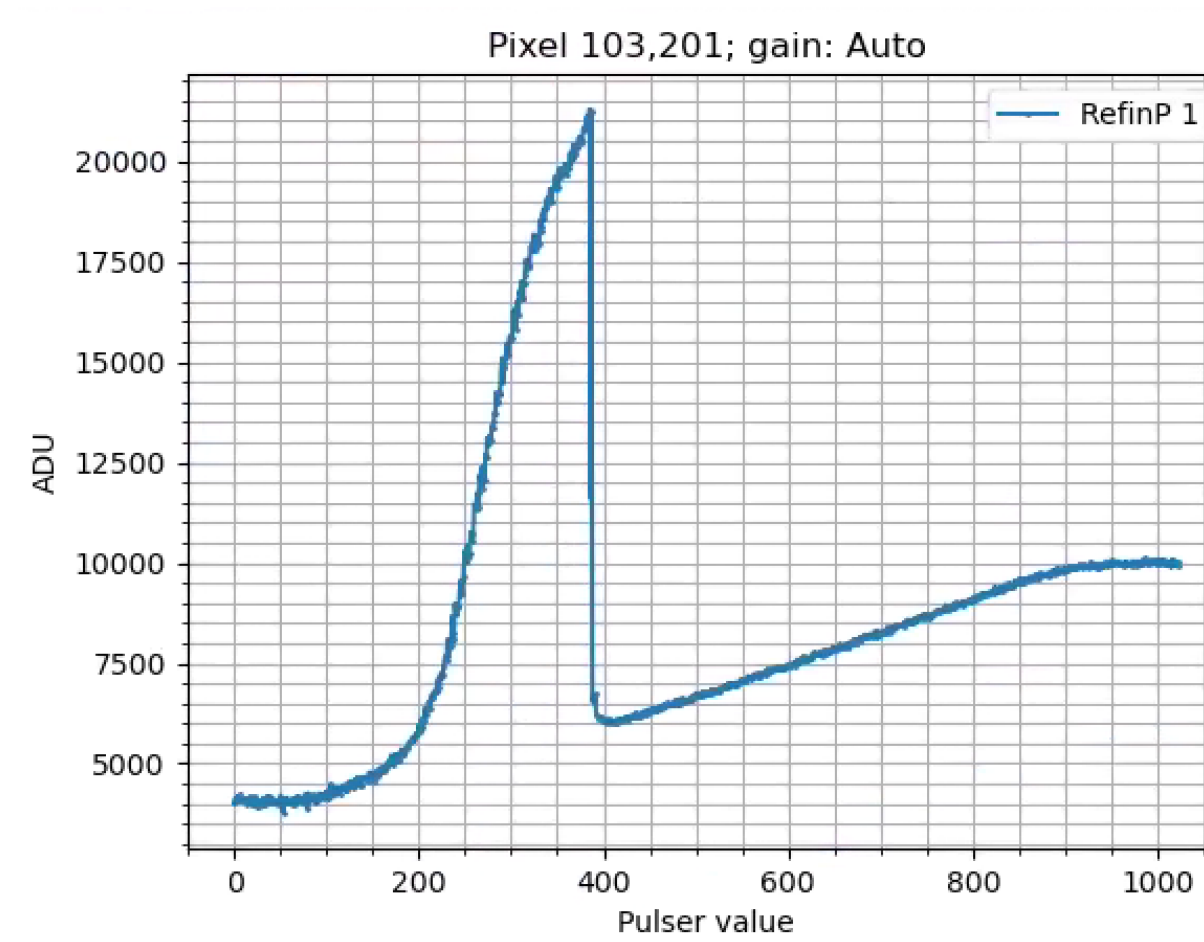
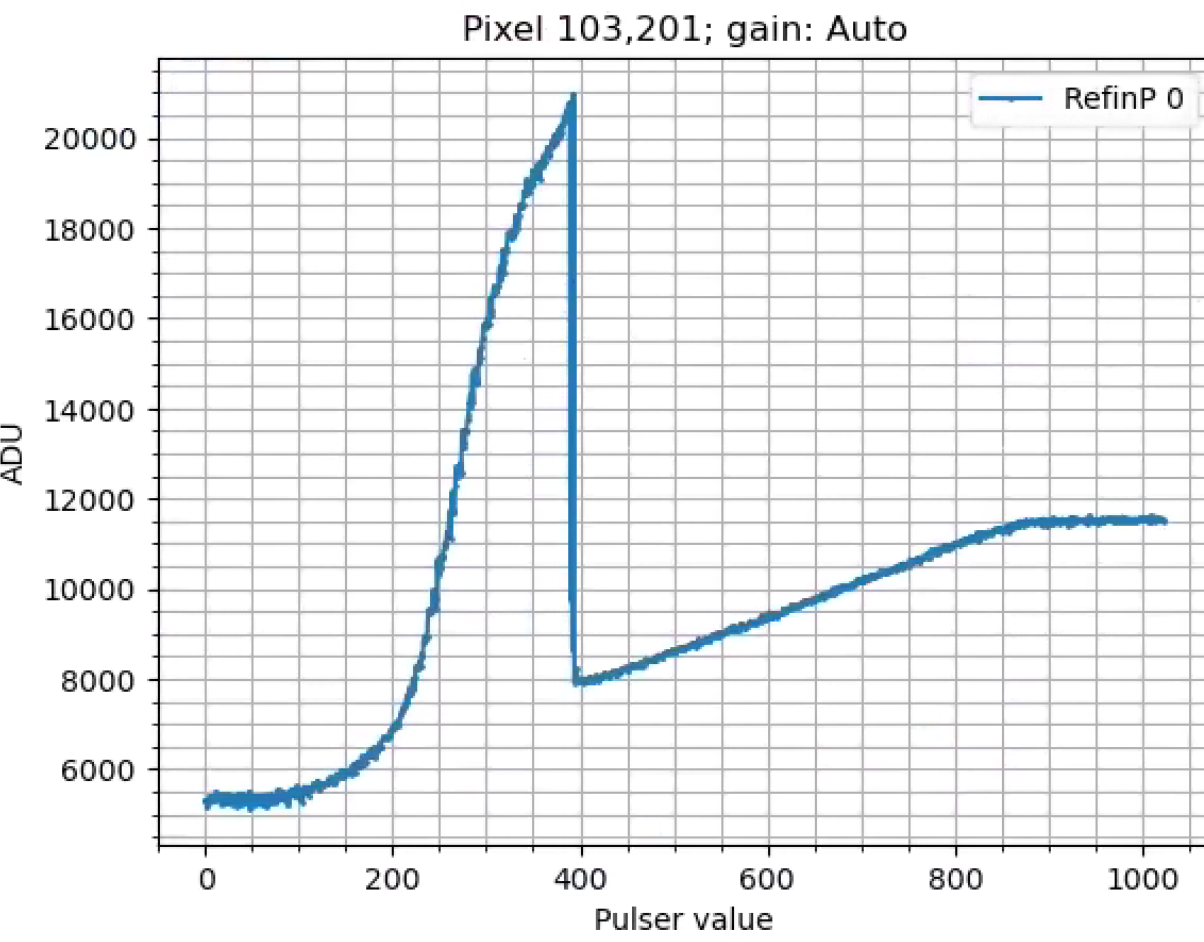
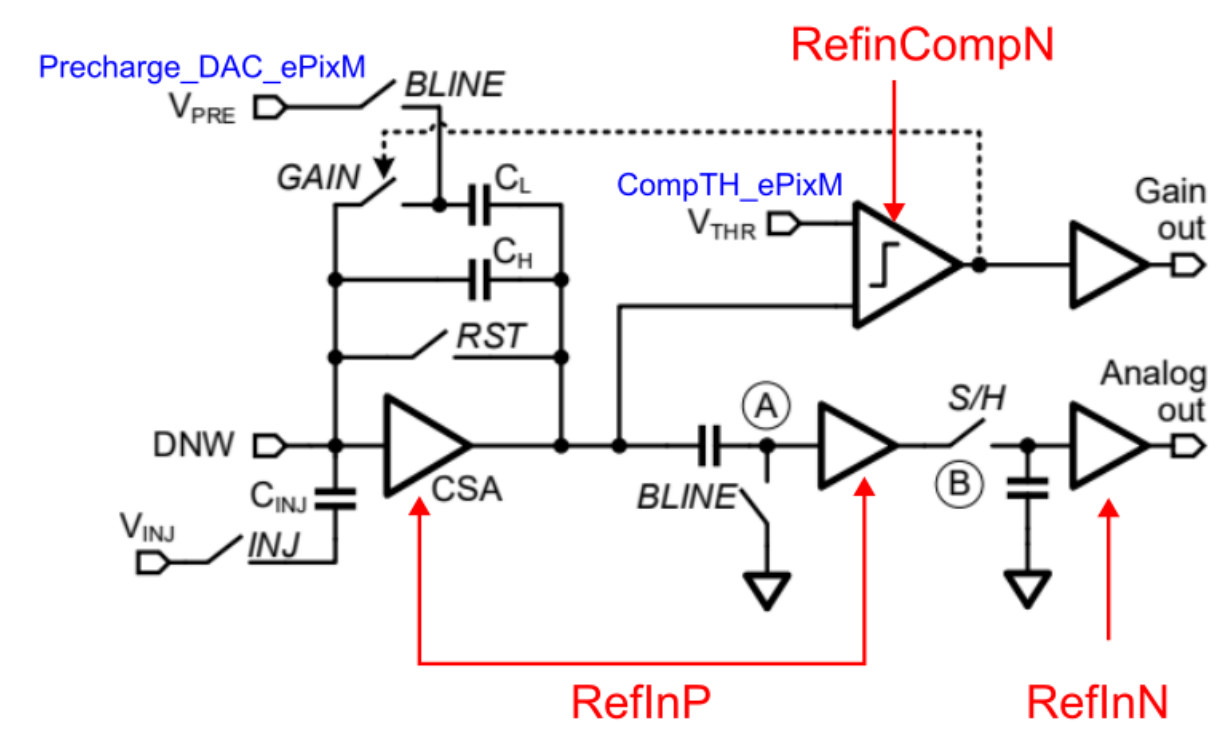


ePixM register optimization

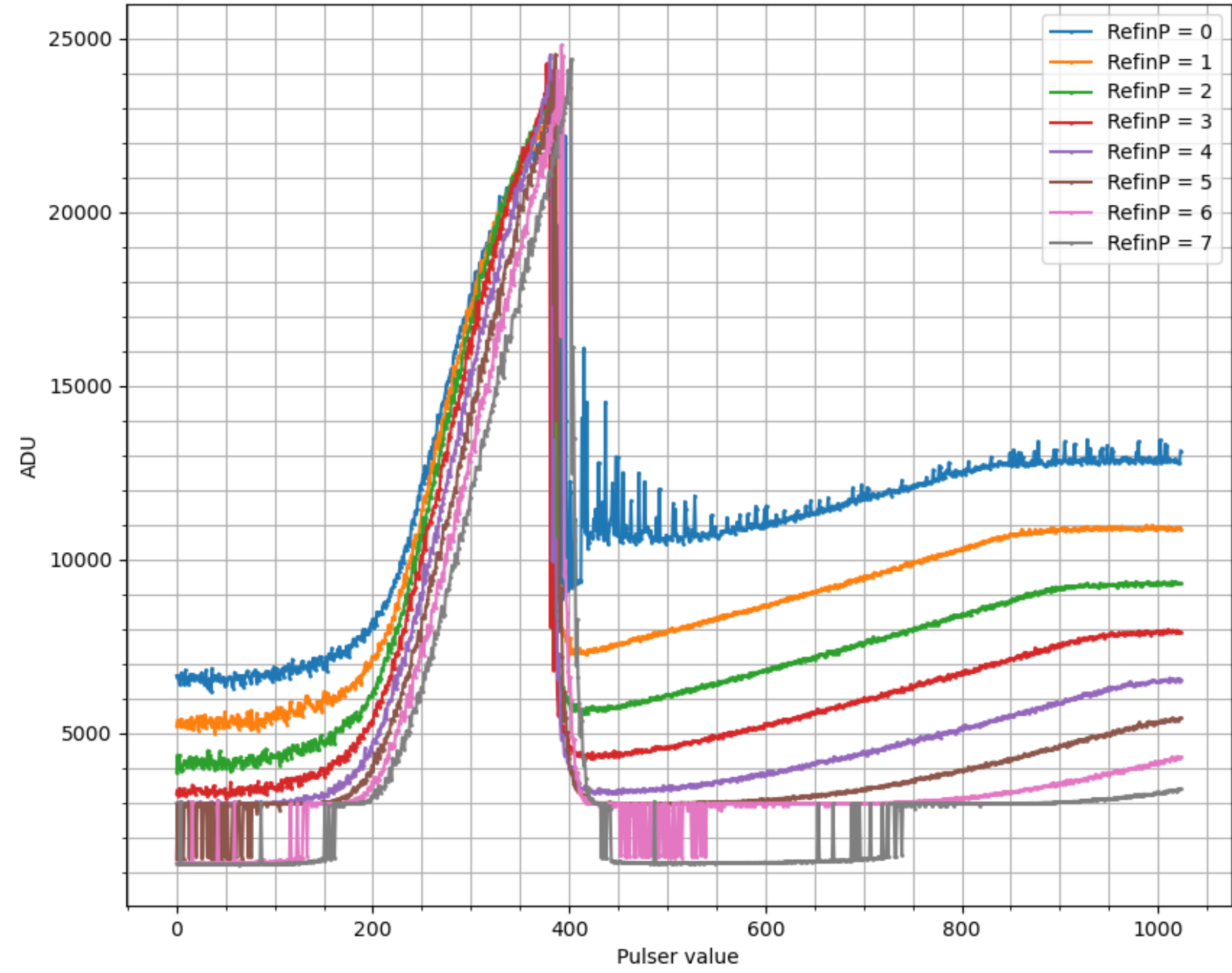
Part 1



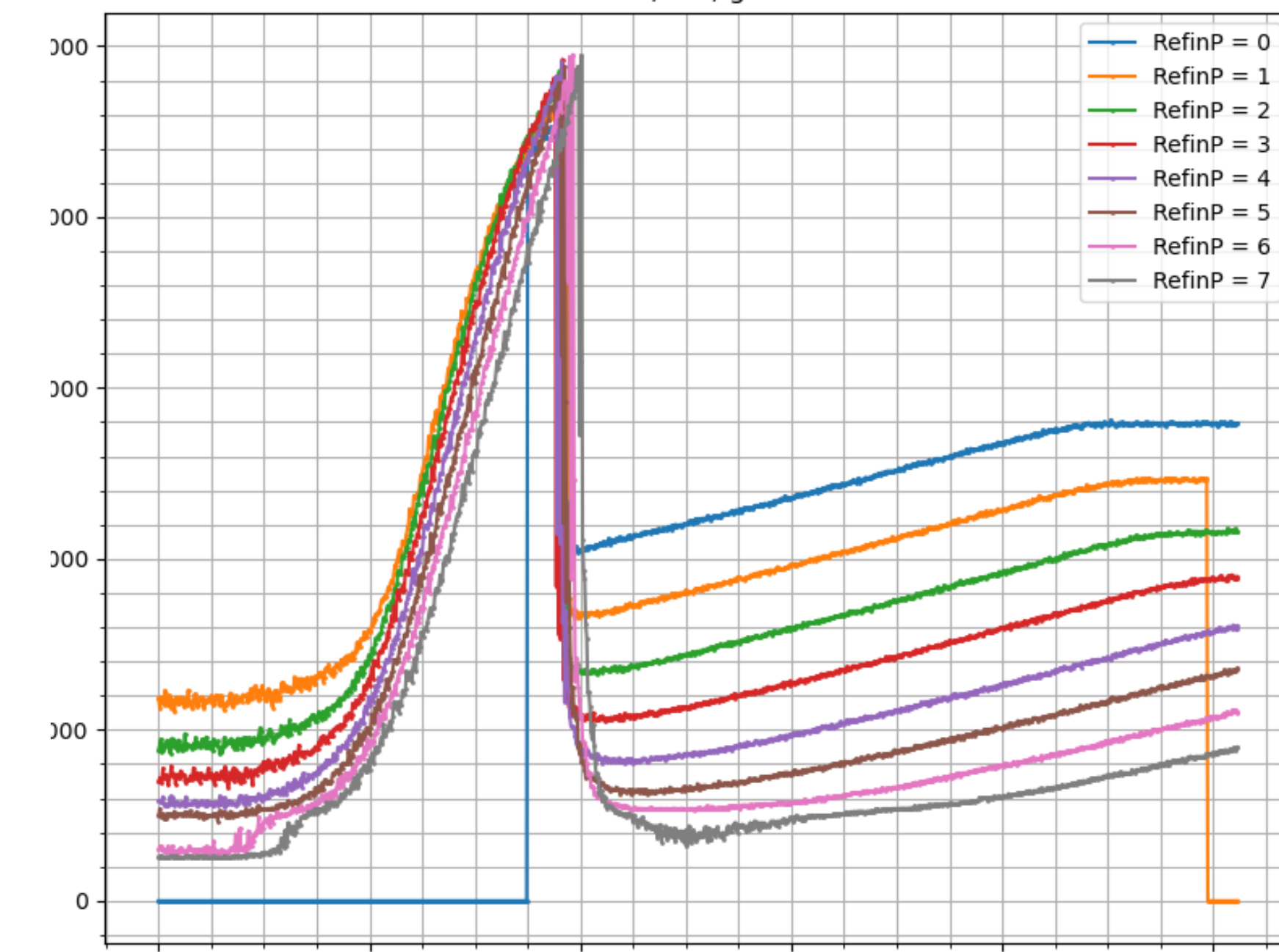
• RefinP optimization



Pixel 103,10; gain: Auto

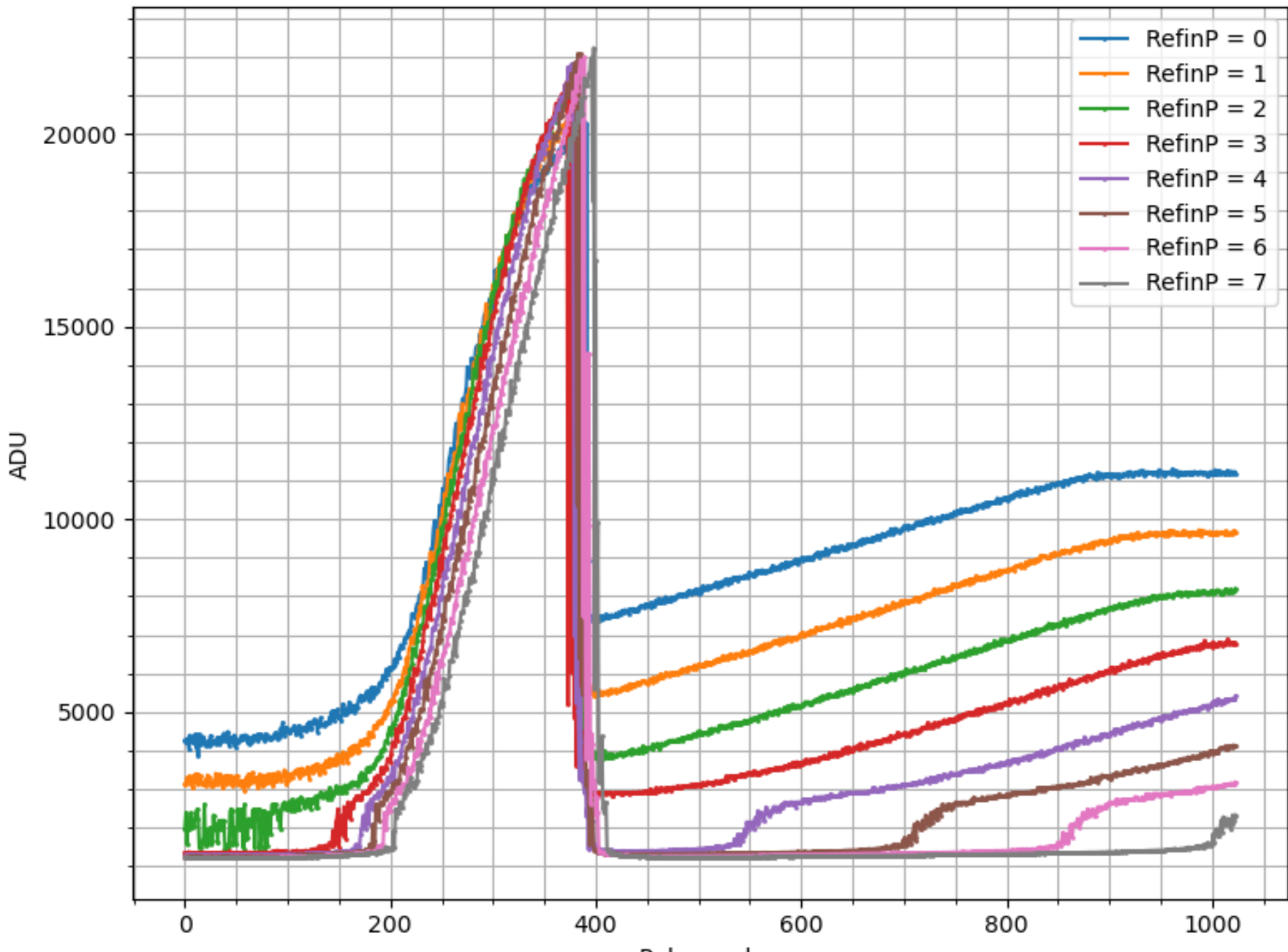


Pixel 103,110; gain: Auto

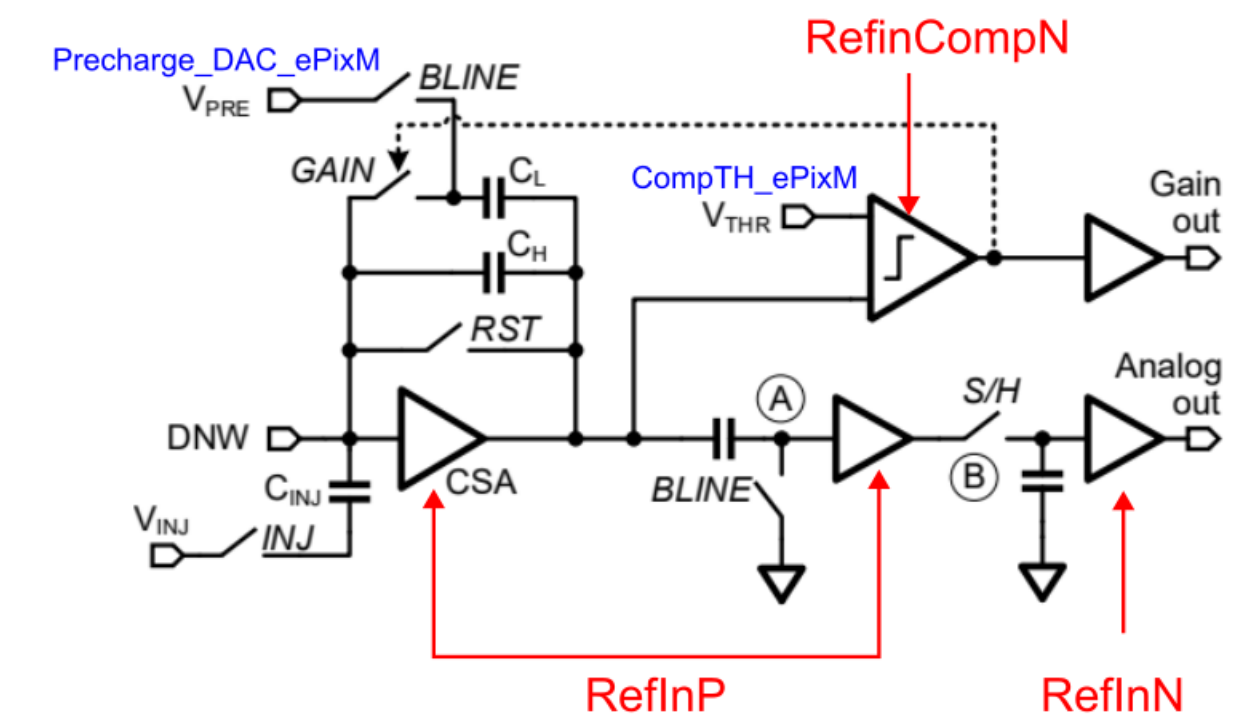
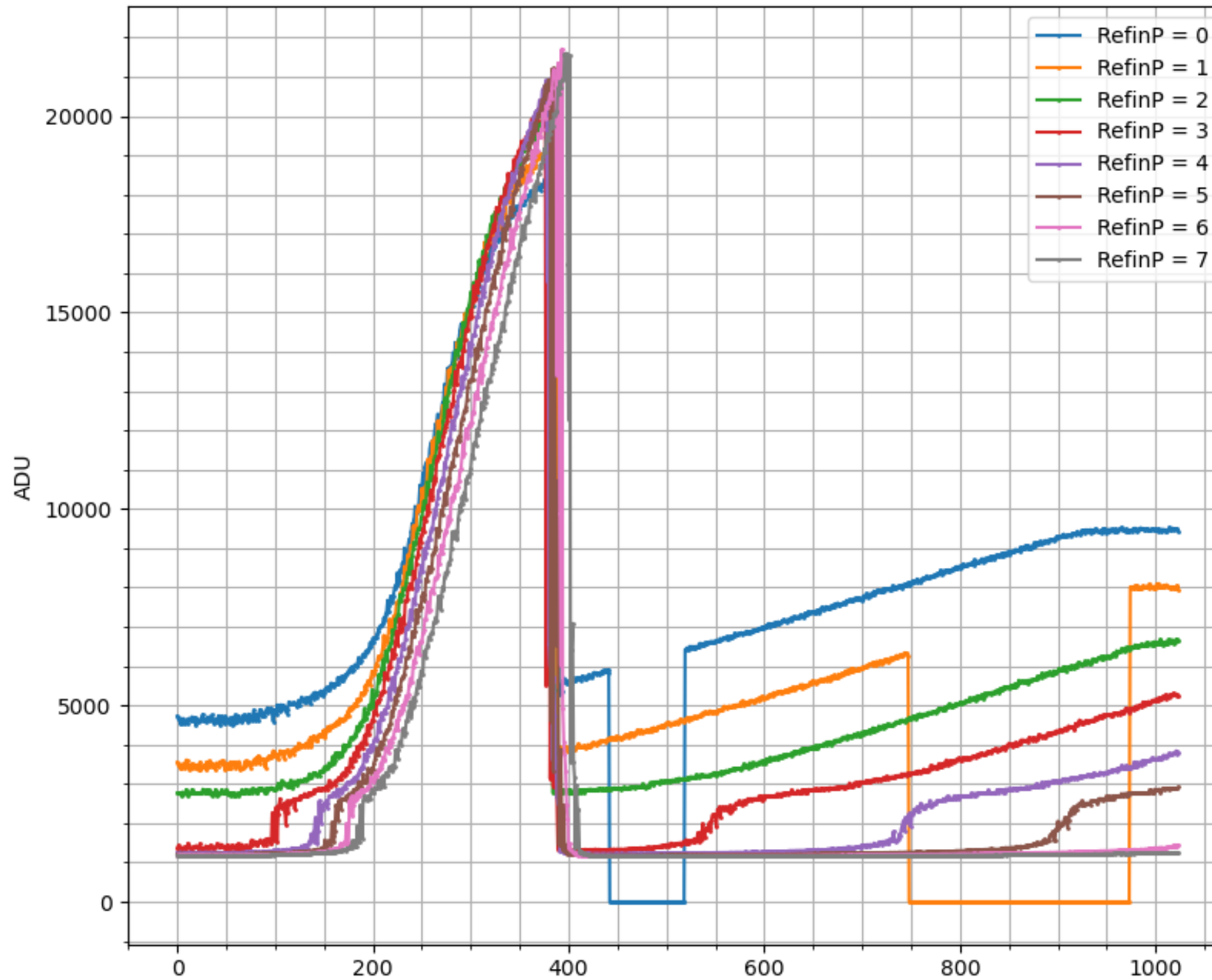


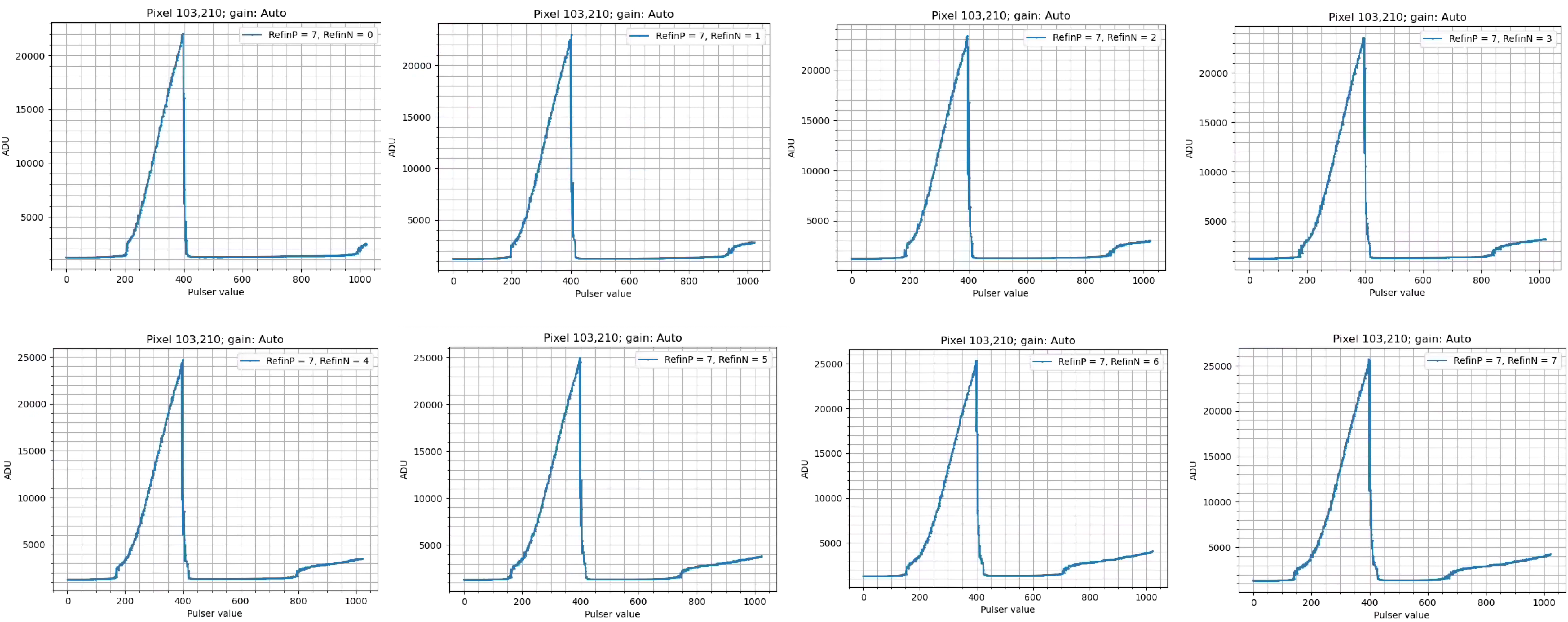
- Horizontal variability/
gradient across ASIC

Pixel 103,210; gain: Auto

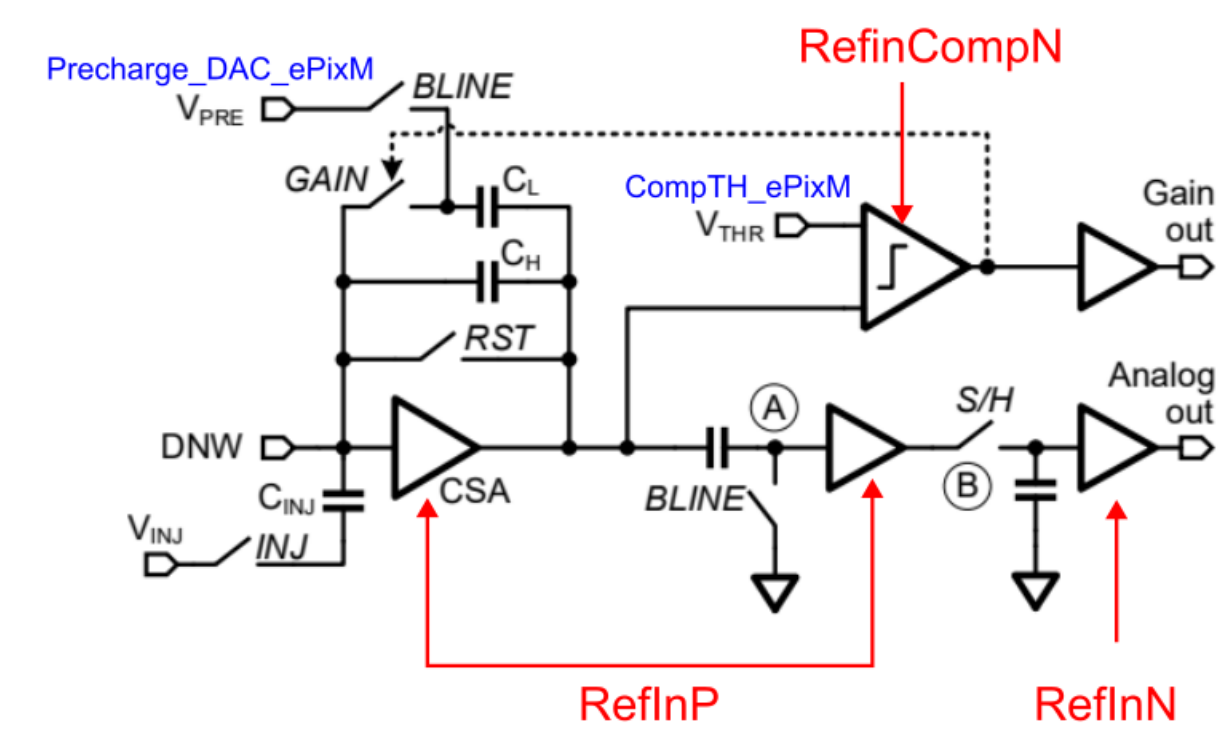


Pixel 103,310; gain: Auto



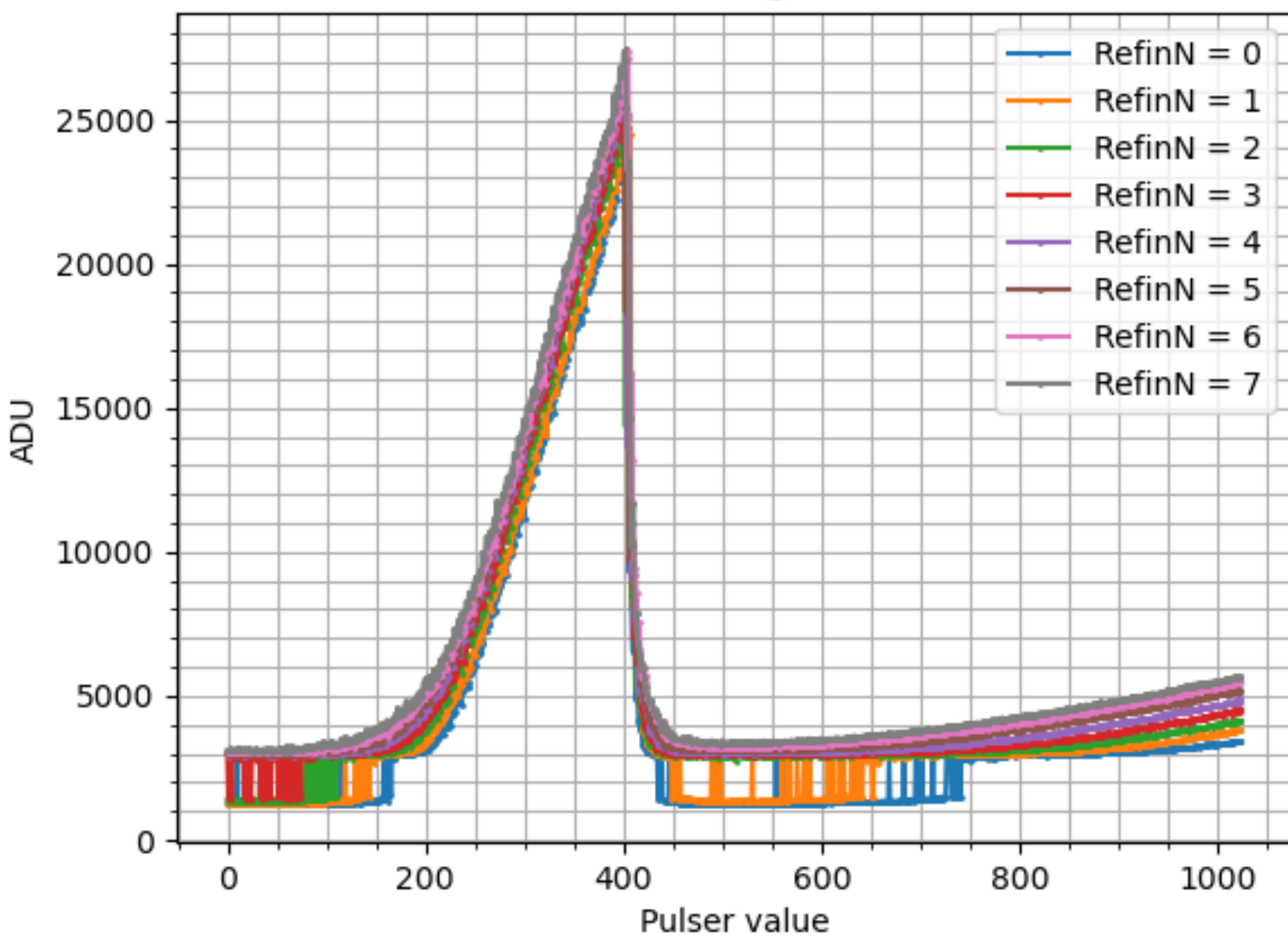


- RefinN optimization

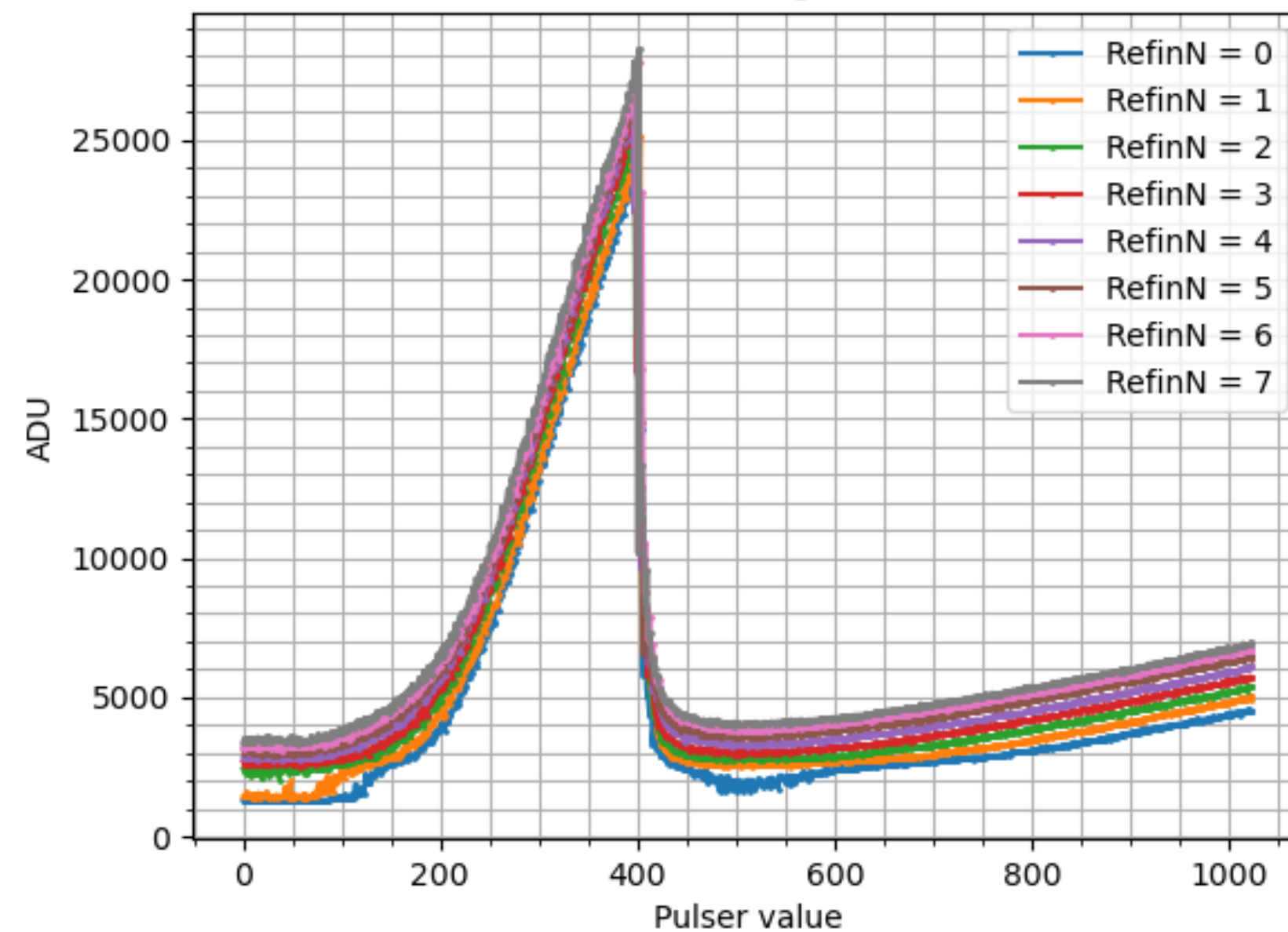


- Horizontal variability/gradient across ASIC

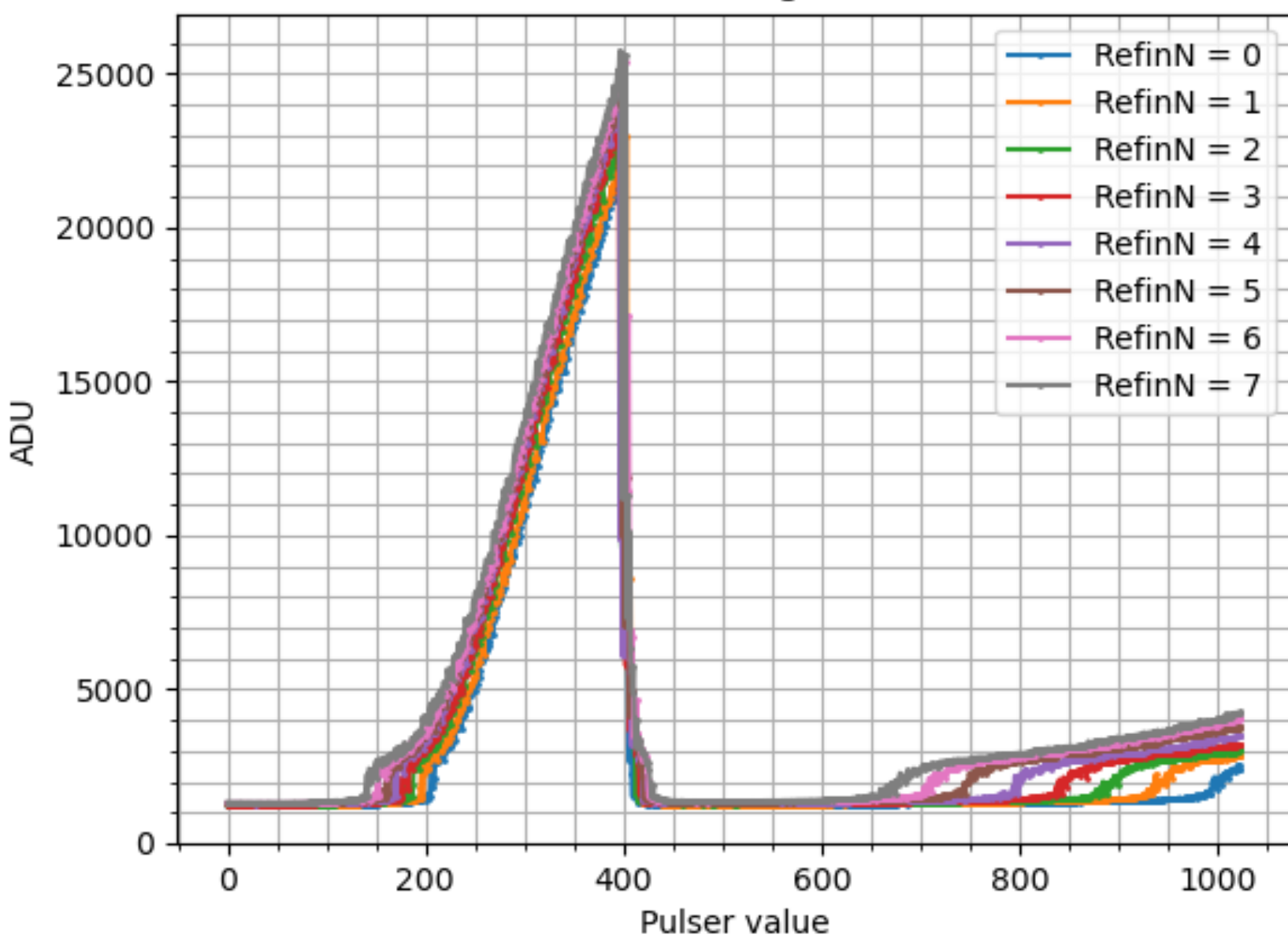
Pixel 103,10; gain: Auto



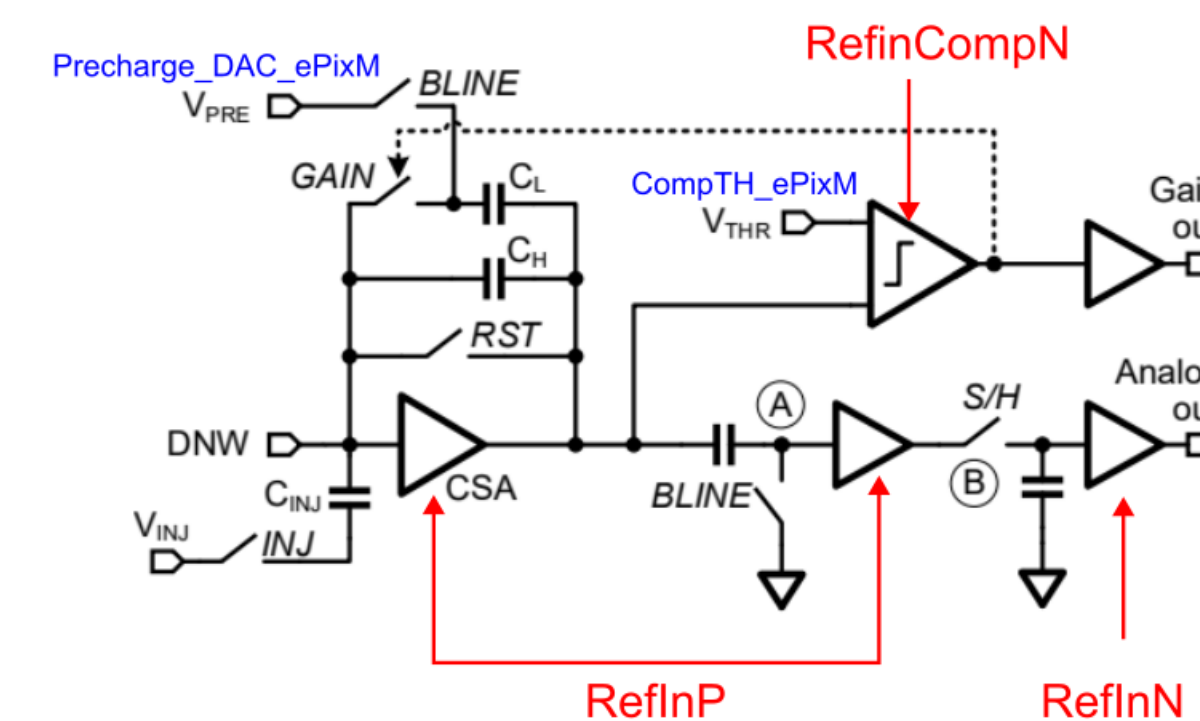
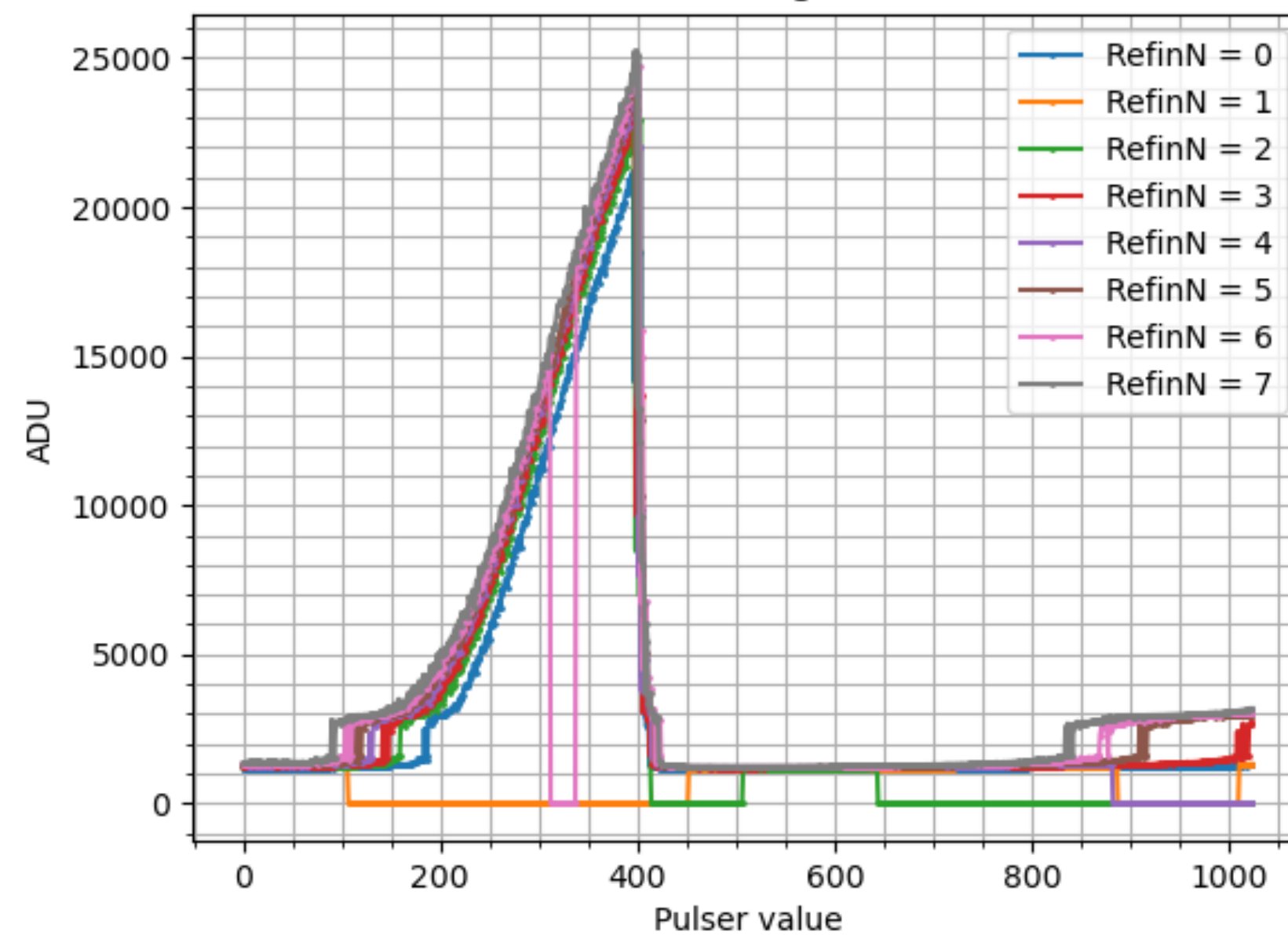
Pixel 103,110; gain: Auto



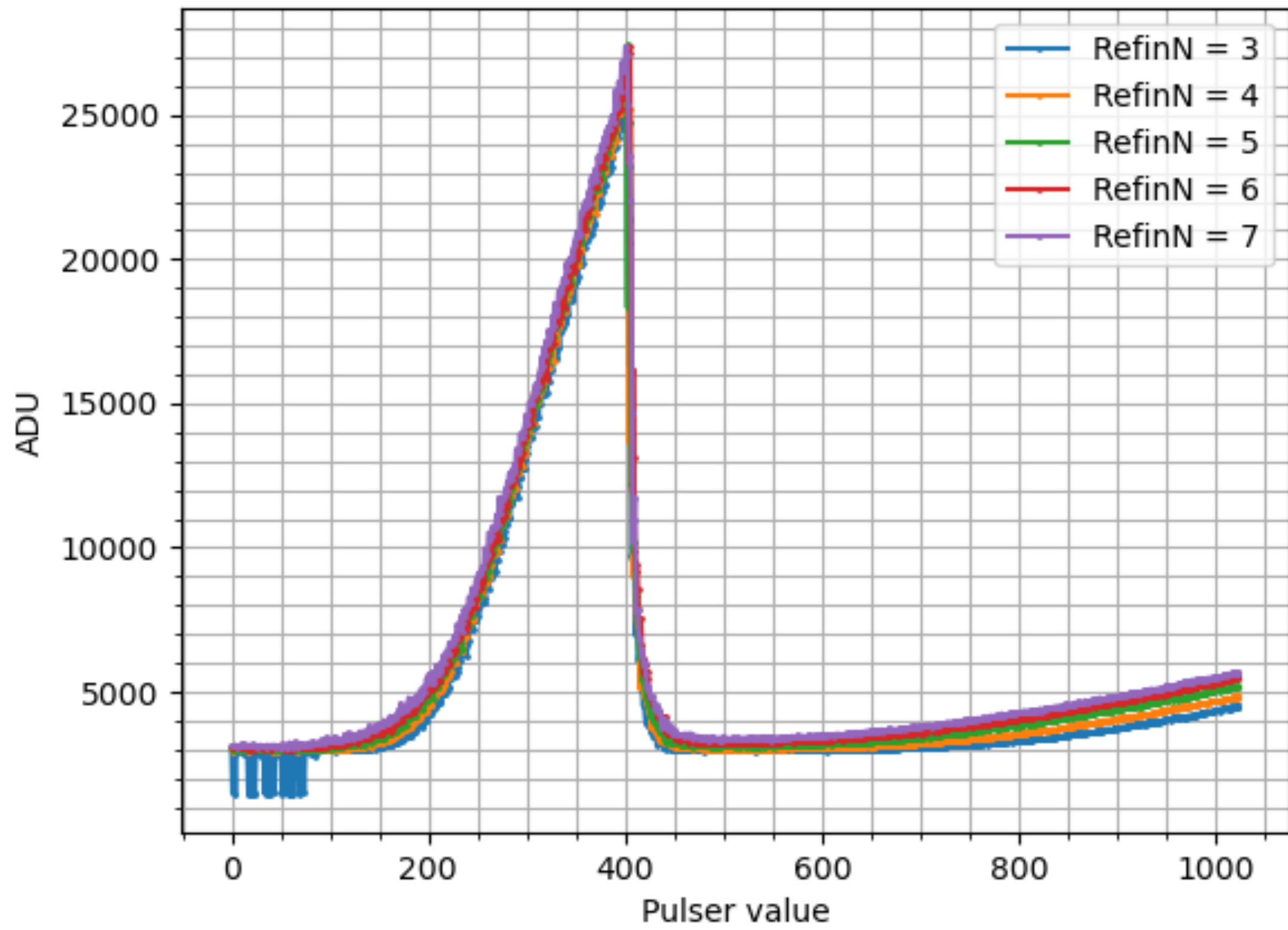
Pixel 103,210; gain: Auto



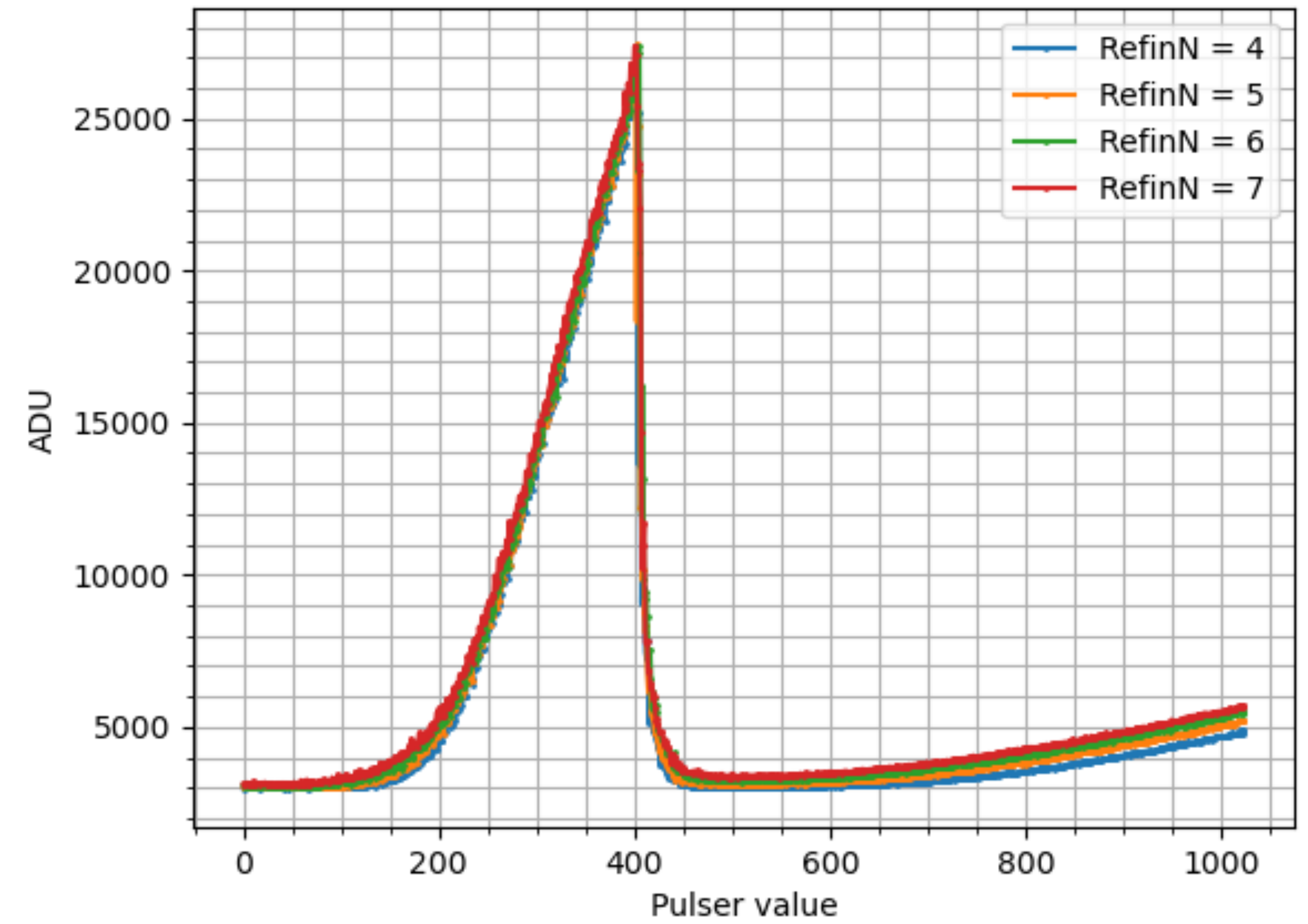
Pixel 103,310; gain: Auto



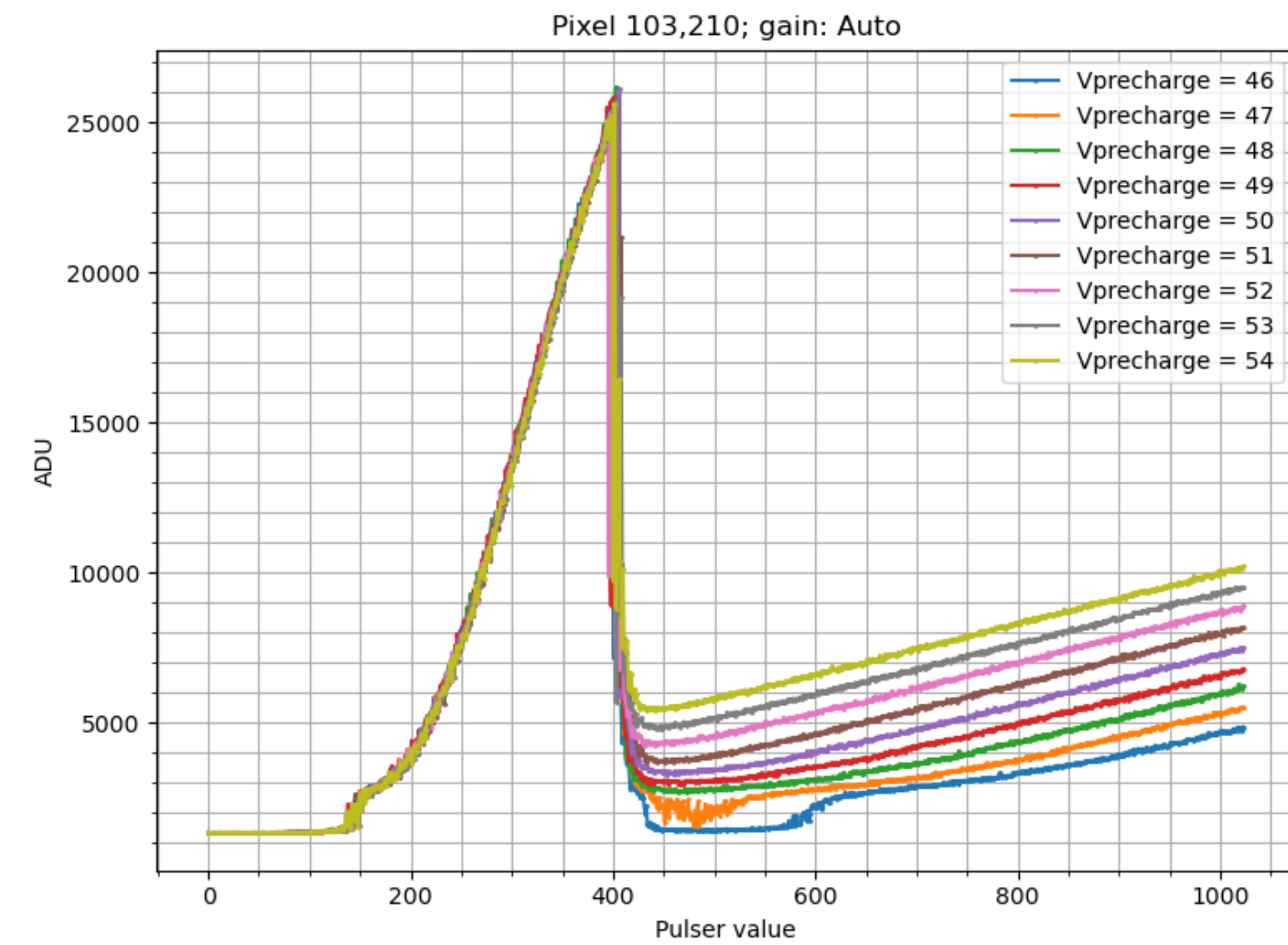
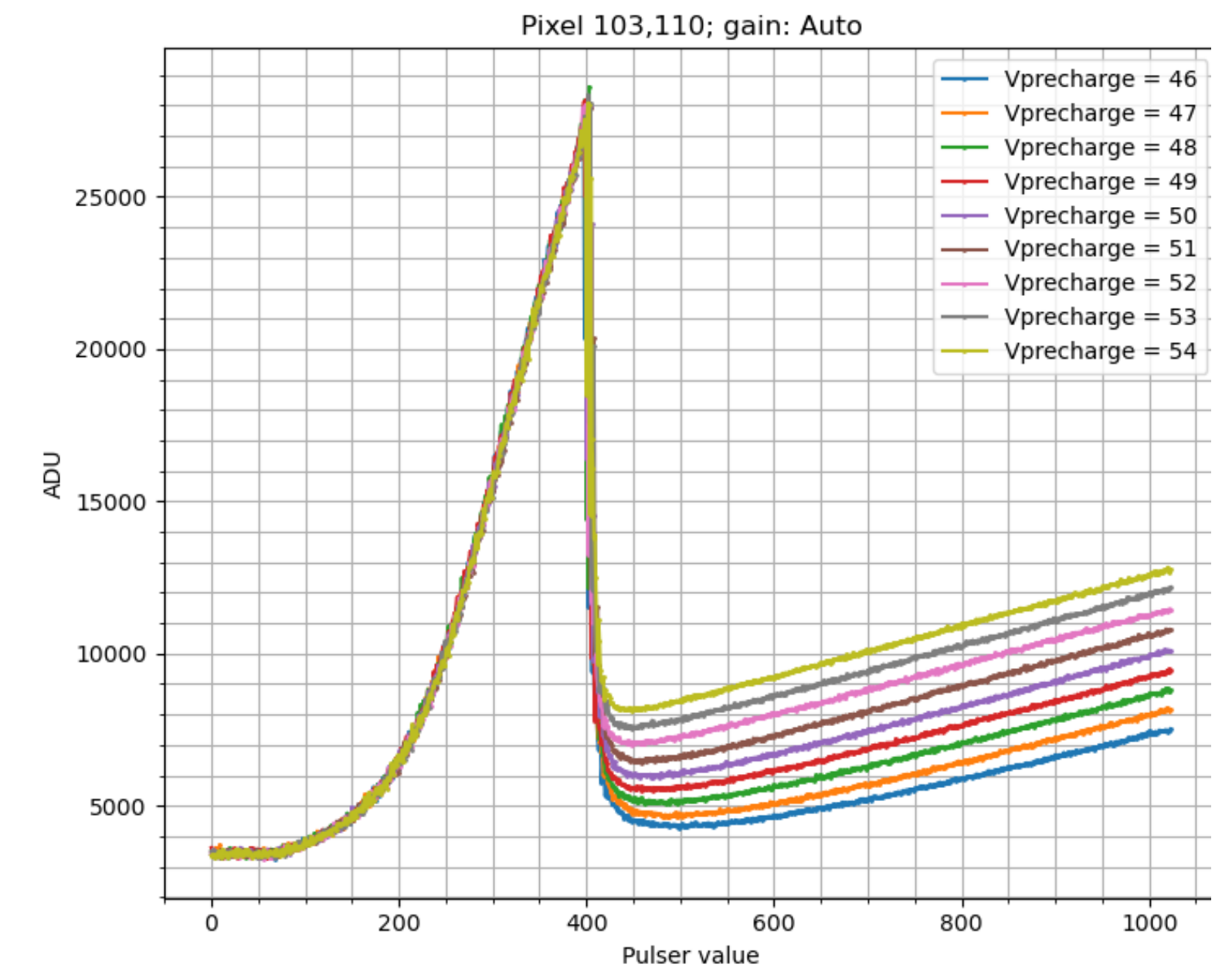
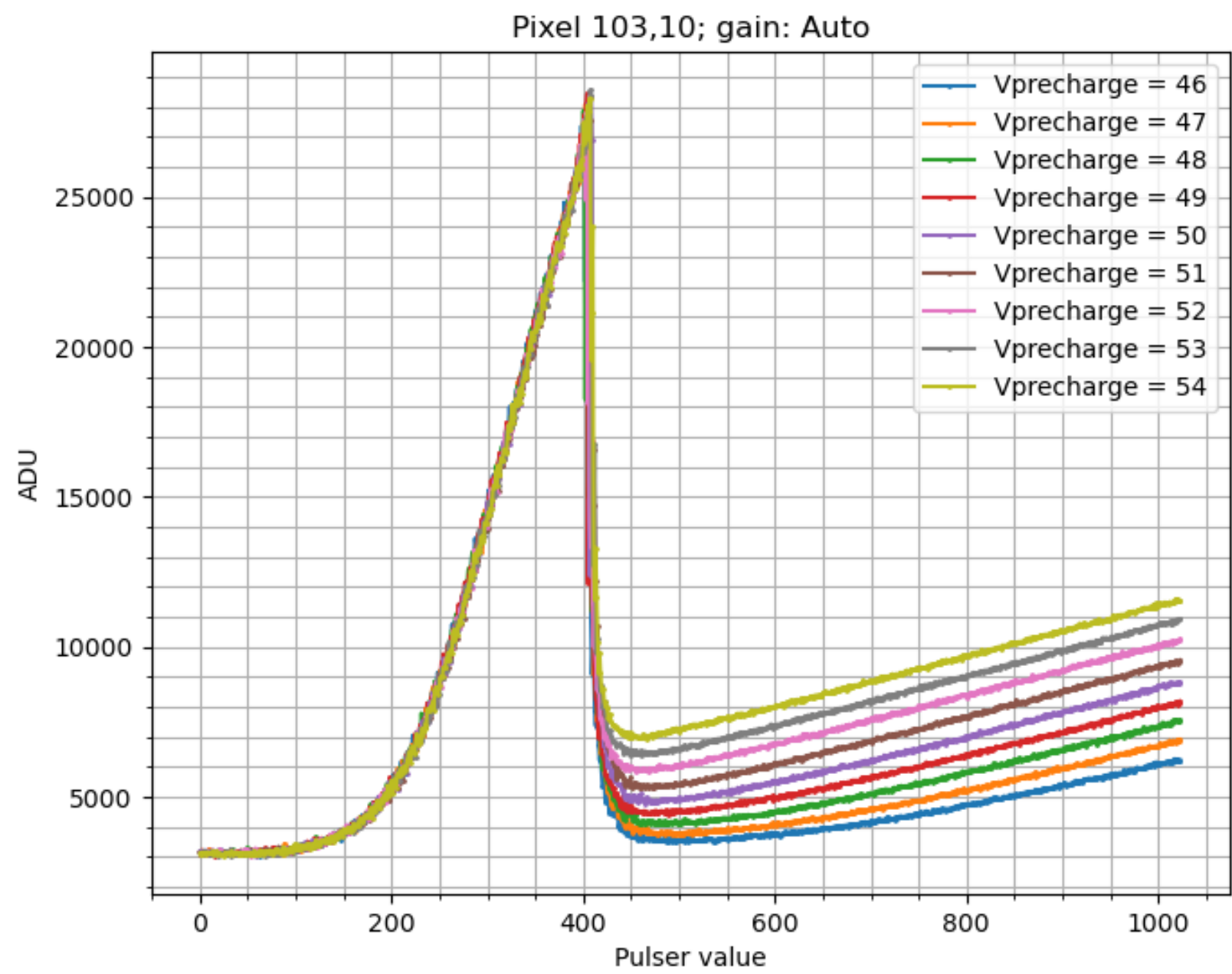
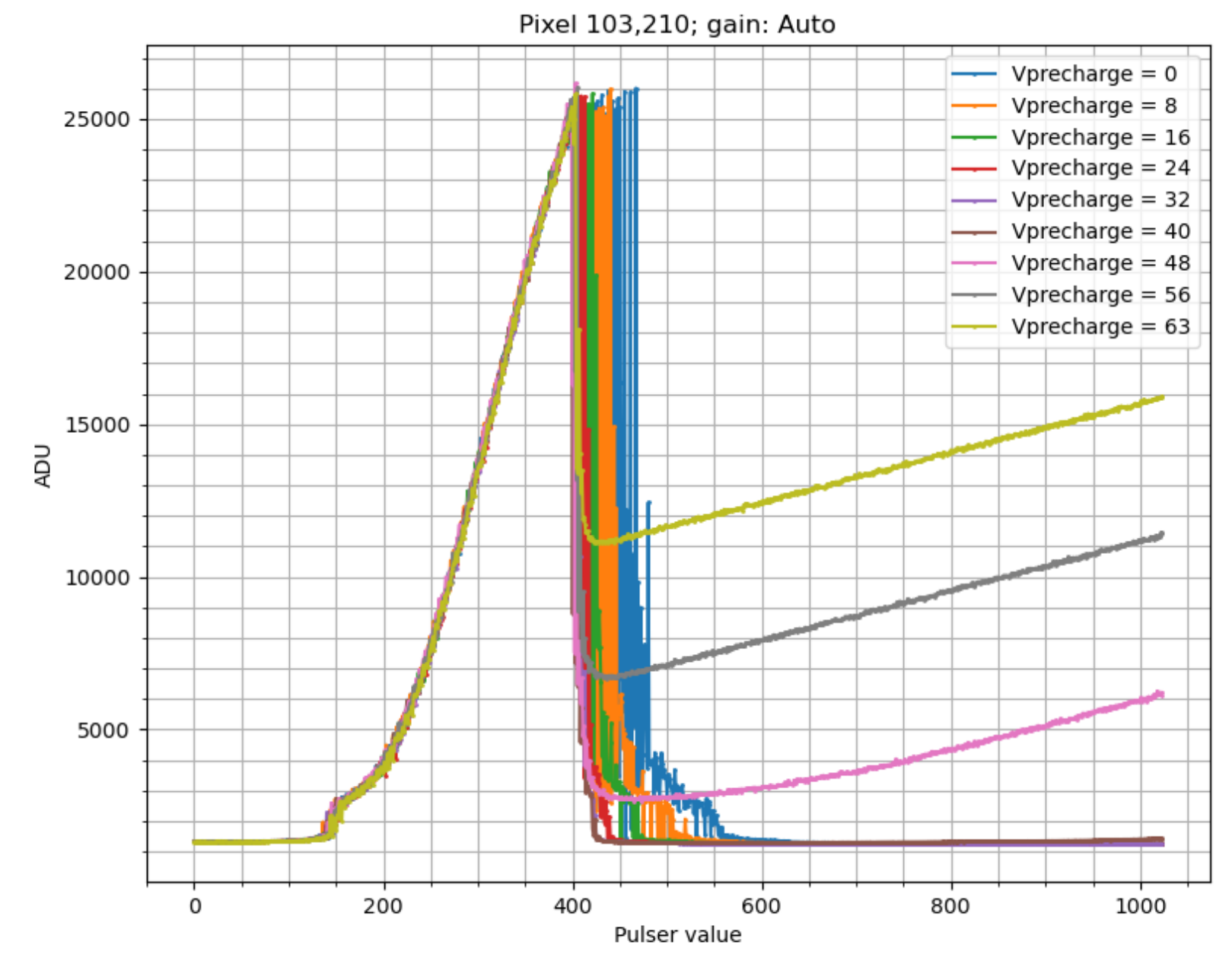
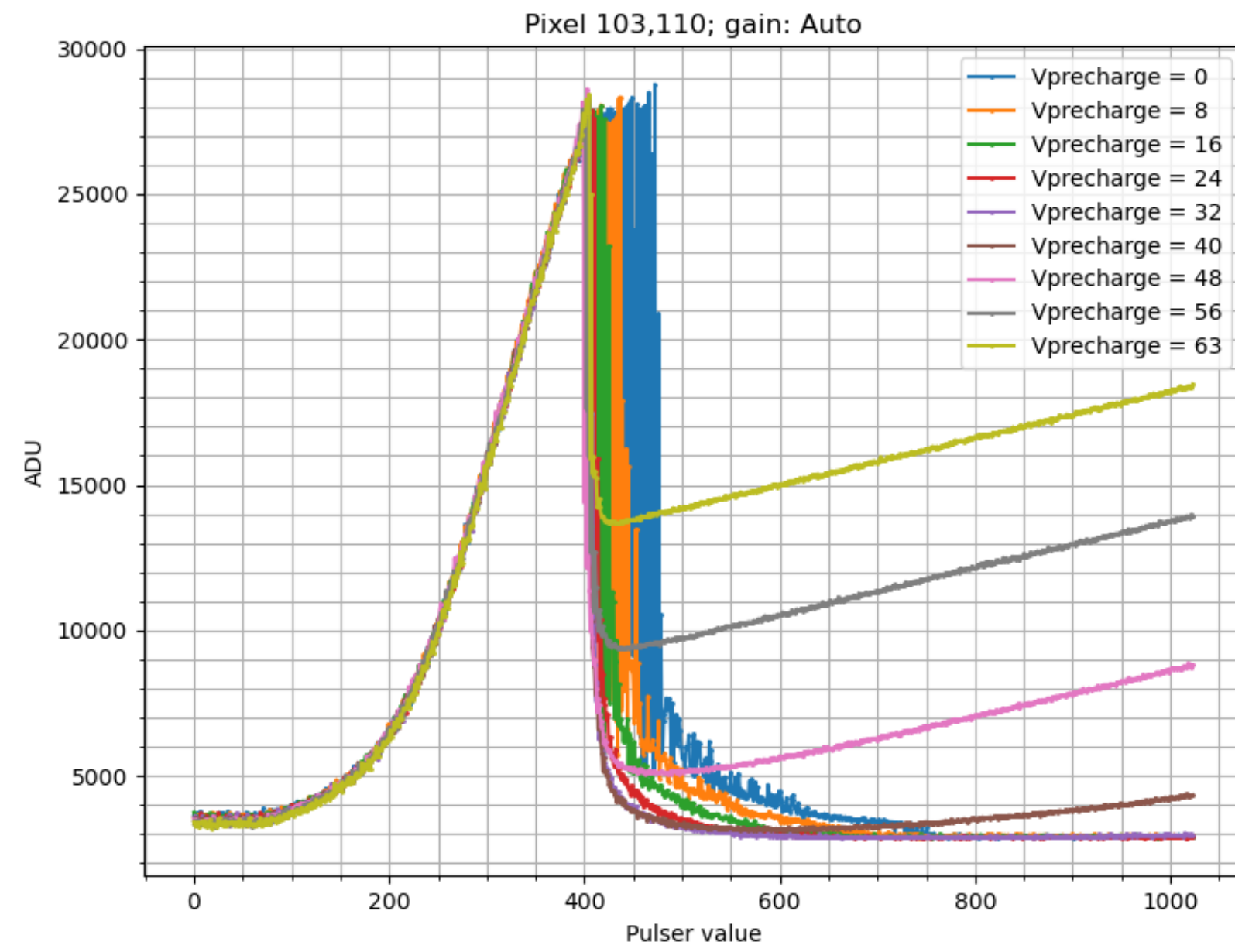
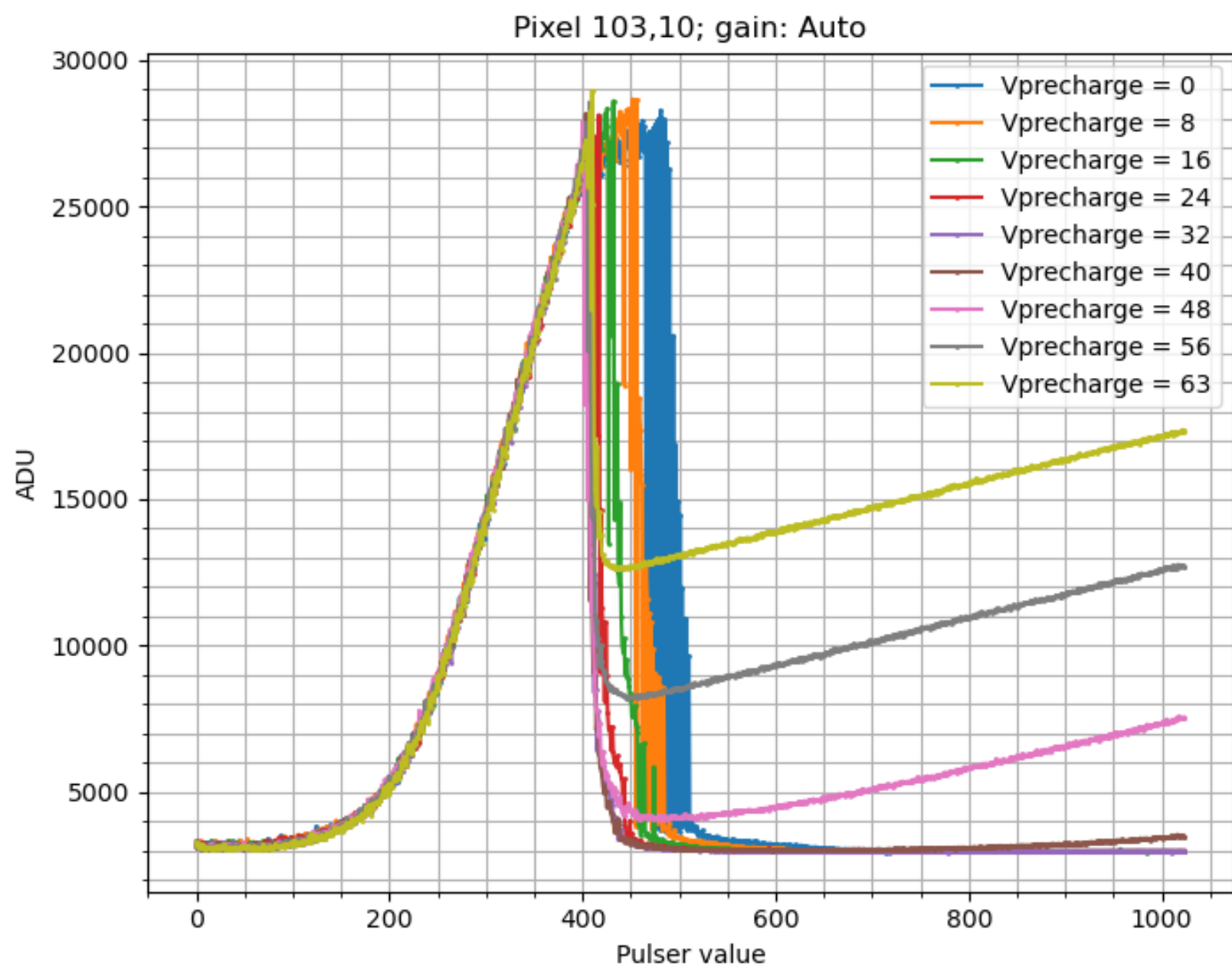
Pixel 103,10; gain: Auto



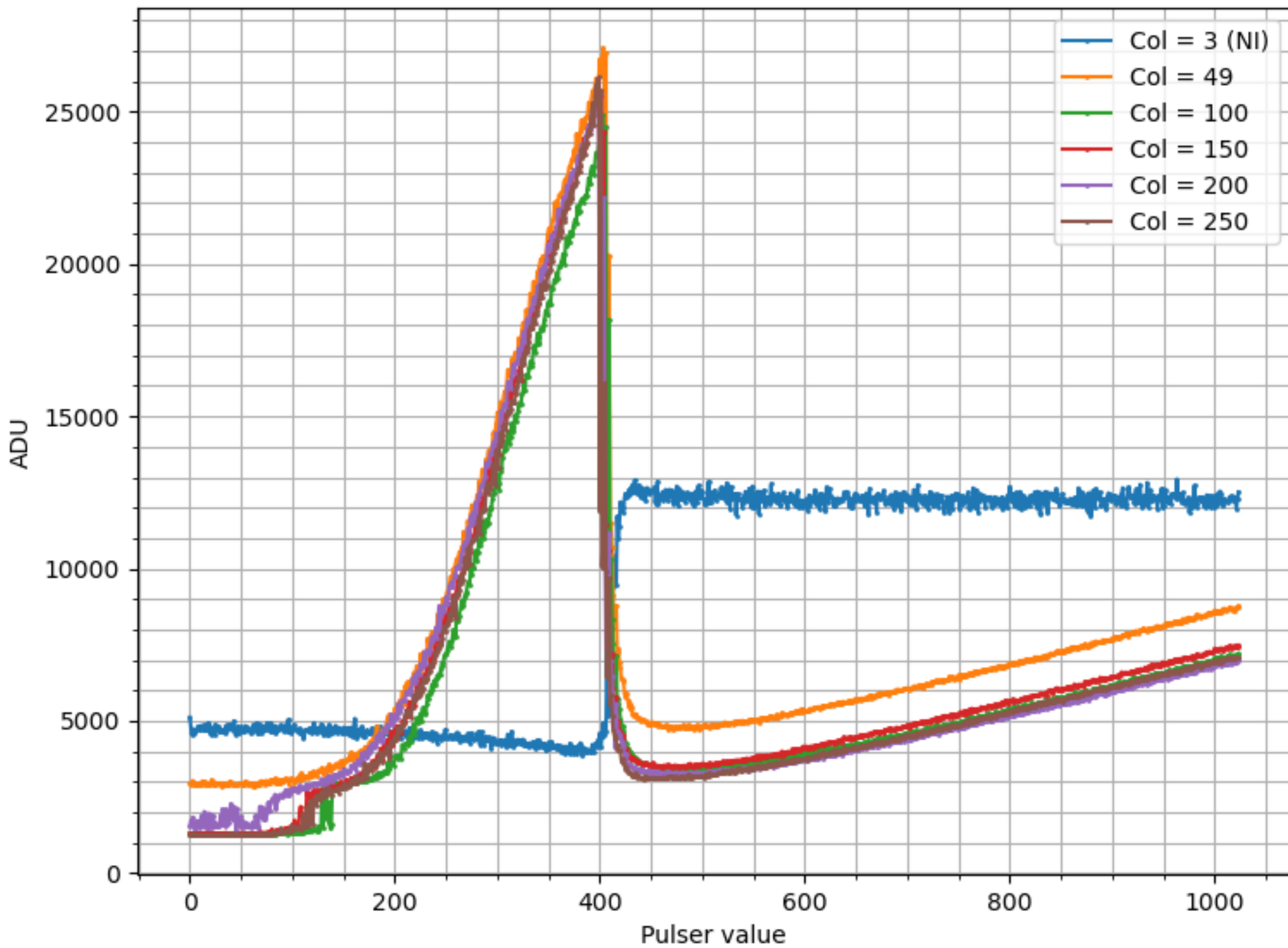
Pixel 103,10; gain: Auto



- Low RefinN cut-off

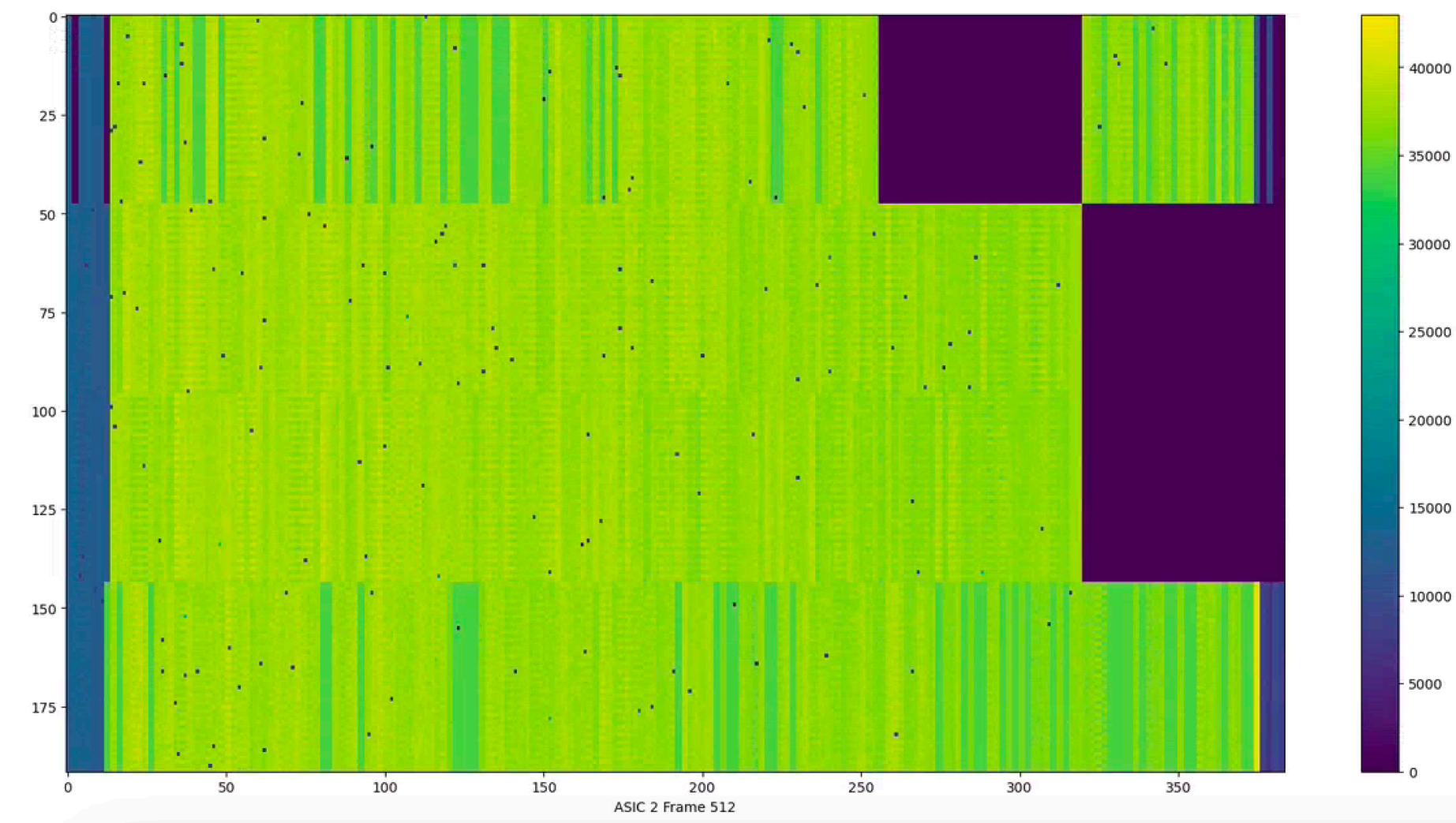
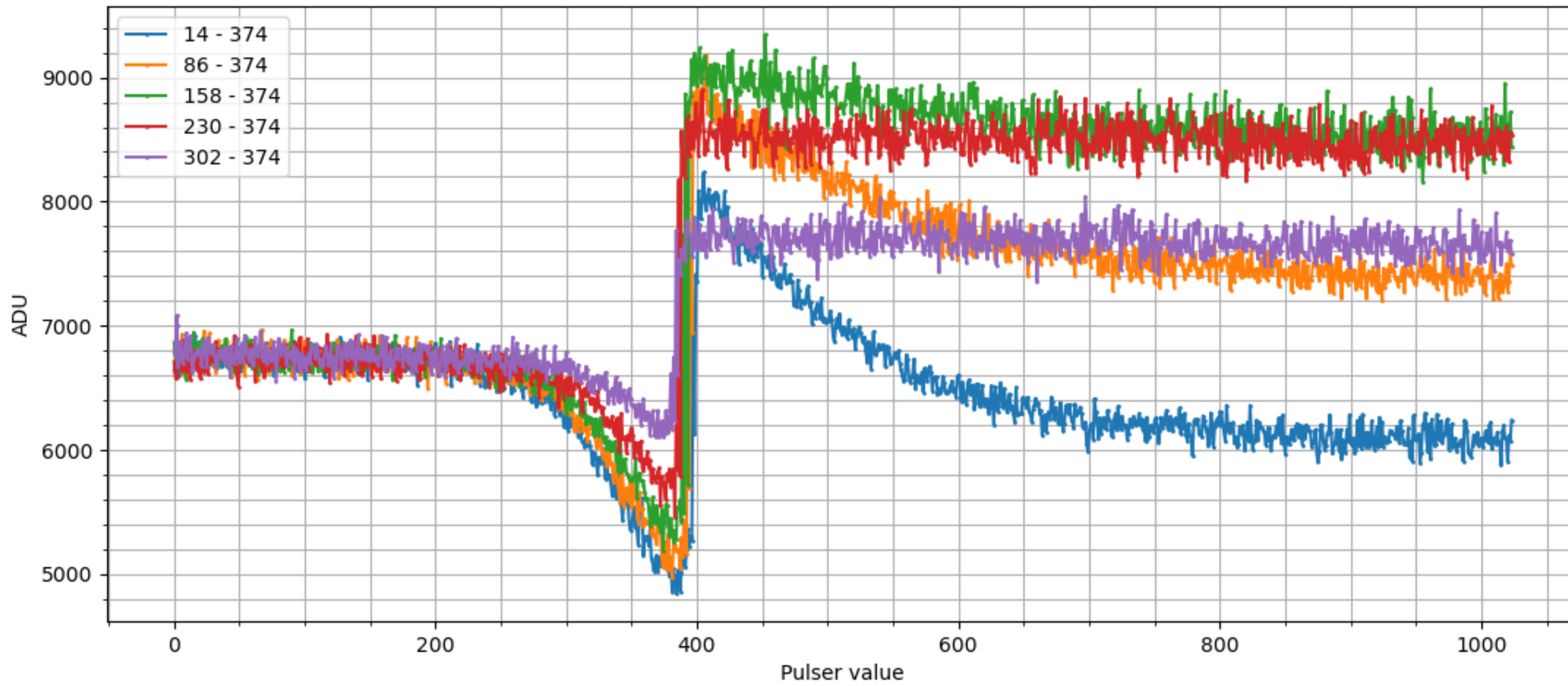


Pixel row 103; gain: Auto

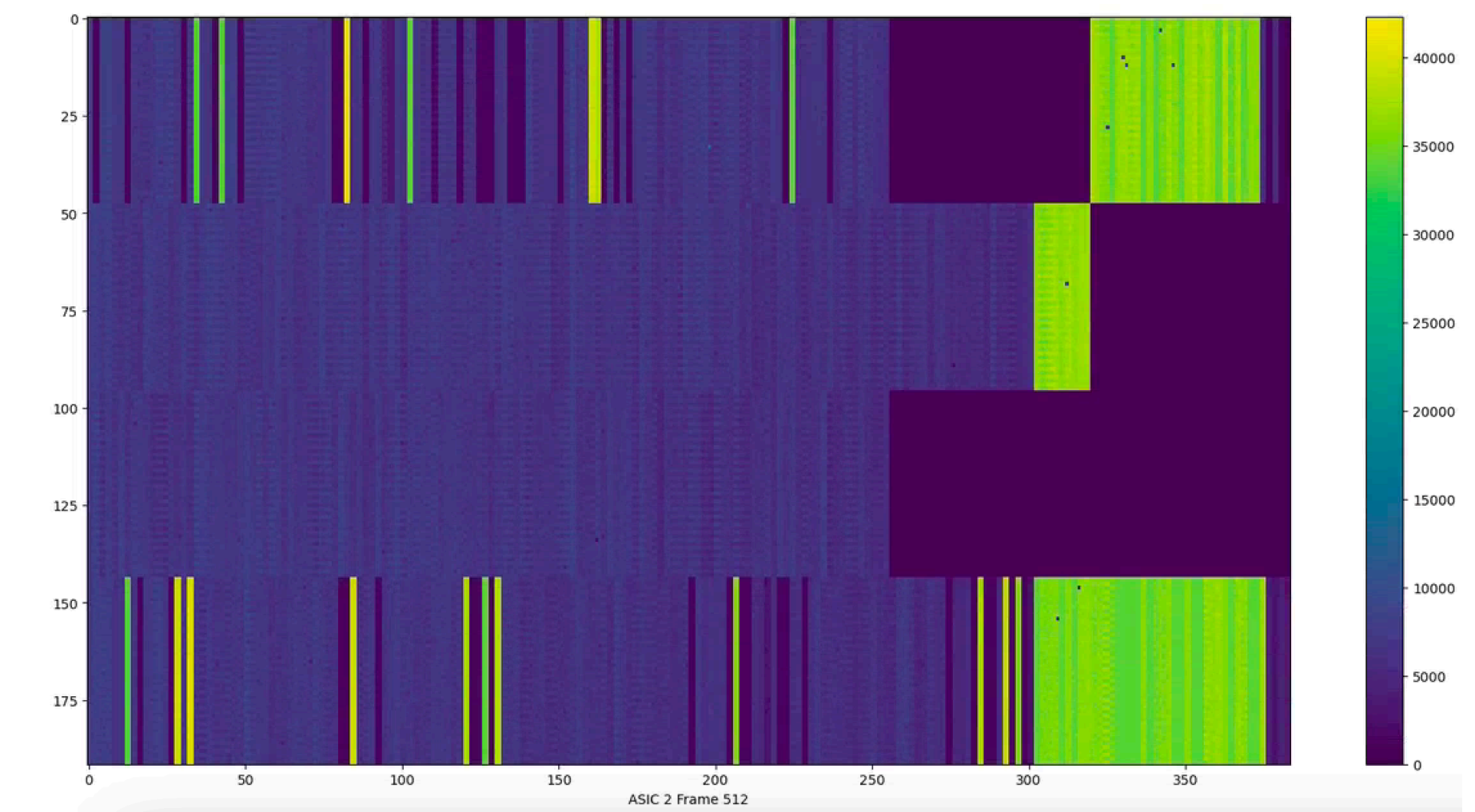
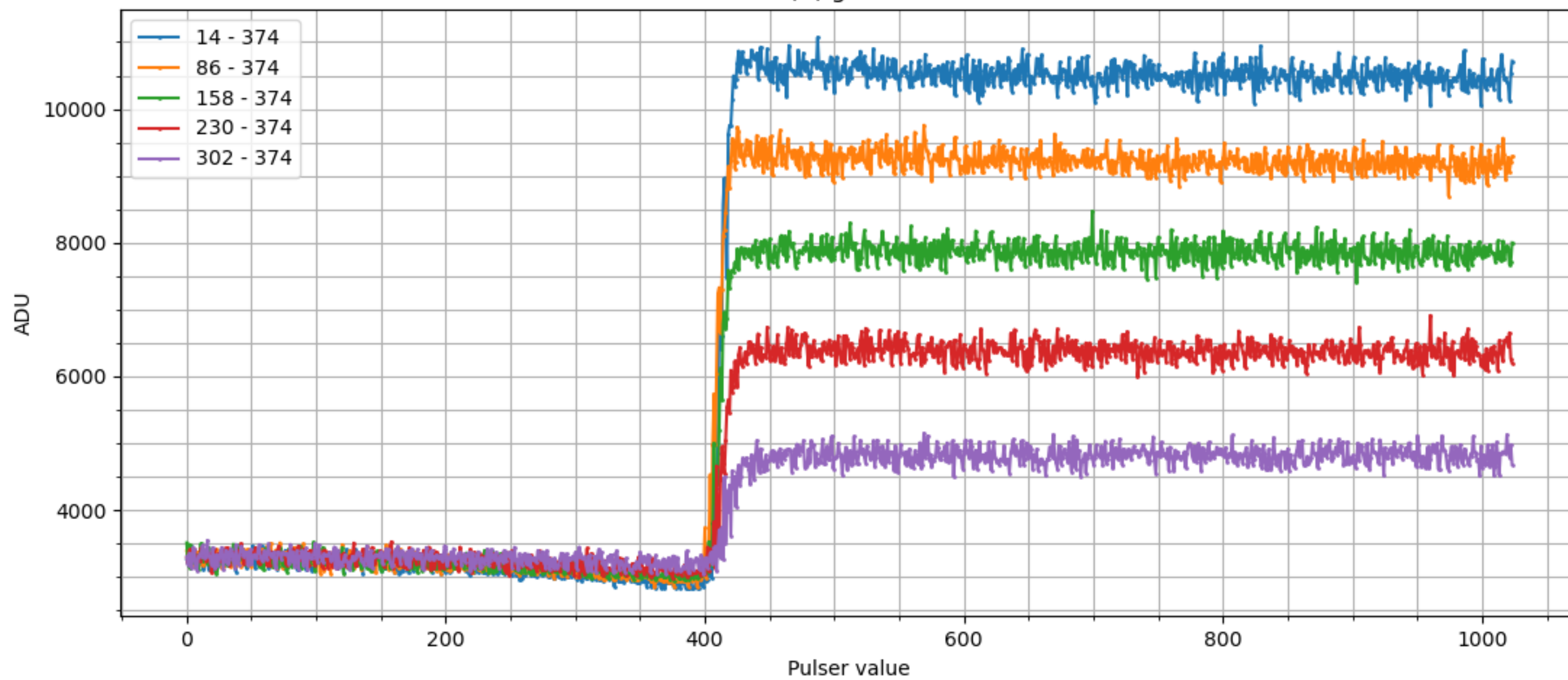


- Horizontal variability/gradient across ASIC

Pixel 103,4; gain: Auto



Pixel 103,4; gain: HDR Auto



Crosstalk with various amount of charge