

# *KPiX & IHEP RPC Studies*

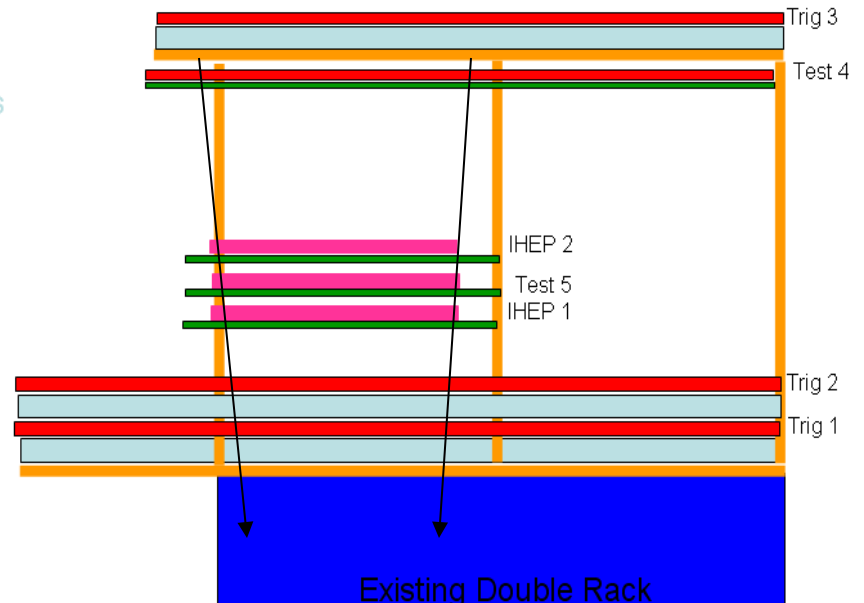
*Henry Band*

*University of Wisconsin*

# RPC Teststand

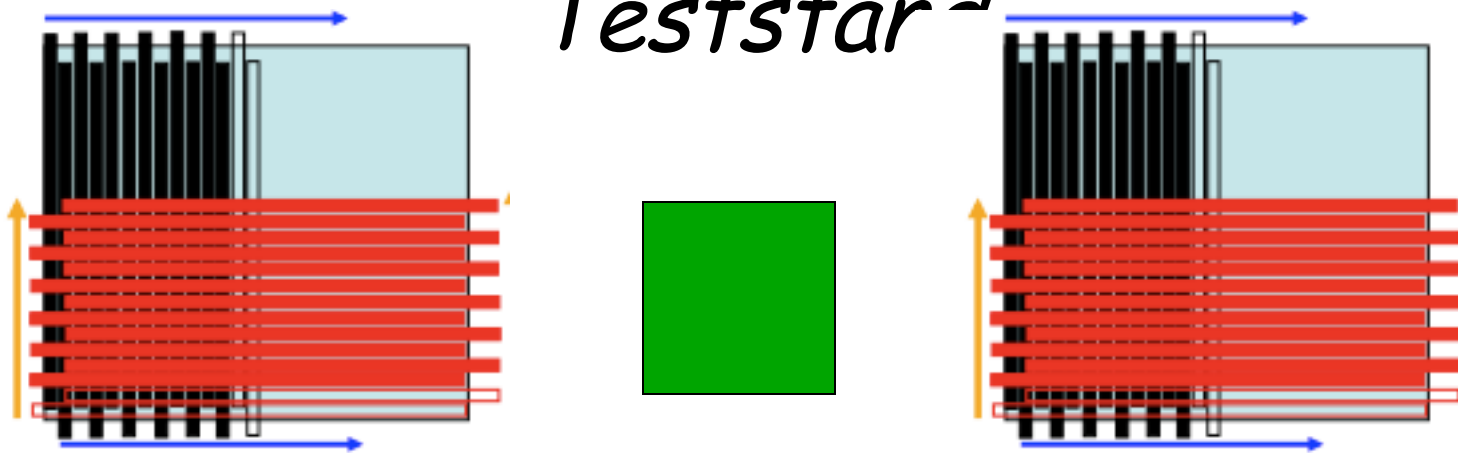
- *RPC test stand with BaBar spares*
- **Available Gases**
  - **BaBar streamer gas** -  
34.9% Freon 134a, 60.6% Argon, 4.5% isobutane
  - **BaBar avalanche gas** -  
75.5% Freon 134a, 19.4% Argon, 4.5% isobutane, 0.6% CF6
  - **Argon**
  - **Ordered CERN/ANL**
  - 94.5% Freon 134a, 5.0% isobutane, 0.5% CF6
- **Trigger** ~ 10 Hz
  - **3-fold coincidence**  
**Trig1\*Trig3\*IHEP 2**

Trigger RPCs  
Foam Shelves  
Plywood Shelves  
Test RPCs  
Unistrut



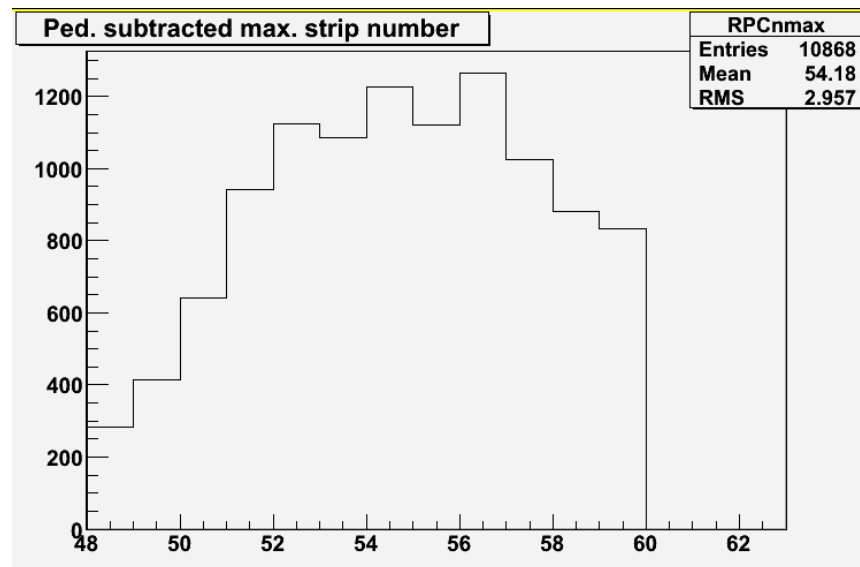
- **Available RPCs**
- **IHEP 0.5 by 0.5 m (4 + 7)**
- **Italian Bakelite 0.5 by 0.5 m**
- **BaBar spares 1.1 by 1.3-1.6 m**

# Teststar $\downarrow$

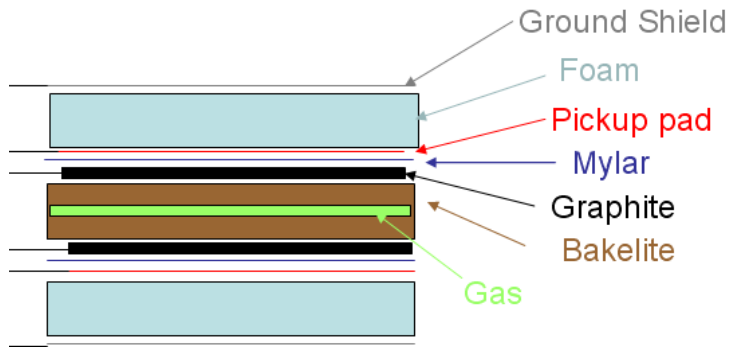


Trigger made from subset of x and y strips to match IHEP chamber size

For these initial tests  
Trigger coverage non-uniform  
Biased efficiency measurement

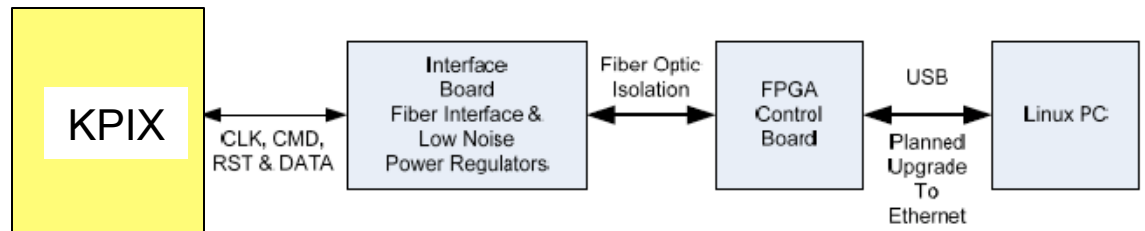
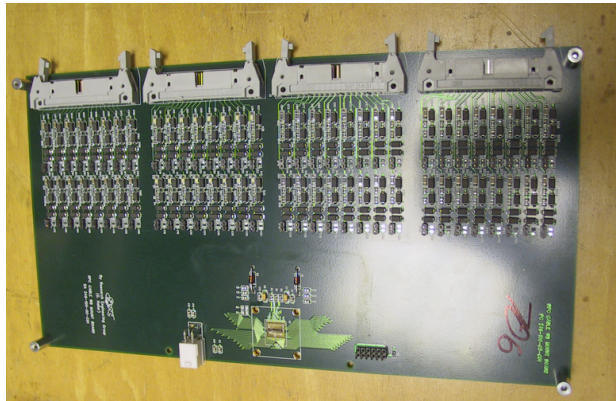
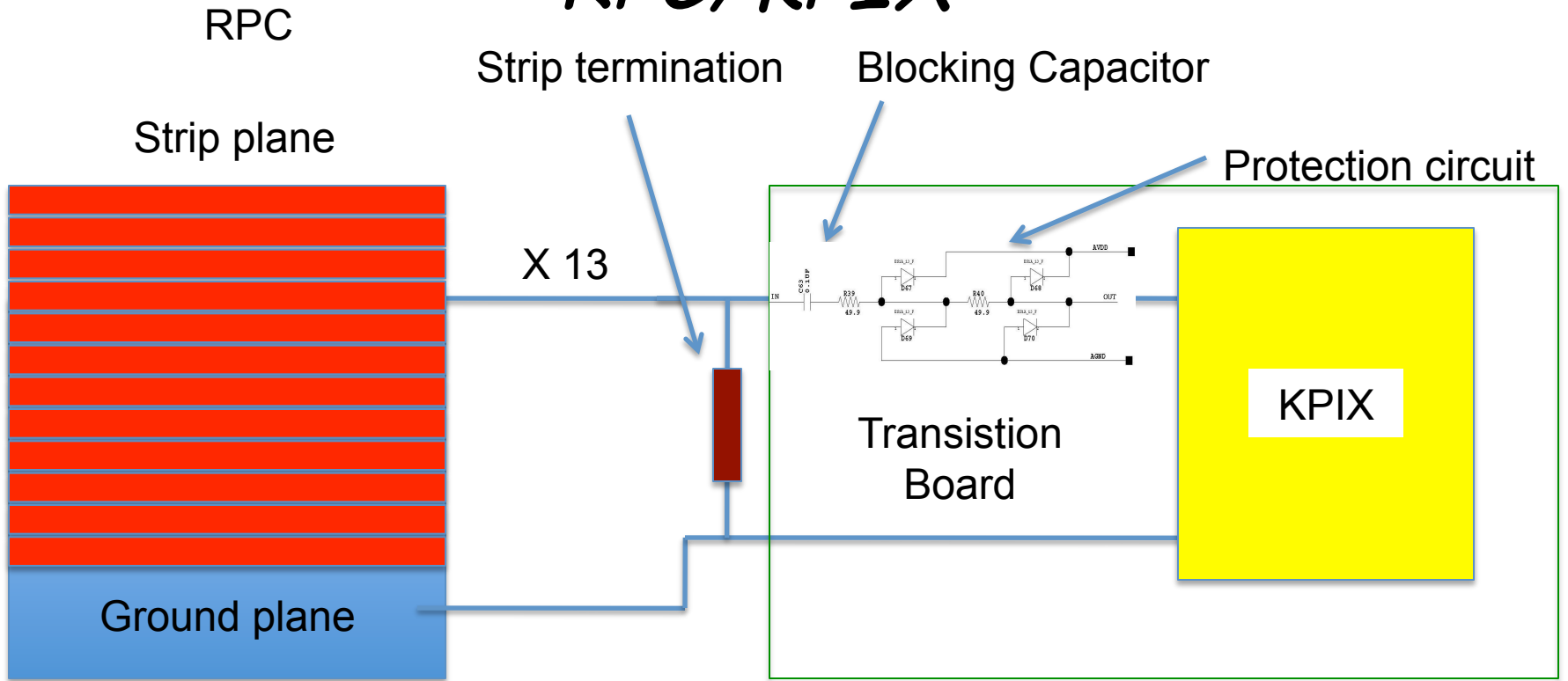


# RPC details



- *IHEP and Italian RPC have 2mm gas gap & 2 mm Bakelite anode & cathode*
- *Pickup strips 22-38 mm pitch, capacitance to gnd. 3-.6 nF*
- *IHEP RPCs have no linseed oil coating*

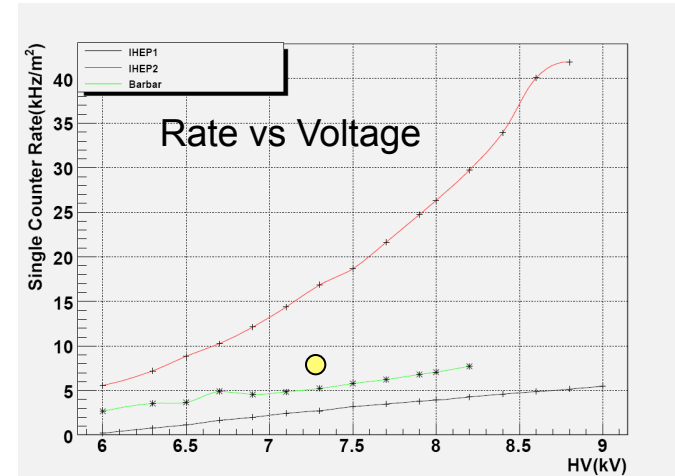
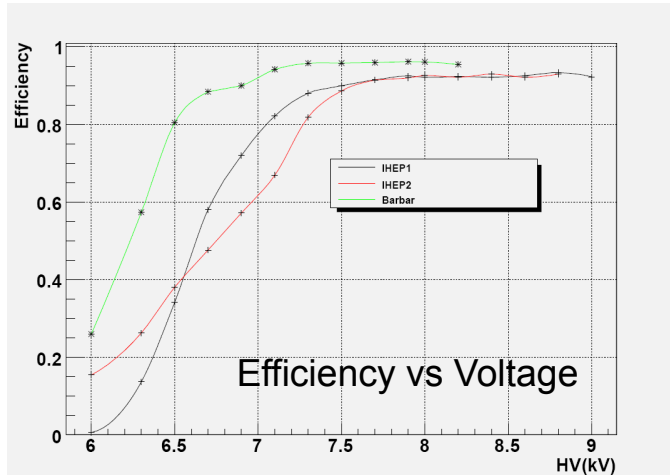
# RPC/KPIX



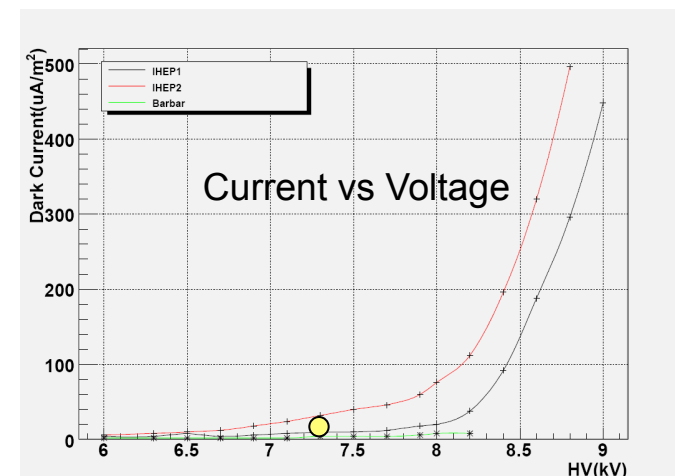
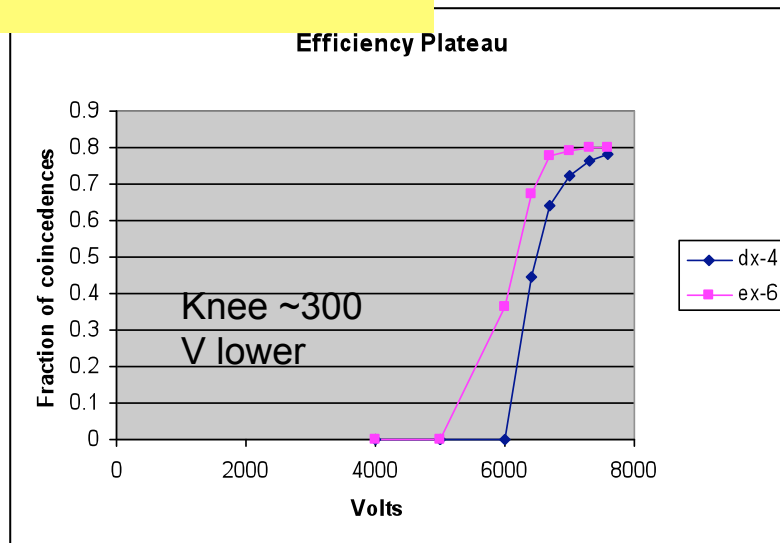
03/03/09

H. Band - SiD Workshop 3/09

# Streamer Mode



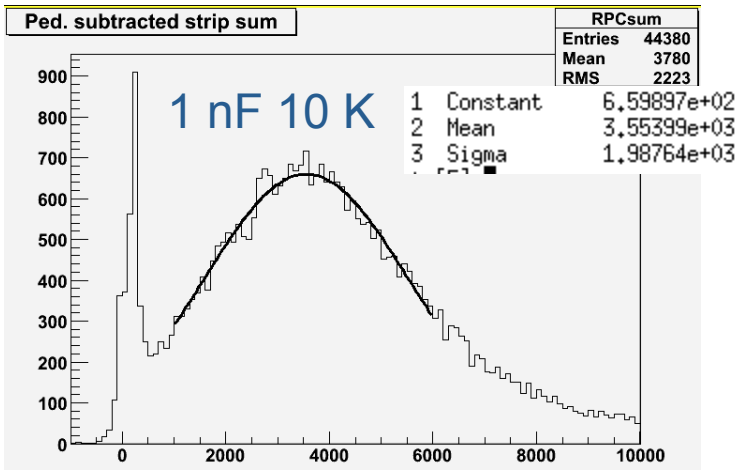
## New IHEP RPCS



# Preliminary RPC/KPIX Data

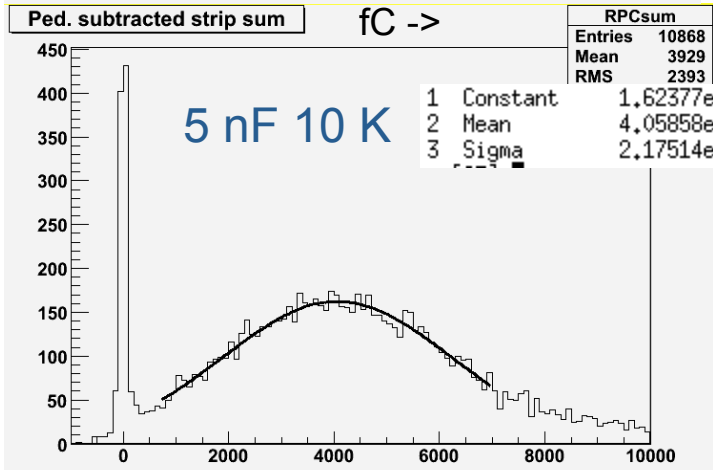
Ryan Herbst,  
Dieter Freytag  
SLAC

- *"Proof of Concept"*
- *RPC interface board*  
*64 channels*
- *First tests -AC coupling*
  1. *Optimize resistor/capacitor values*
  2. *Protection circuits*
  3. *KPIX readout modes*
- *Vary*
- *Strip Termination*  
*5-100 k $\Omega$*
- *Blocking Capacitor 0.1 - 5 nF*
- *KPIX int. time 1.4 - 4  $\mu$ S*
- *Asynch. or triggered readout*
- *Periodic or DC resets*



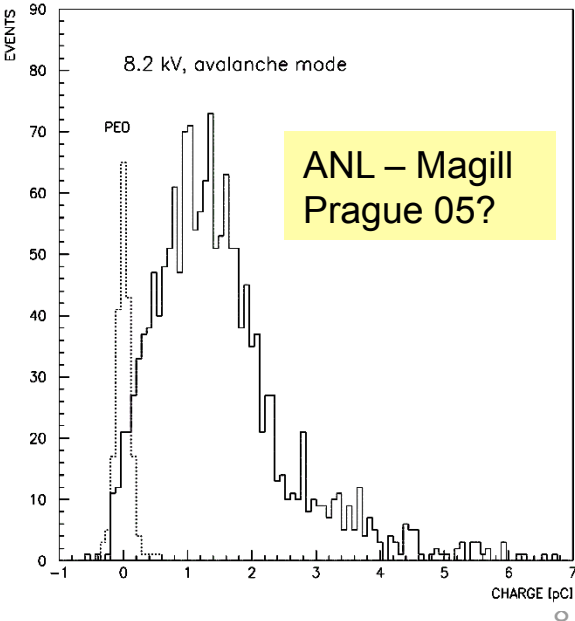
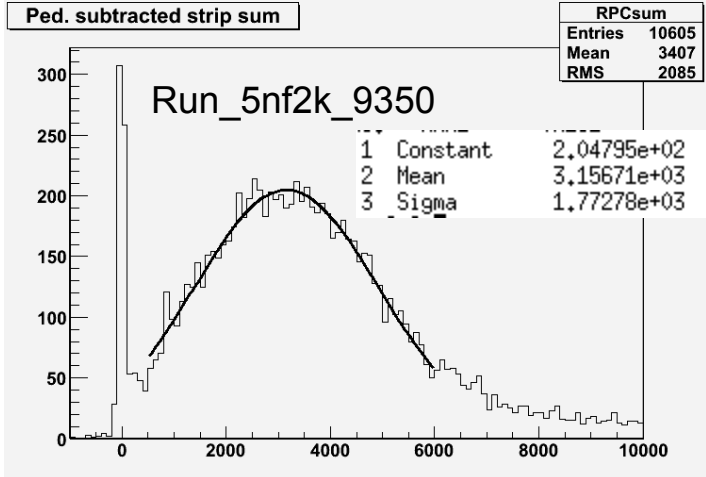
9350 V

Compare transition board components  
~20% variation



RPC probably poorly aligned resulting in more events without signal

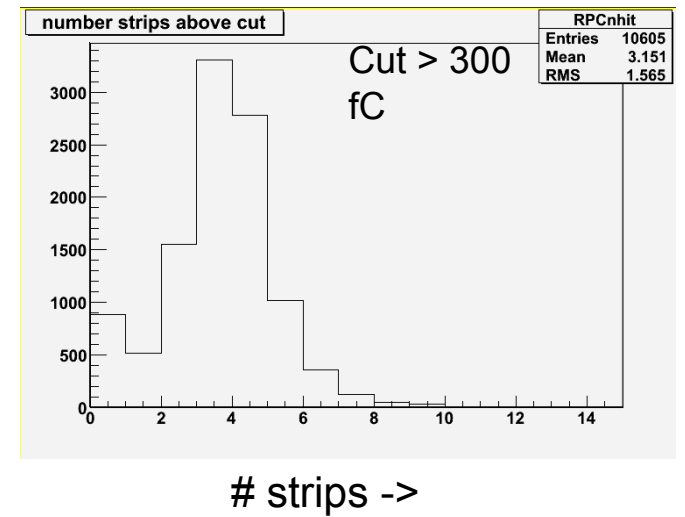
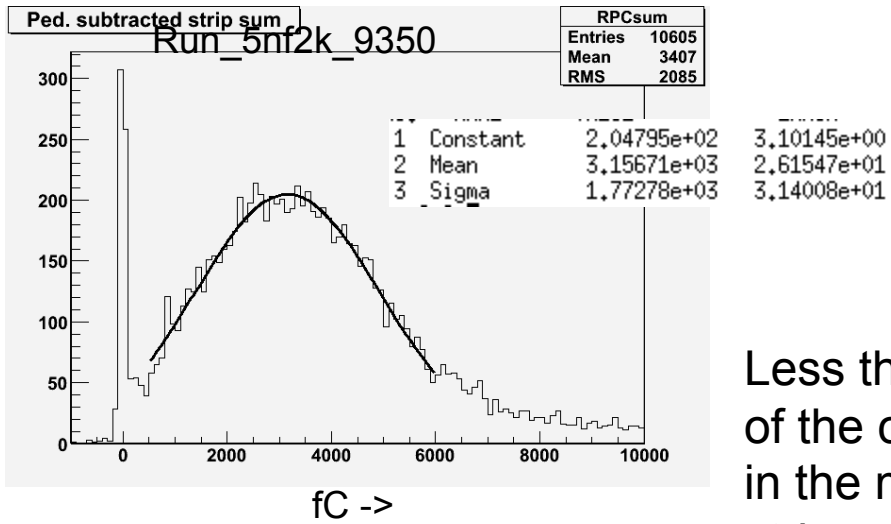
Total charge 3-4 pC, larger, as expected, than ANL RPCs. Need absolute calibration.



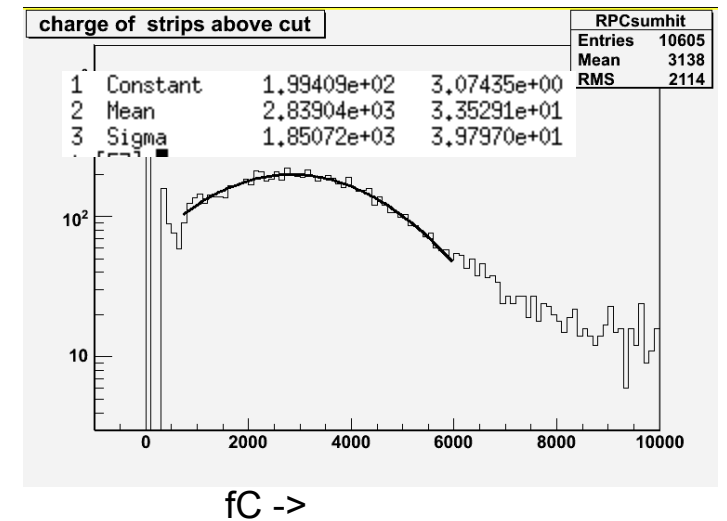
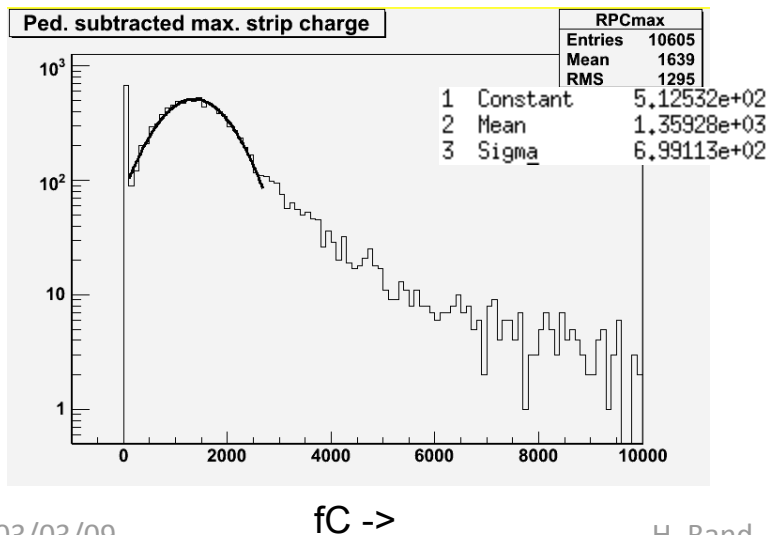


Charge Sum of all strips  
 Charge of Max. strip  
 Charge Sum of strips above cut

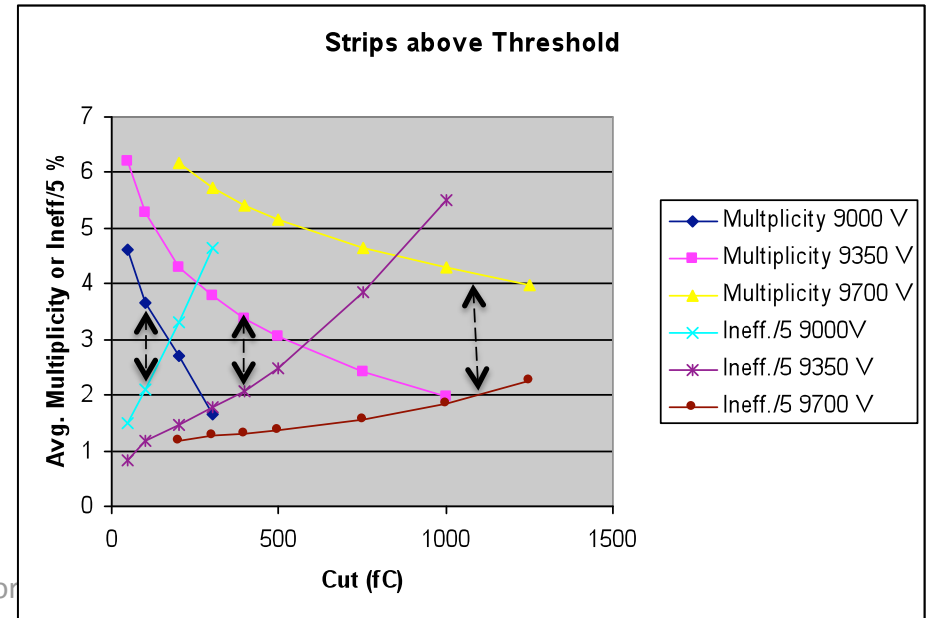
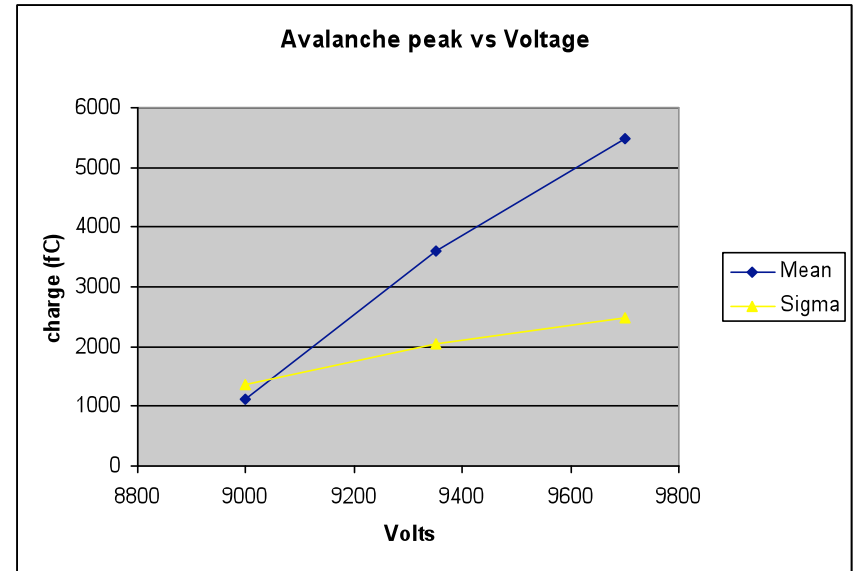
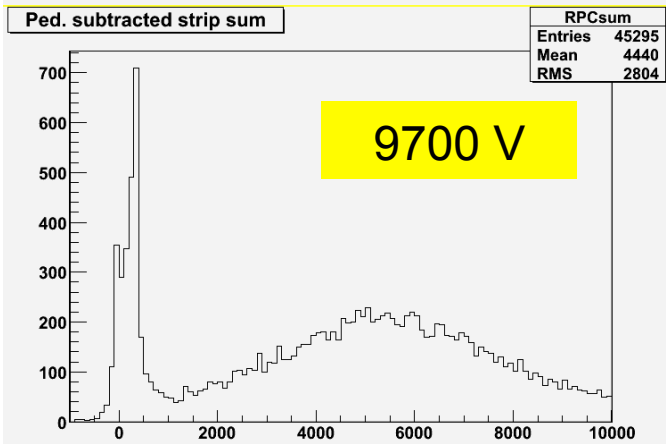
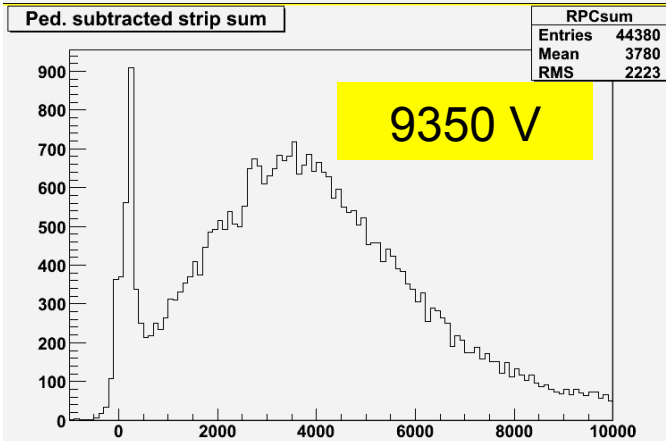
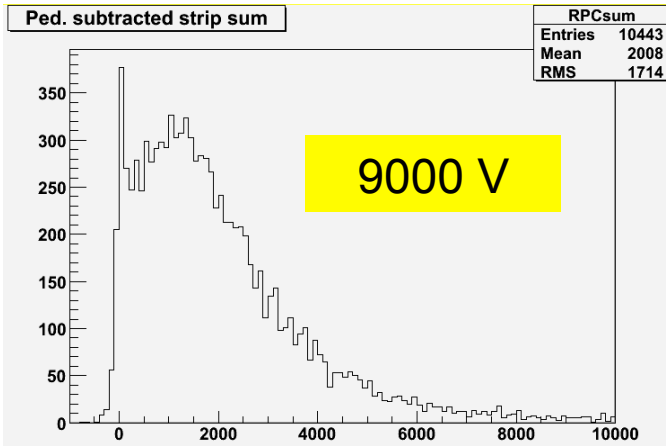
Strips 3.8 cm wide



Less than half  
 of the charge is  
 in the max.  
 strip



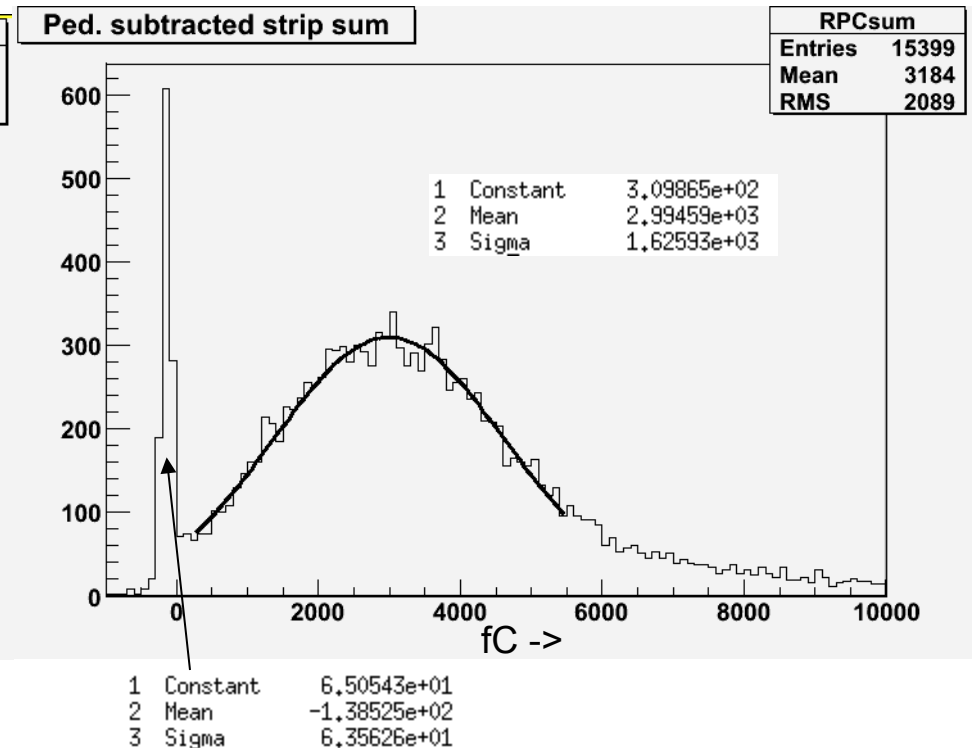
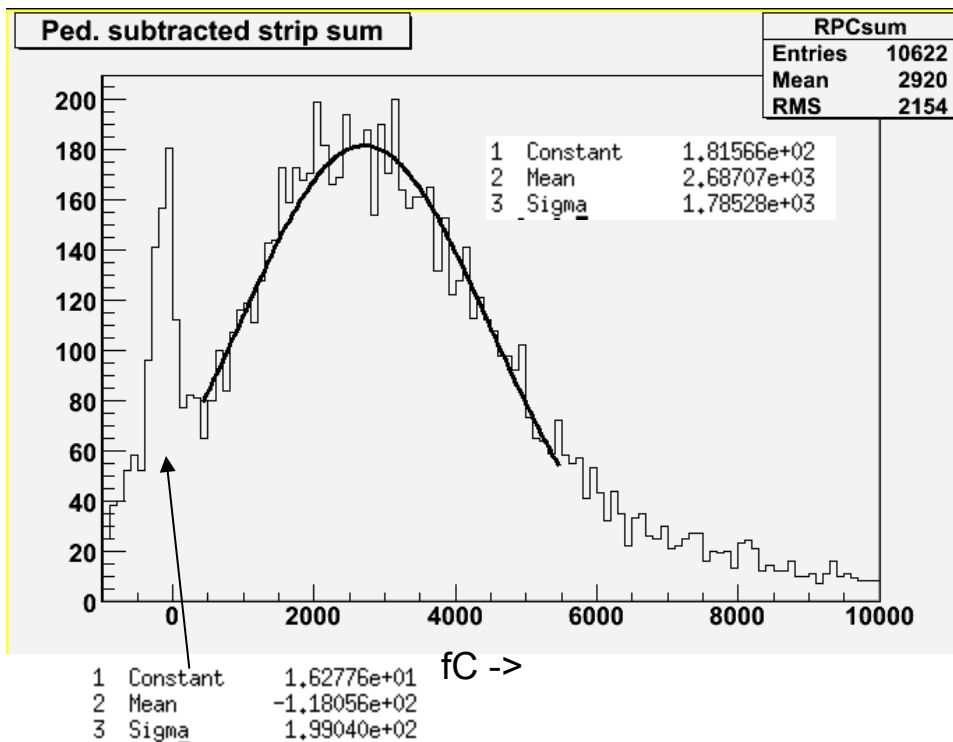
# HV Scan



# KPIX Reset Mode Study

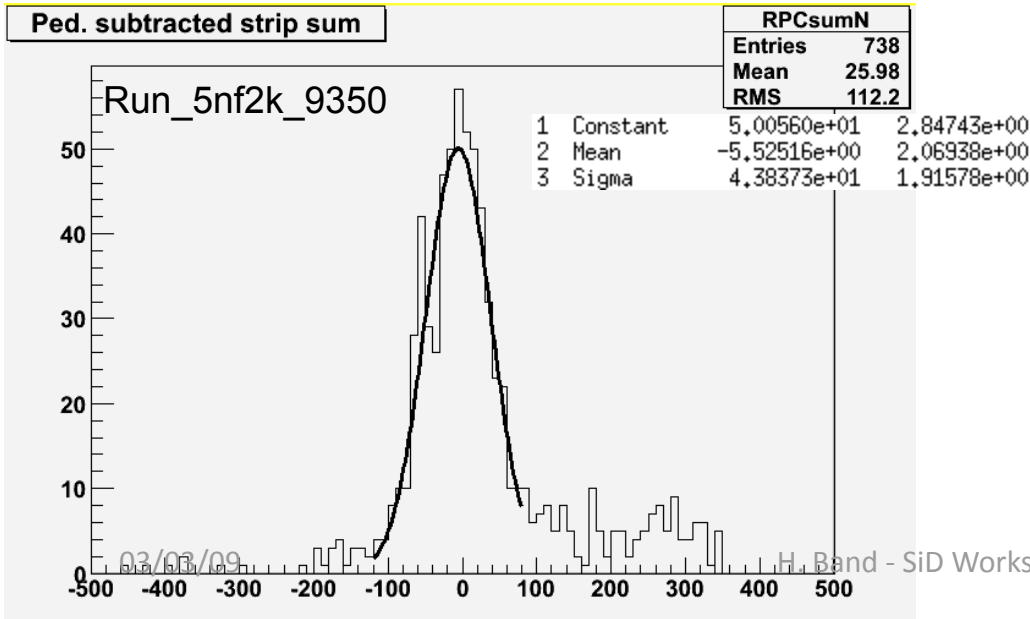
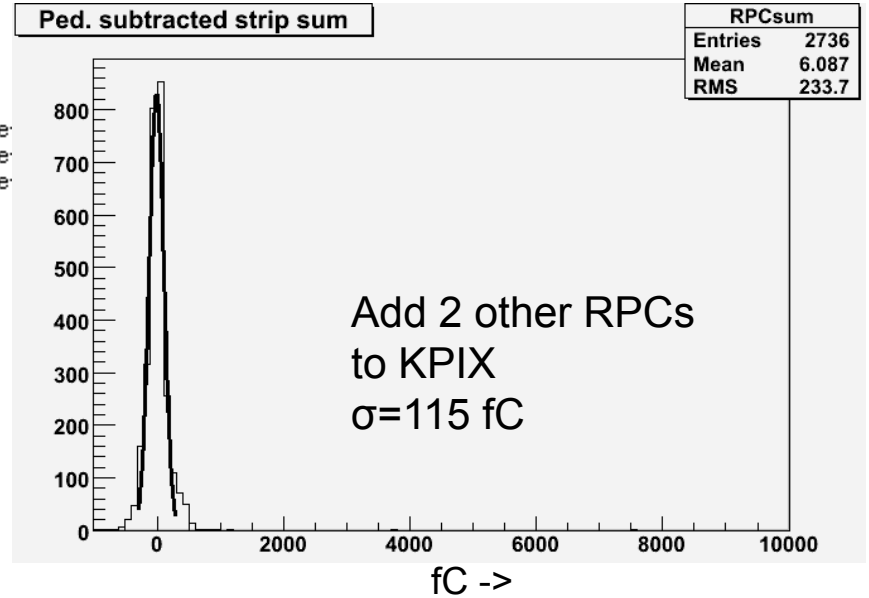
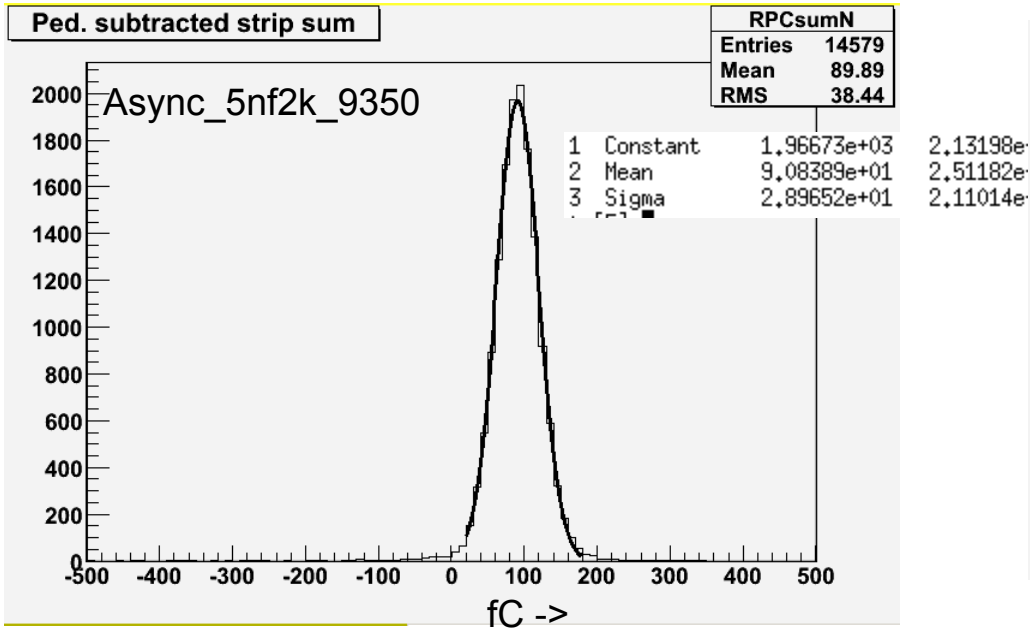
DC resets

per resets



In normal LC operation the KPIX charge amp is reset between beam pulses every 400ns  
 A continuous DC reset mode was added for cosmic rays tests  
 However, the noise seems 2-3 times worse in DC mode  
 Noise 8 times worse with no reset

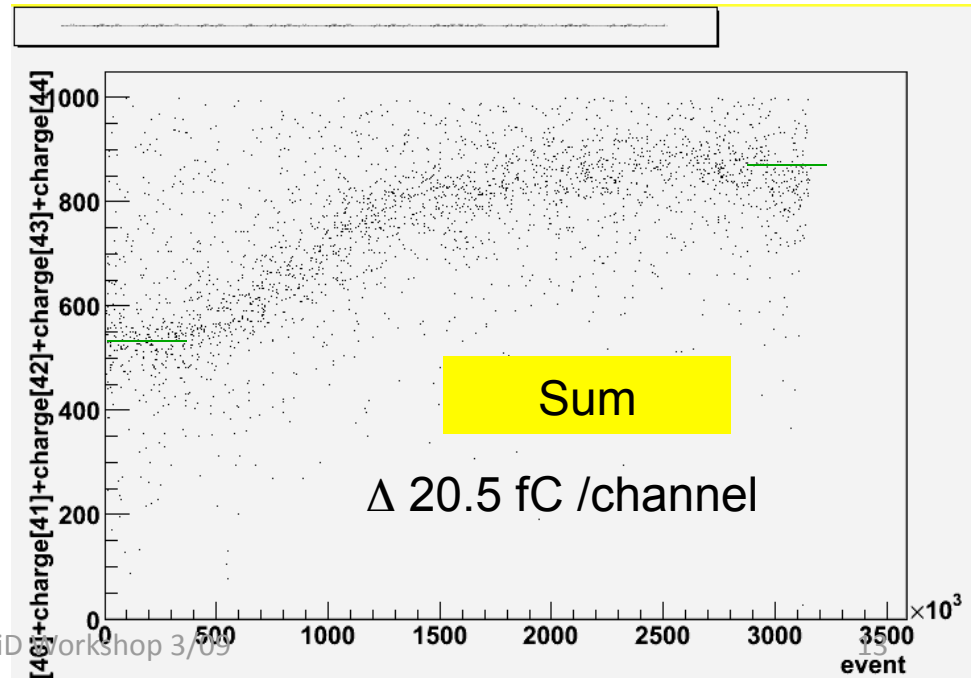
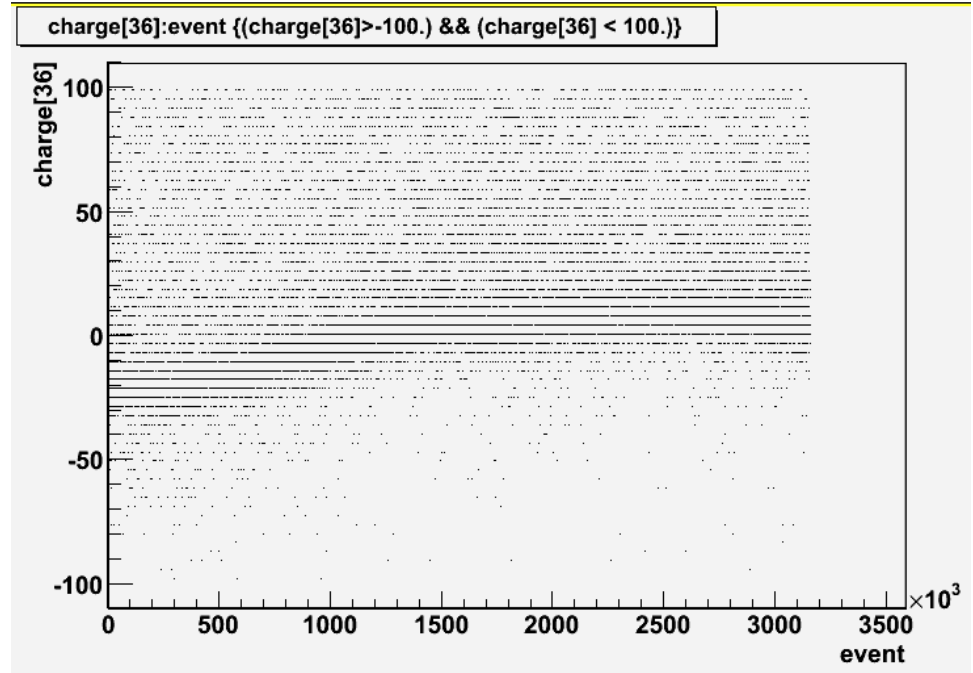
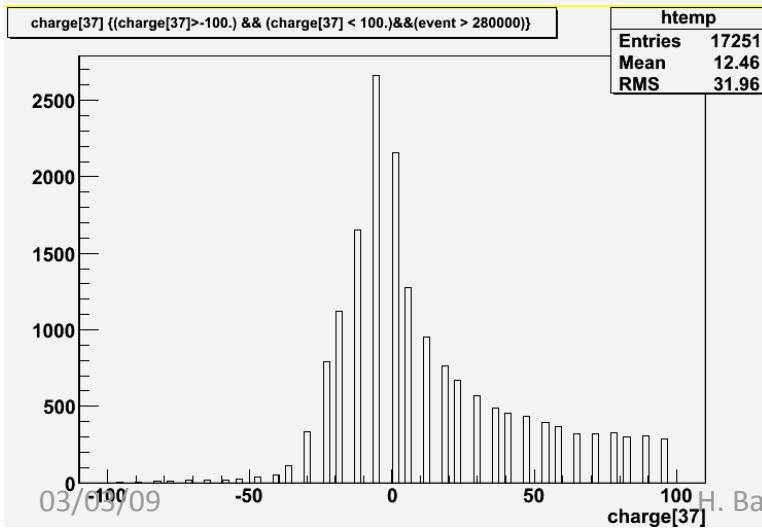
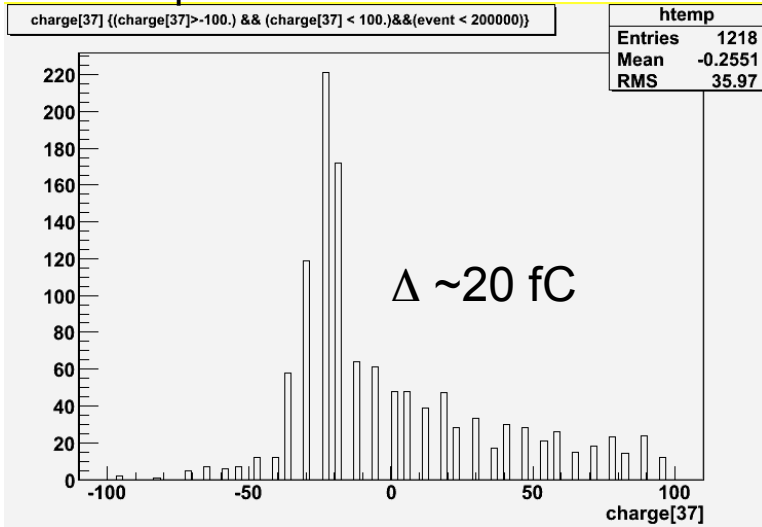
# Pedestal Studies



- Pedestal width higher for triggered events even if no track is seen
- Possible correlation between the time of the trigger and the reset
- Adding multiple RPCs increases the overall noise
- Without cables - noise  $\sim 5$  fC/channel
- Need to better understand grounding

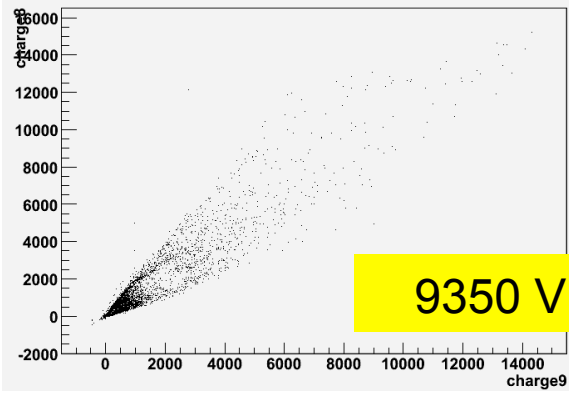
# Pedestal Drift

Overnight run shows pedestal drift with time, presumably due to temperature



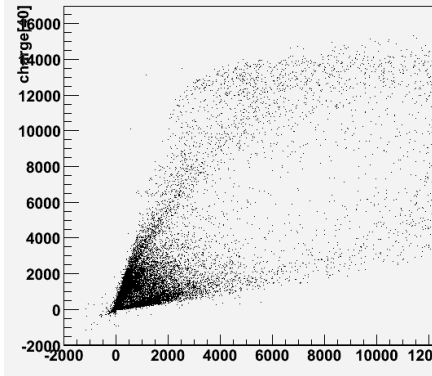
# Compare correlations

charge8:charge9



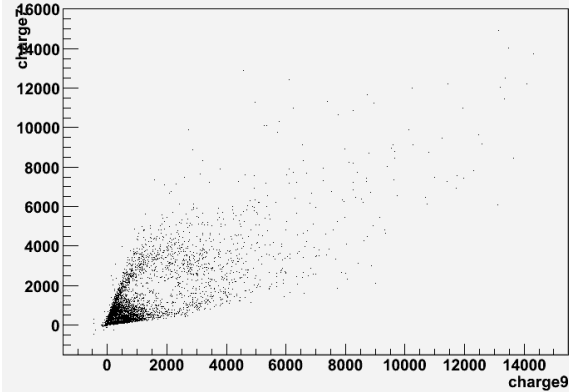
9350 V

charge[40]:charge[41]

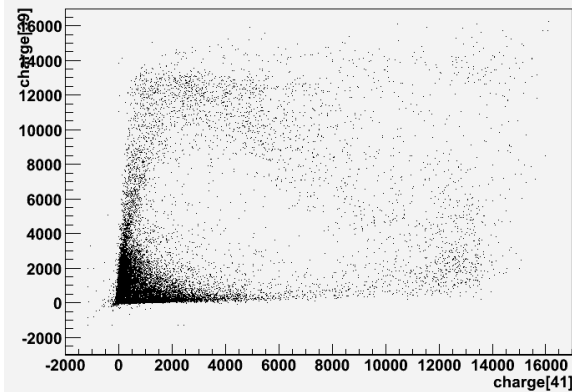


9350 V + 1  
MΩ

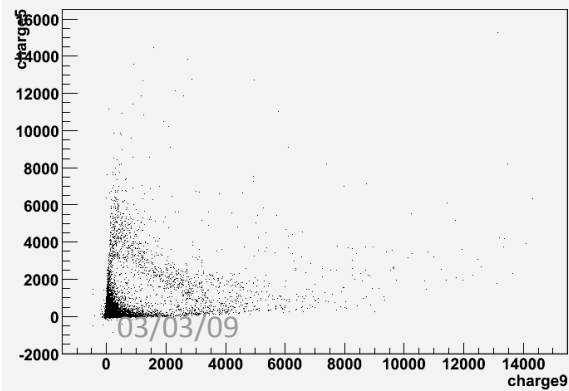
charge7:charge9



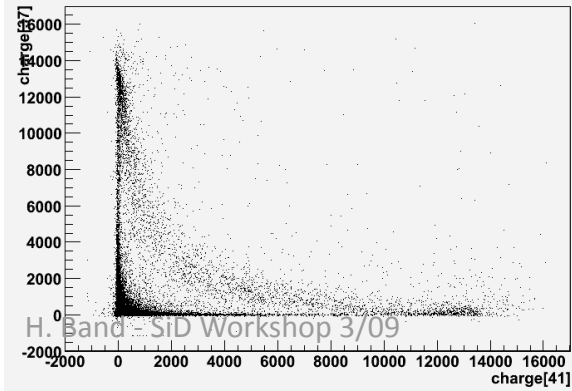
charge[39]:charge[41]



charge5:charge9



charge[37]:charge[41]



# Recent developments

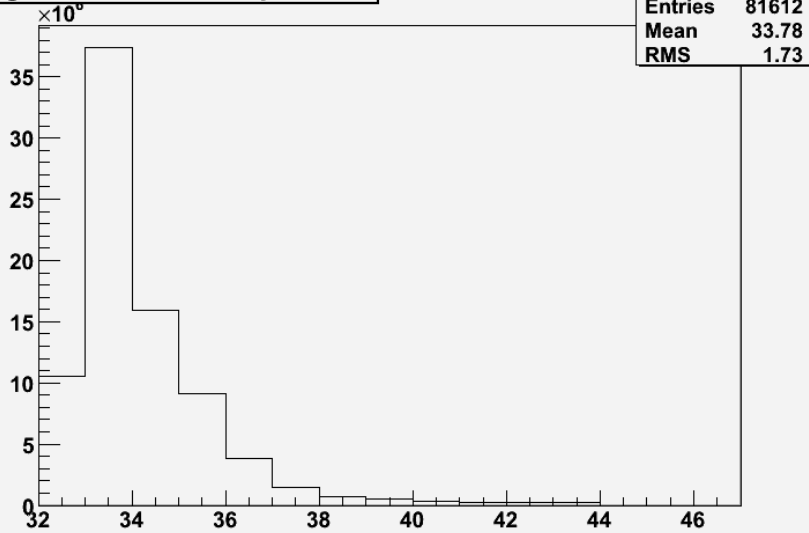
- Use all 64 channels for readout of 4 RPC planes to enable tracking studies
- 7 new IHEP RPCs in hand of varying bulk resistivity  $3 \cdot 10^{11} - 5 \cdot 10^{12} \Omega\text{cm}$
- Improved software -GUI interface

The screenshot displays the KPIX software interface, which is divided into several functional areas:

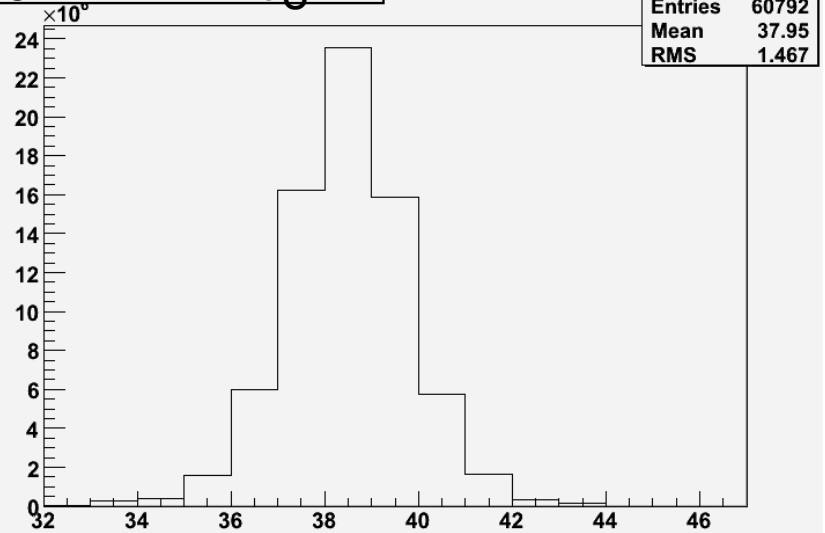
- Main Menu:** Includes tabs for Main, FPGA, Config, Timing, Trigger, and Inject.
- KPIX List:** A table listing KPIX channels with columns for Address, Serial, Version, and Pos Pixel.
- Description:** A text area for entering a base run file for asynchronous triggers.
- Run Variables:** A table for defining run variables with columns for Name, Value, and Description.
- Data Directory:** A field for specifying the data directory path.
- Calibration & Settings File:** A field for specifying the calibration and settings file path.
- Control Panel:** Contains buttons for Register Test Menu, Calibration Menu, Thresh Scan Menu, Run Menu, Read Counters, Clear Counters, Read Configuration, Write Configuration, Set Defaults, Dump Settings, and checkboxes for enabling calibration changing settings and various debug options (KpixAsic, KpixFpga, SidLink).
- Histograms:** Four charge histograms are displayed, each for a different channel (Channels 41, 42, 40, and 56). The histograms show the distribution of charge values for each channel.
- Channels To Display:** A list of channels from Kpix 0 - Ch 41 to Kpix 0 - Ch 56, with a dropdown menu currently set to Kpix 0 - Ch 56.
- Event Variables:** A table for defining event variables with columns for Name, Value, and Description.
- Control Panel (Right):** Includes checkboxes for enabling raw data storage, channel distribution plots, and network control. It also features fields for Iterations (5997) and Rate (28), a Network Port dropdown (8888), a Triggers field (5997), a Run Command dropdown (Calibrate), and buttons for Run, Stop, Pause, and View Run Data.
- Status Bar:** Shows the current data directory path and the status "Running - Low Gain".

# Strips Above Threshold

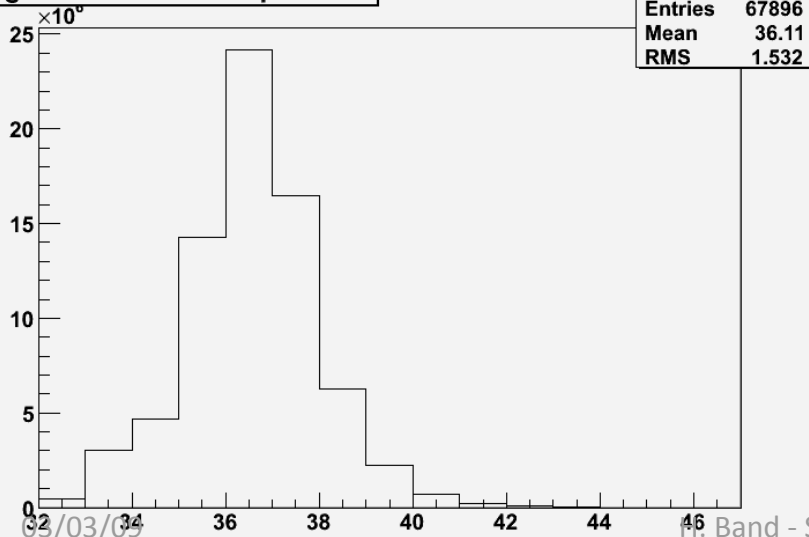
avg of all evts with strip 2 max



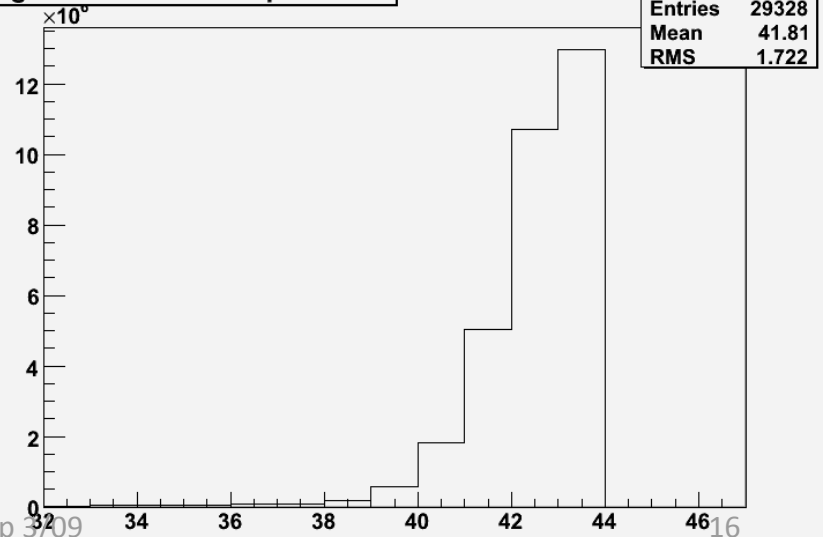
avg of all evts with strip 8 max



avg of all evts with strip 5 max



avg of all evts with strip 11 max





- *FY2009 Milestones:*
  - *Relocate test-stand*
  - *Make current, rate, and efficiency measurements of IHEP test RPCs operating in avalanche mode.*
  - *Readout multiple RPCs with 1 KPiX(v. 7) chip*
  - *Readout negative RPC signals with KPiX(v. 7)*
  - *Test KPiX (v. 7 & v. 8) trigger and reset operating modes.*
  - *Optimize RPC/KPiX interface board design to maximize efficiency and minimize strip multiplicity.*

- *FY2010 & FY2011 Milestones:*
  - *Readout multiple KPix chips*
  - *Use position and charge information from multiple RPC/KPAX devices to make fitted cosmic ray tracks*
  - *Study position resolution of RPC/KPAX tracks,*
  - *Test HCAL prototypes in teststand*
  - *Study response on IHEP RPCs to HF.*
  - *Begin IHEP RPC aging studies*