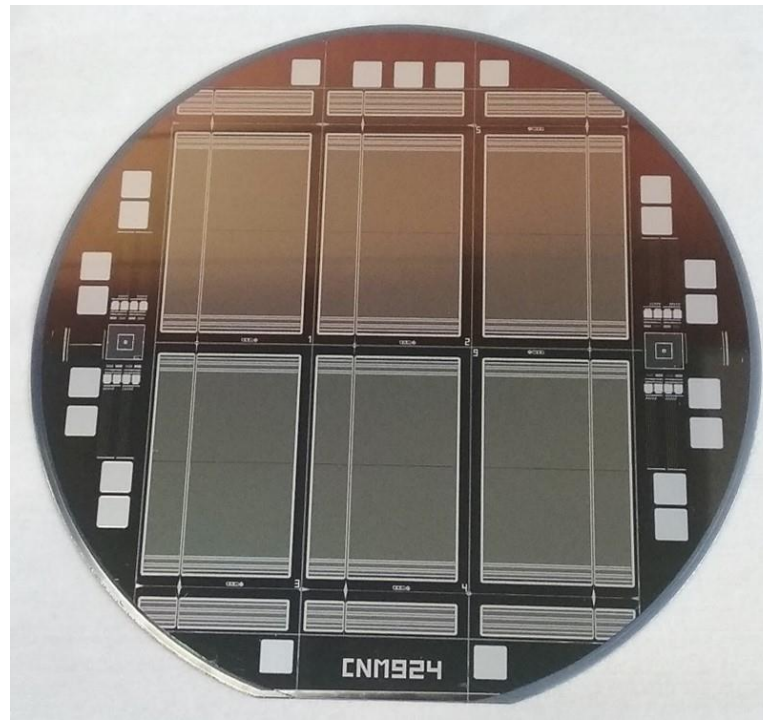


# Preliminary characterization of microstrip detectors for HPS experiment



# Characterization on wafer before dicing



	Pinholes				Pinholes				Pinholes				
	Sector A	Sector B			Sector A	Sector B			Sector A	Sector B			
<b>W01</b>													
	S1	broken		W06	S1	0	0		W11	S1	0	0	
	S2	87	0		S2	0	0			S2	68	30, 31	
	S3	broken			S3	0	0			S3	65, 66		
	S4	0	0		S4	221	0			S4	146		
	S5	97, 99, 14, 100	91		S5	broken				S5	n.m.	n.m.	
	S6	0	0		S6	0	169, 170			S6	193, 196	45	
<b>W02</b>													
	S1	0	0		W07	S1	0	0		W12	S1	0	216, 234, 235
	S2	121, 177, 118, 176				S2	0	0			S2		17, 30, 31
	S3	0	0			S3	0	176			S3	0	0
	S4	0	0			S4	0	0			S4	39, 48, 49, 64, 77, 78	0
	S5	97, 99, 100	0			S5	n.m.	n.m.			S5	0	0
	S6	0	0			S6	32, 33, 34, 35, 36, 73, 74, 77, 79, 85	0			S6	193, 194, 196	0
<b>W03</b>													
	S1	0	0		W08	S1	170, 171	0		W13	S1	0	0
	S2	0	0			S2	65, 66	0			S2	0	0
	S3	0	0			S3	175, 176	0			S3	0	0
	S4	0	0			S4	0	0			S4	0	0
	S5	37, 100	0			S5	n.m.	n.m.			S5	0	0
	S6	0	0			S6	0	0			S6	0	0
<b>W04</b>													
	S1	n.m.	n.m.		W09	S1	45	0		W14	S1	0	0
	S2	n.m.	n.m.			S2	0	0			S2	0	0
	S3	n.m.	n.m.			S3	0	0			S3	97, 99, 108, 109, 110, 111	
	S4	n.m.	n.m.			S4	0	182			S4	0	0
	S5	n.m.	n.m.			S5					S5	n.m.	n.m.
	S6	n.m.	n.m.			S6	0	47			S6	211, 212, 214	
<b>W05</b>													
	S1	0	0		W10	S1	n.m.	n.m.		W15	S1	148	0
	S2	0	0			S2					S2	204, 205	0
	S3	55, 40, 46	0			S3					S3	0	0
	S4	0	0			S4	n.m.	n.m.			S4	201, 202, 203	
	S5	155, 156	0			S5	n.m.	n.m.			S5	0	0
	S6	0	0			S6	n.m.	n.m.			S6	0	0

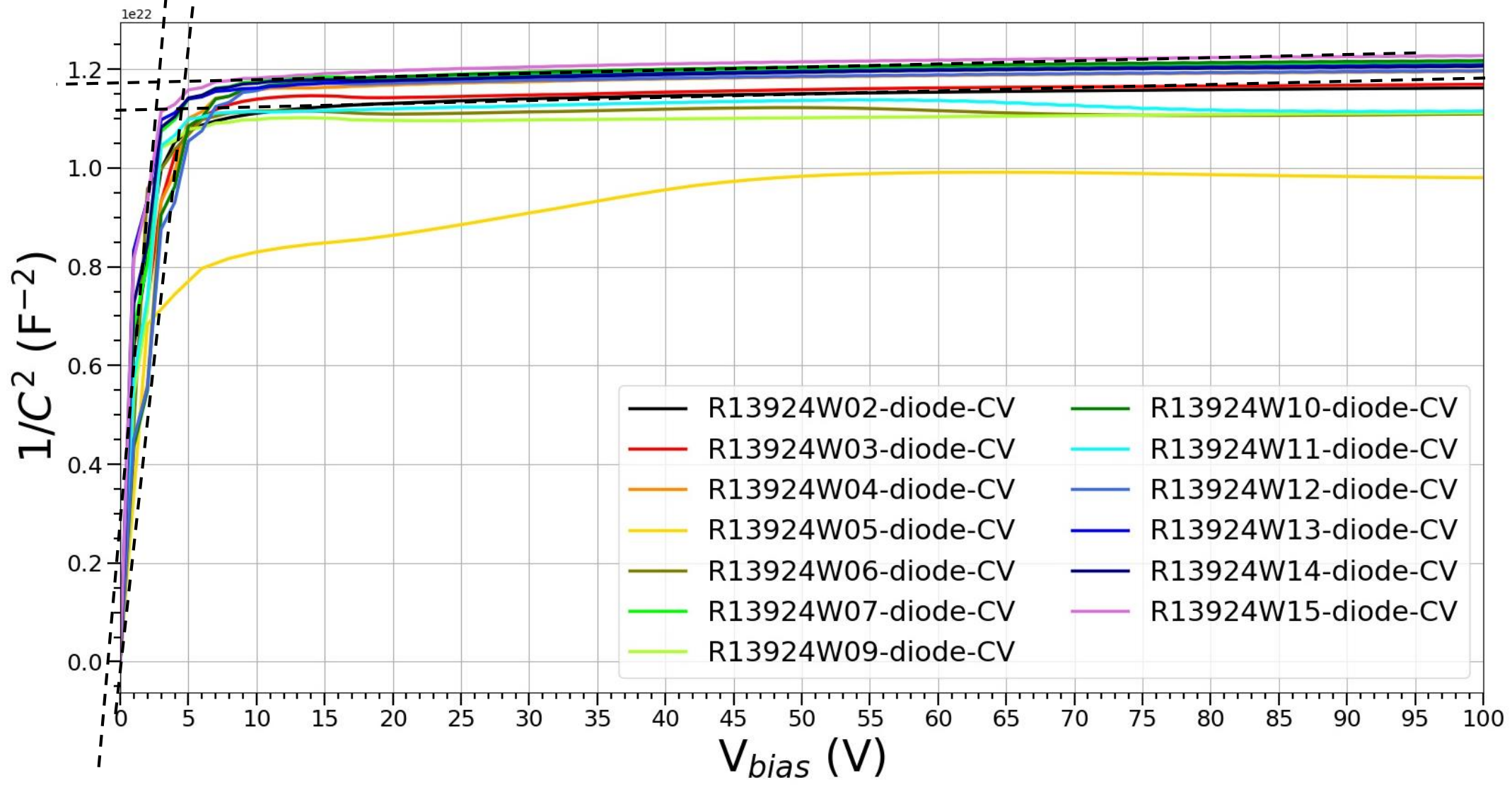
W04 to be characterized

W10 to be characterized

## Coupling capacitance characterization

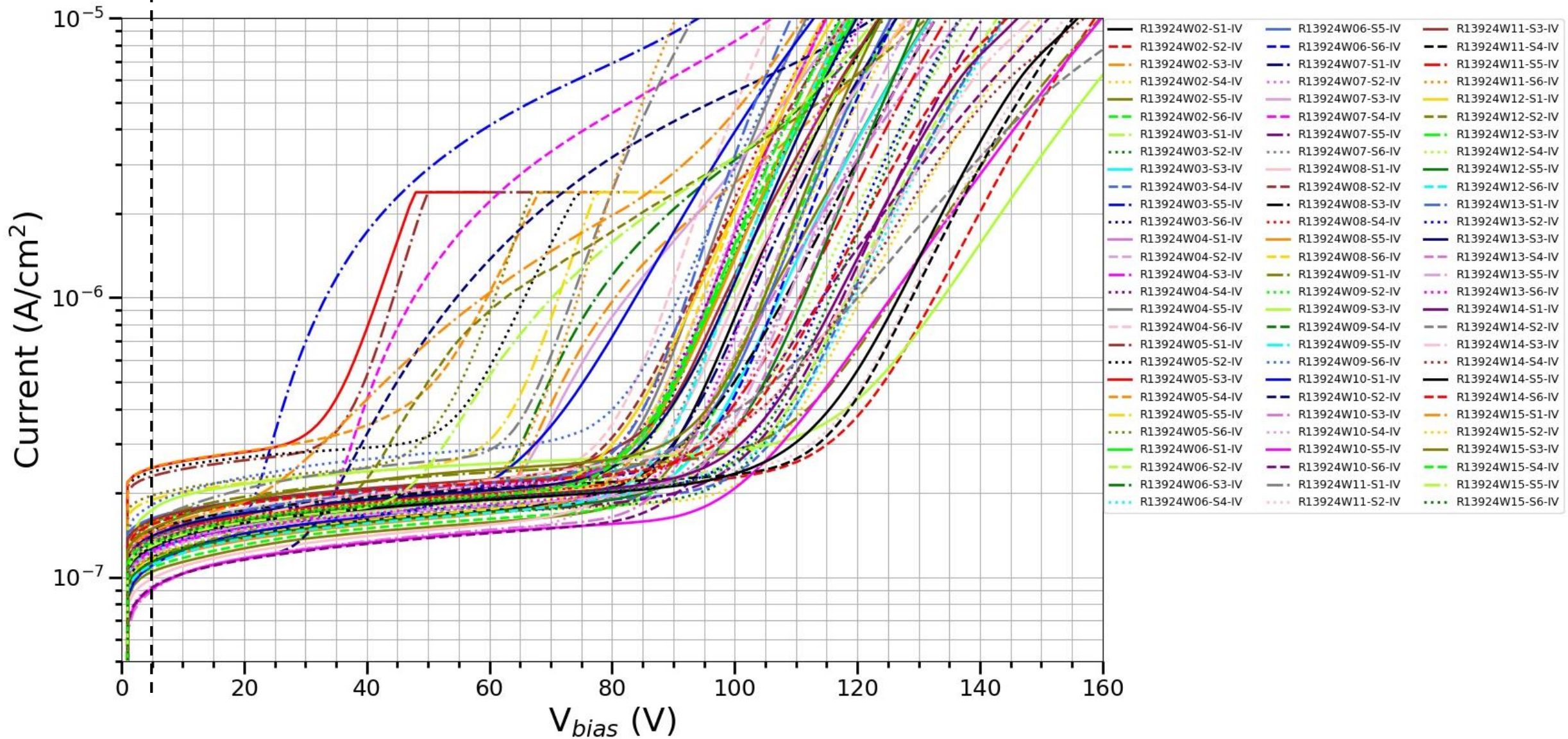
- ✓ Mean value: 48 pF/cm
- ✓ 97% Yield good chips
- ✓ 69 chips with less than 1% of pinholes (total of 512 strips)
- ✓ 16 chips (2 full wafers more 4 single chips) still to be characterized (n.m. not measured)

$1/C^2$  measured on wafers (diodes) before dicing: depletion voltage < 5 V





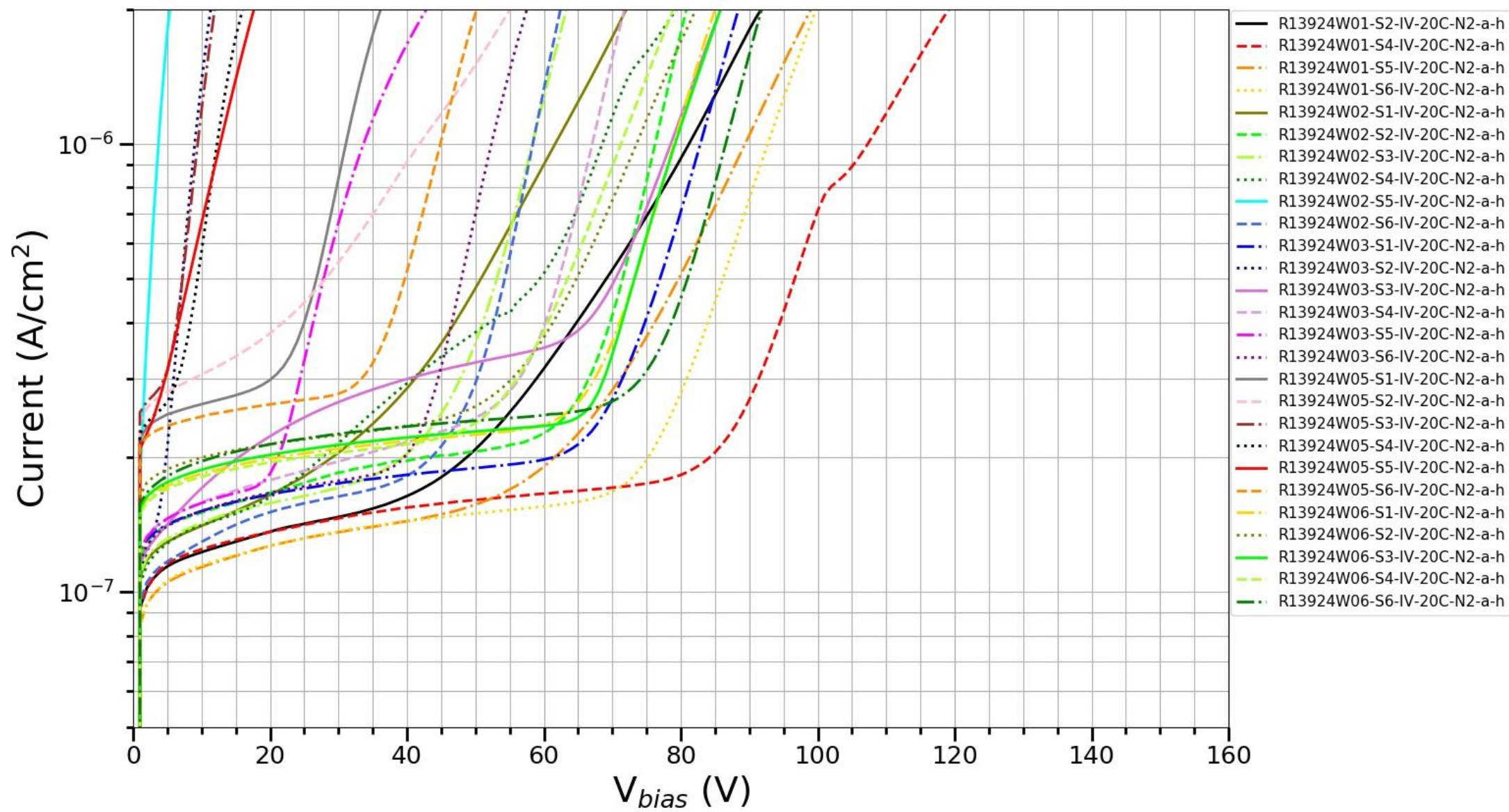
IV characteristic measured on detectors on wafers before dicing



# Sensors characterization after dicing but before cleaving

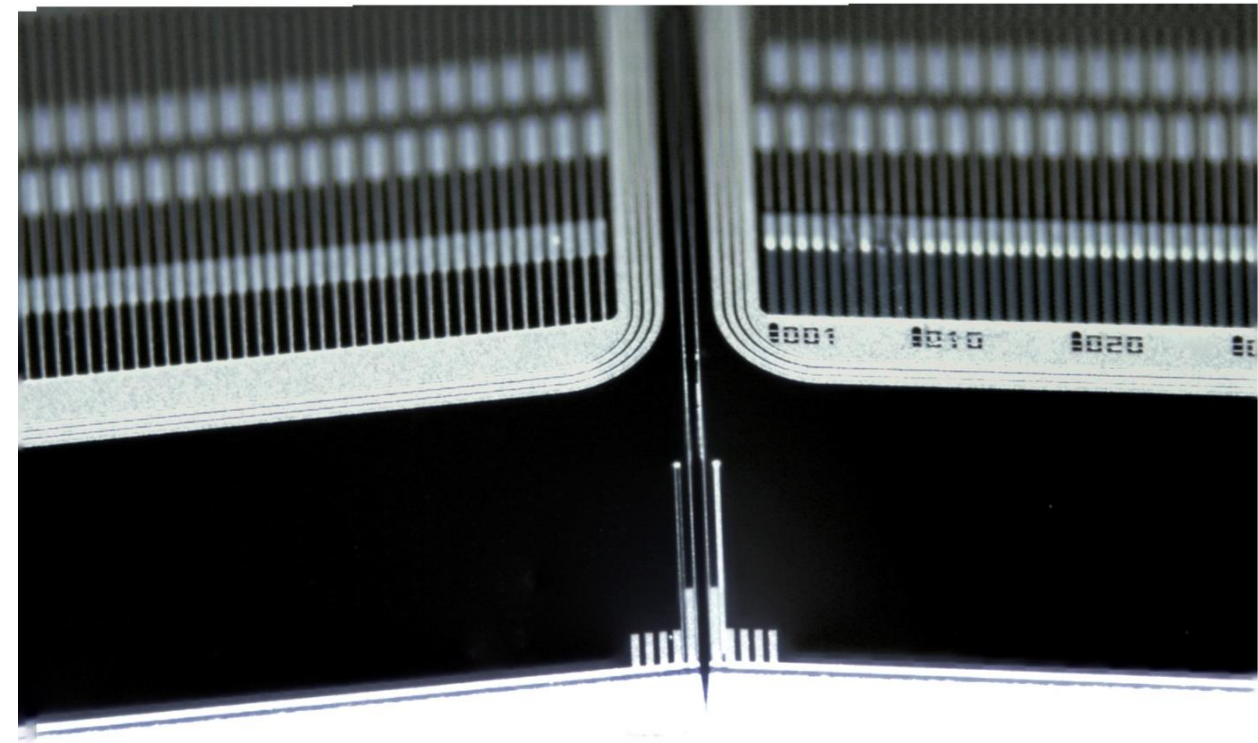
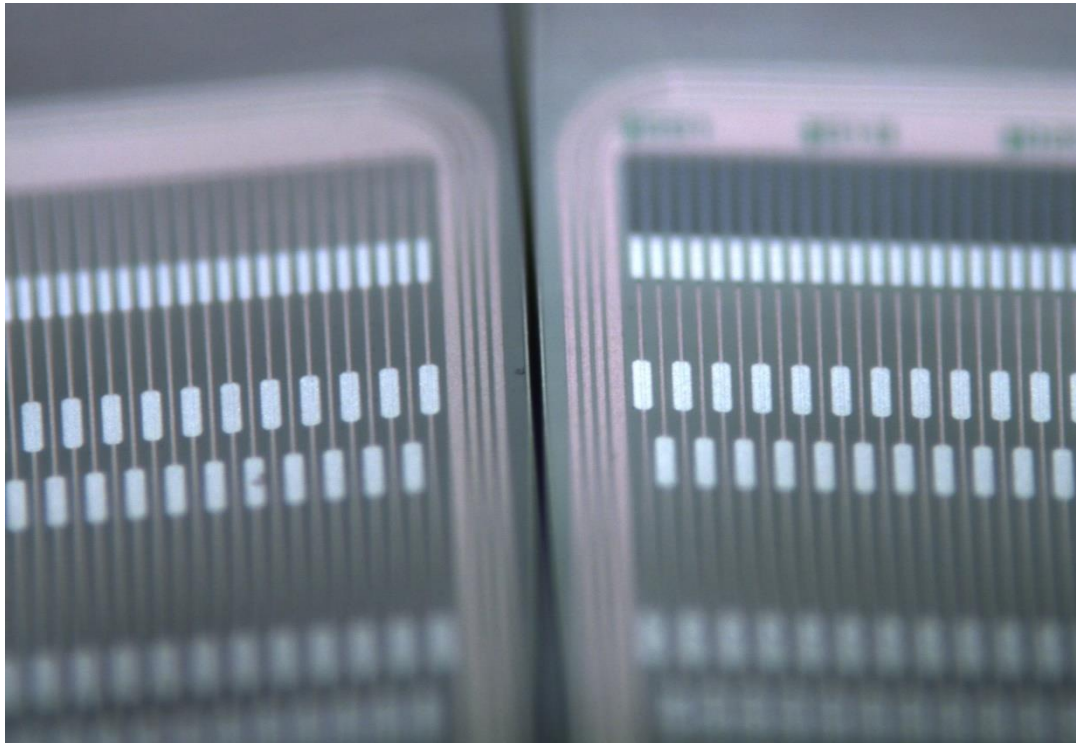




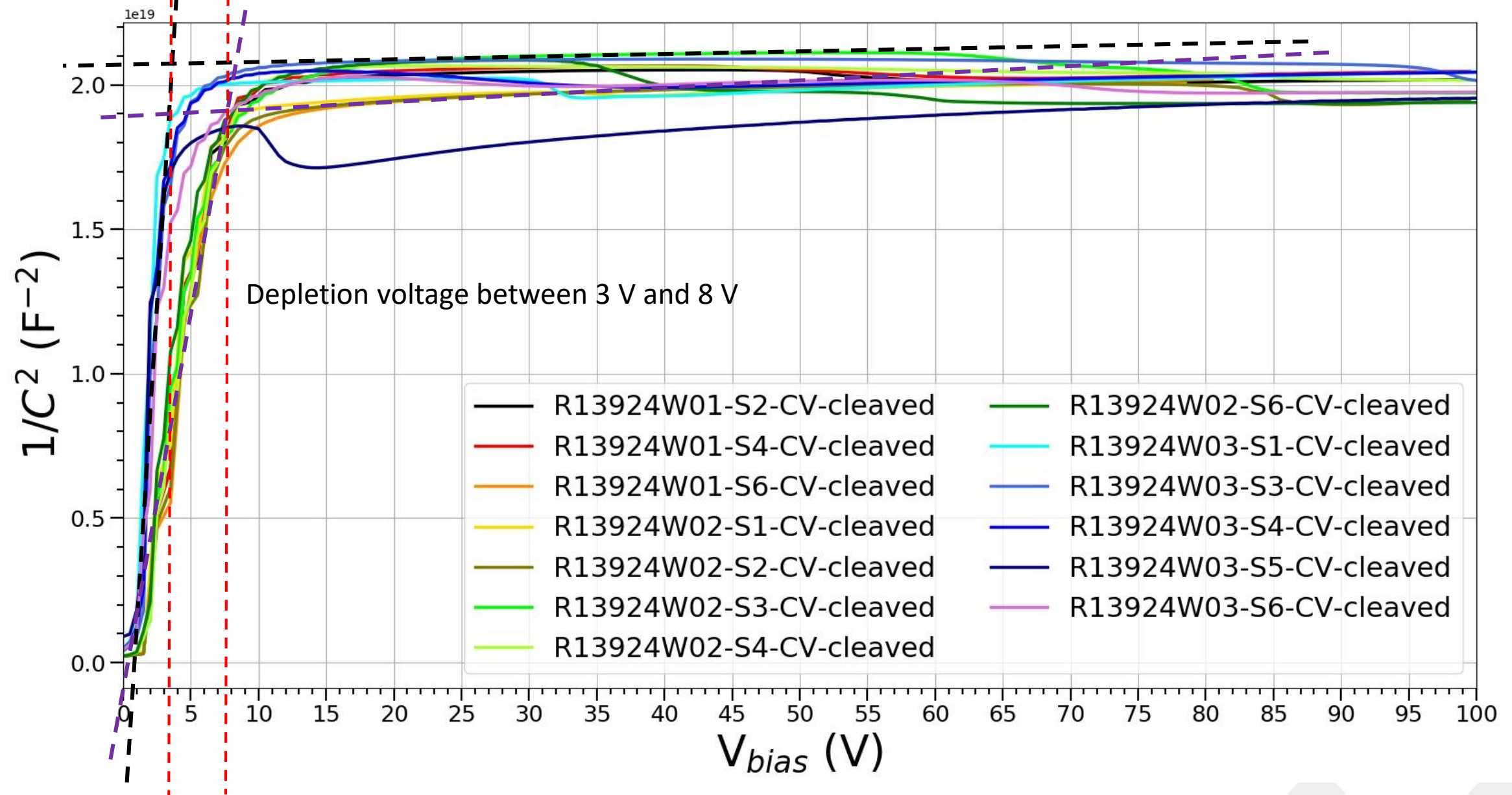


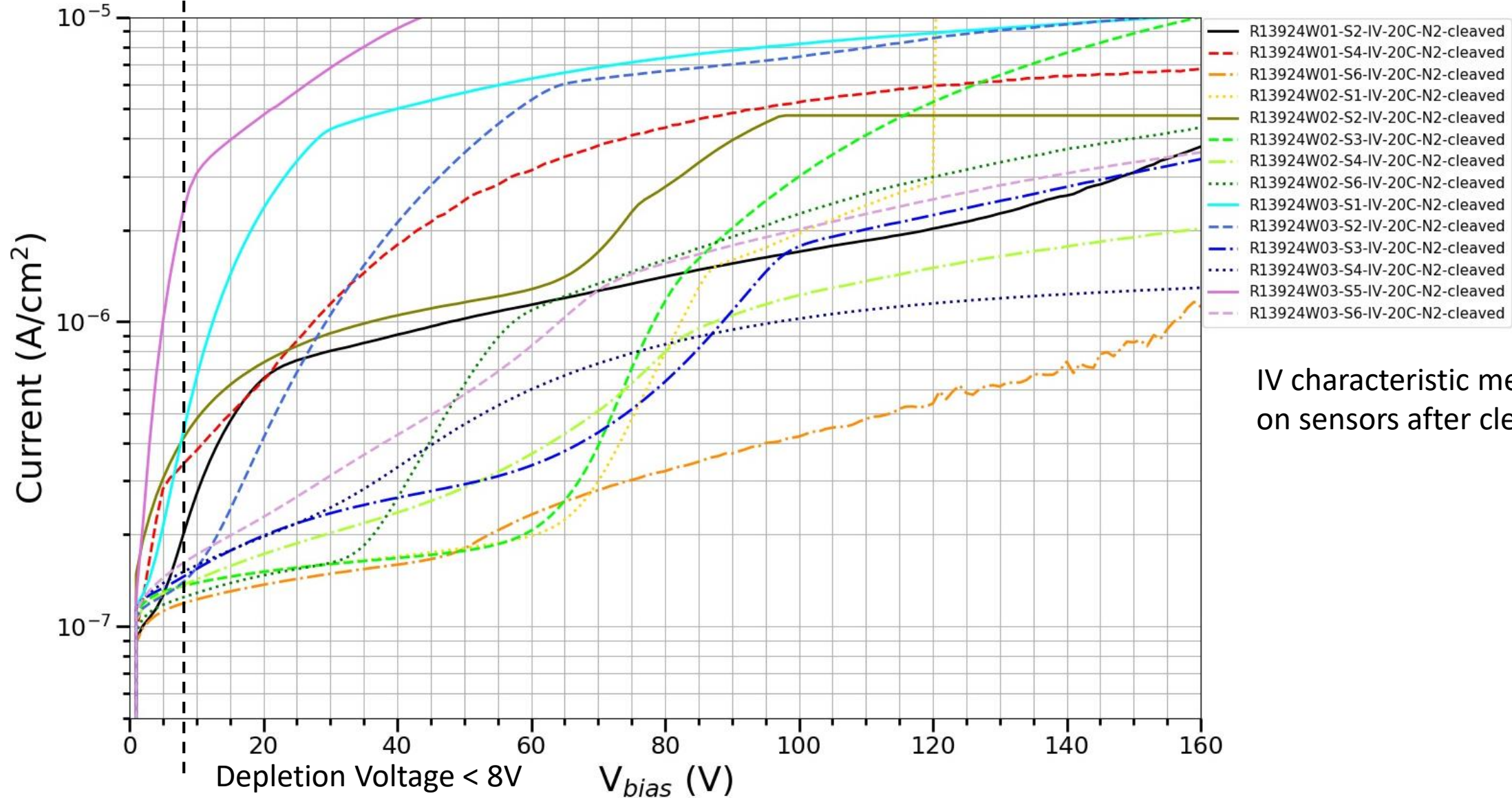
# Characterization after cleaving

Cleaved edge is perfectly along the cleaving path 250  $\mu\text{m}$  far from the active area. Design alignment better than 0,1 degree





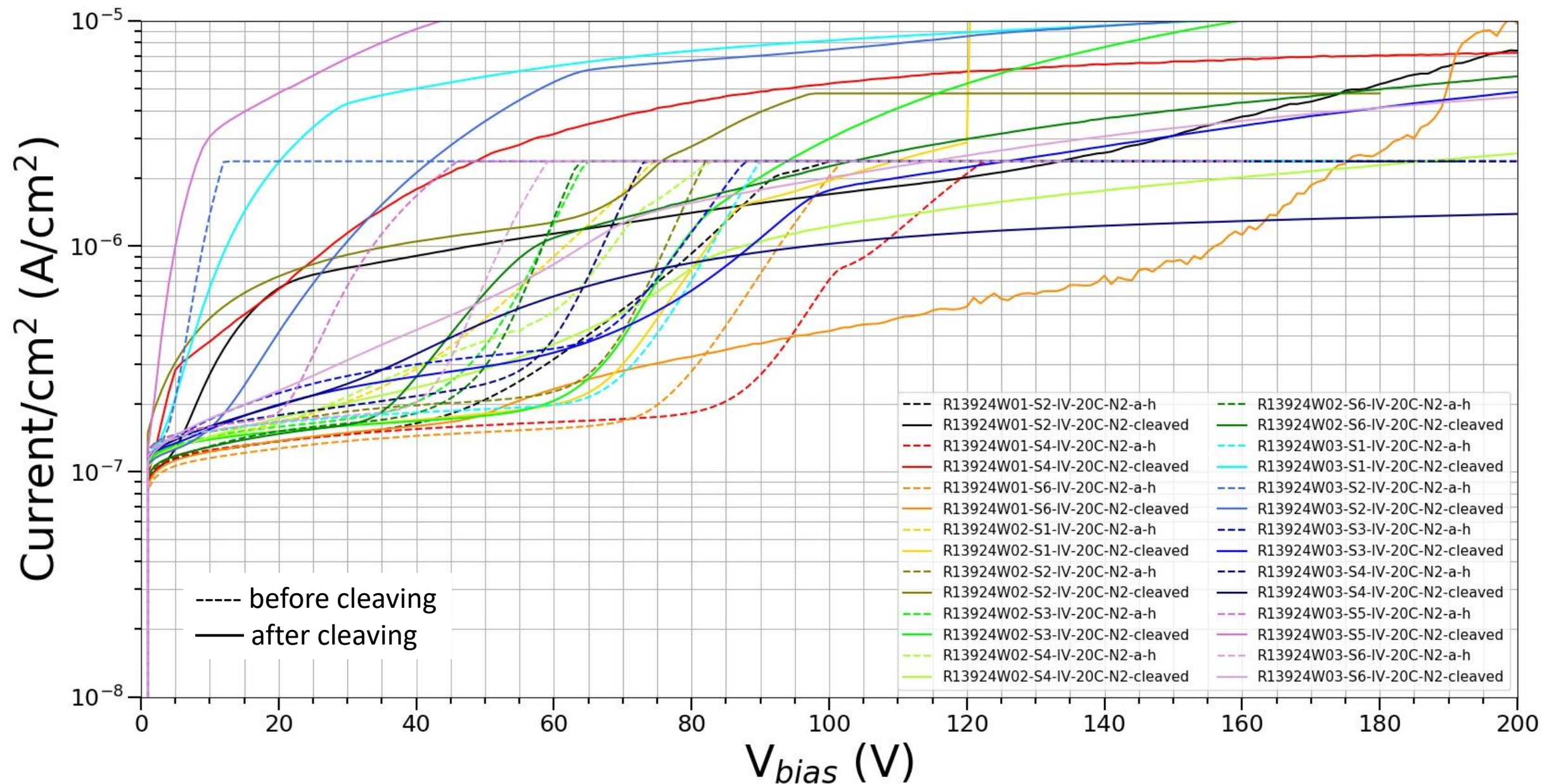


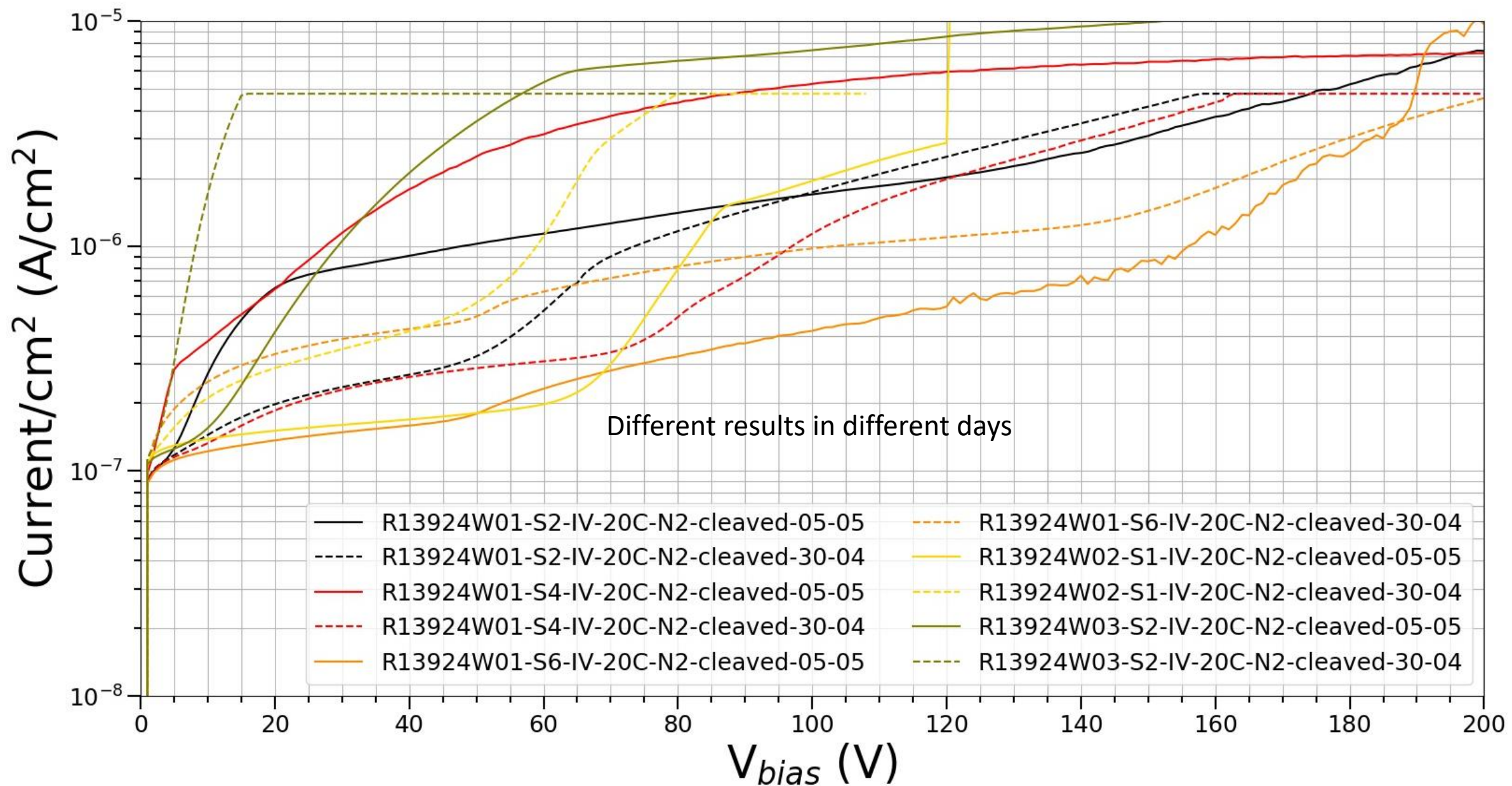


IV characteristic measured on sensors after cleaving



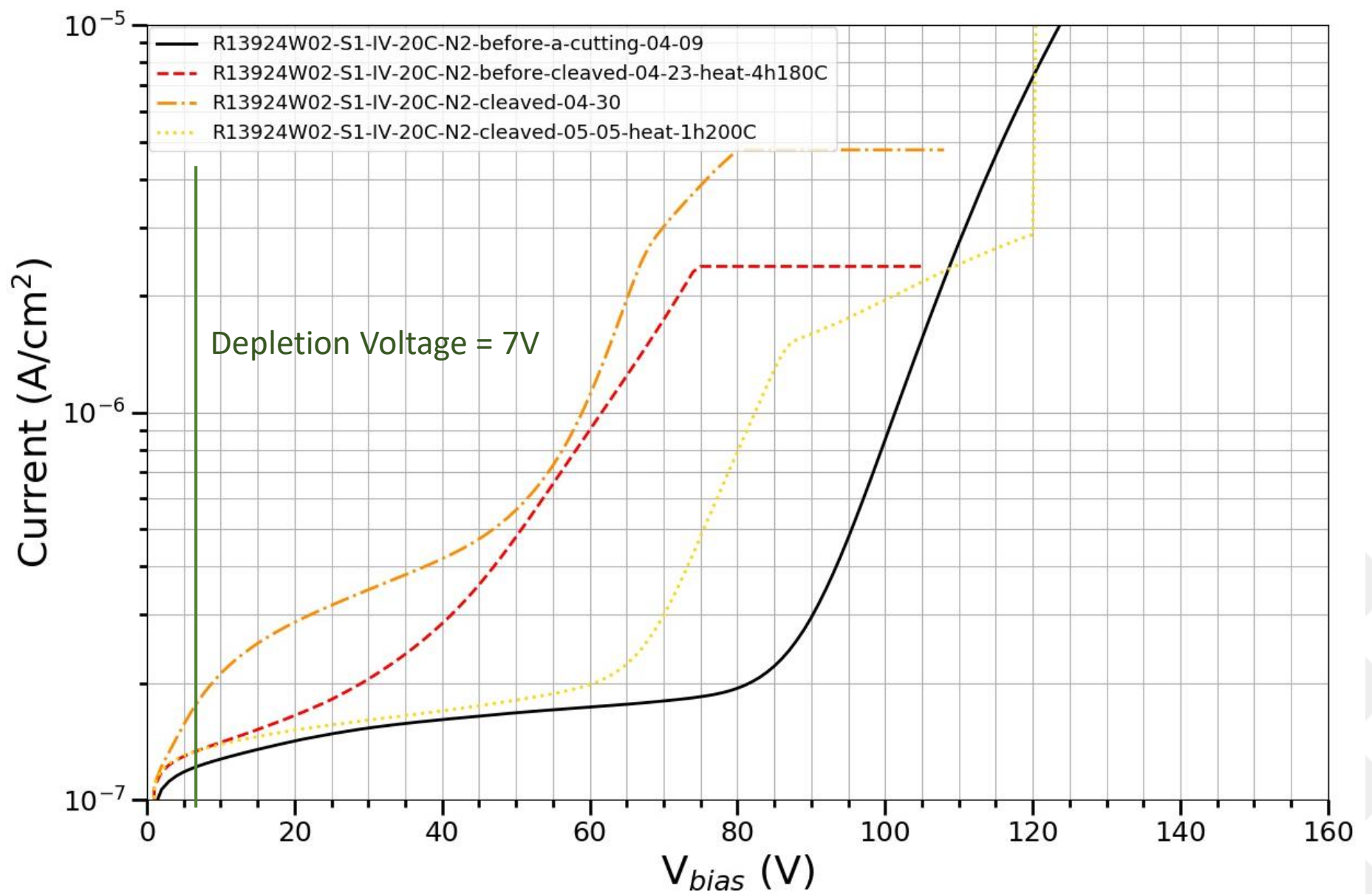




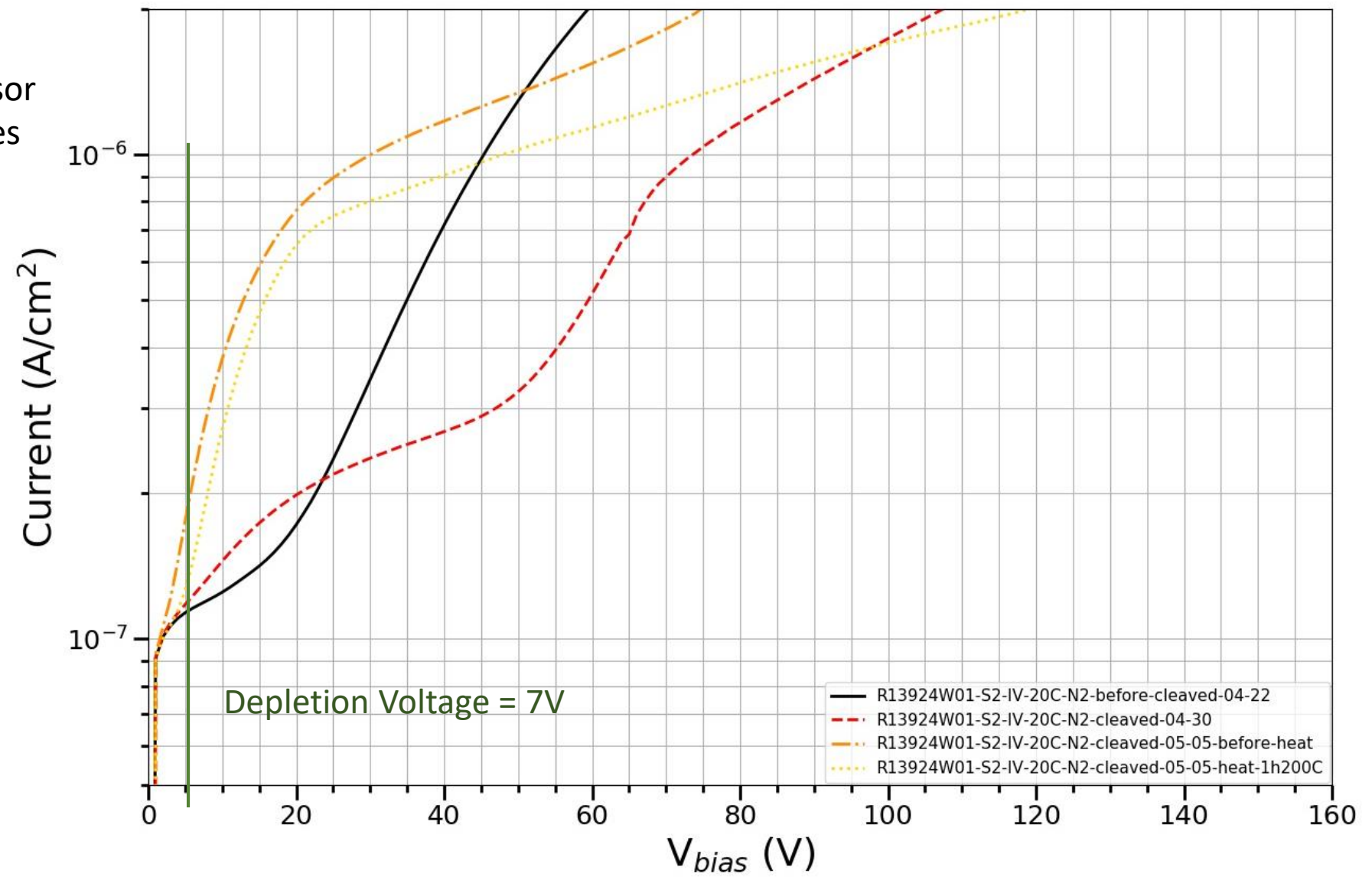




Current evolution in sensor W02 S1 at different stages



Current evolution in sensor W01 S2 at different stages



# Next steps

- Cleaving of the sensors from wafers 5 and 6 (May 11)
- Cleaning, **heating for 24 hours at 200°C** and passivation of the back side of the cleaved sensors (second week of May)
- Electrical characterization of the passivated sensors (3rd week of May)
- Delivery of the cleaved and passivated sensors (third week of May)
- Characterization of the capacitances of wafers 4 and 10 (next week)
- Cut wafers 7, 8, 9, 10, 11, 12, 13, 14, 15 (next week)
- Characterize the diced sensors (next week)
- Cleave and passivate the new sensors for delivery

