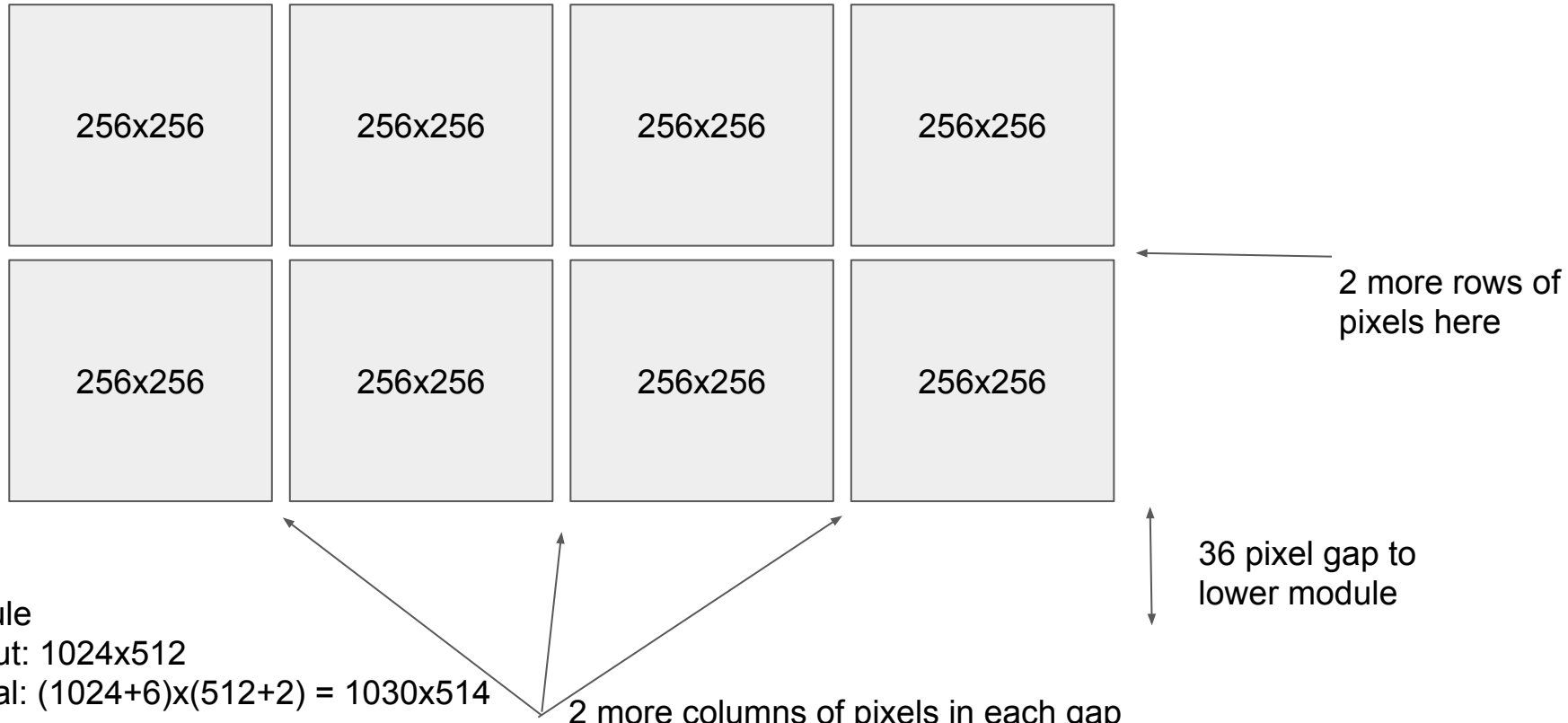
1 module has 8 ASICs in 4x2 arrangement:



1 module
Readout: 1024x512
Physical: (1024+6)x(512+2) = 1030x514

2 modules
Readout: 1024x512 twice
Physical: (1024+6)x((512+2)x2 + 36) = 1030x1064