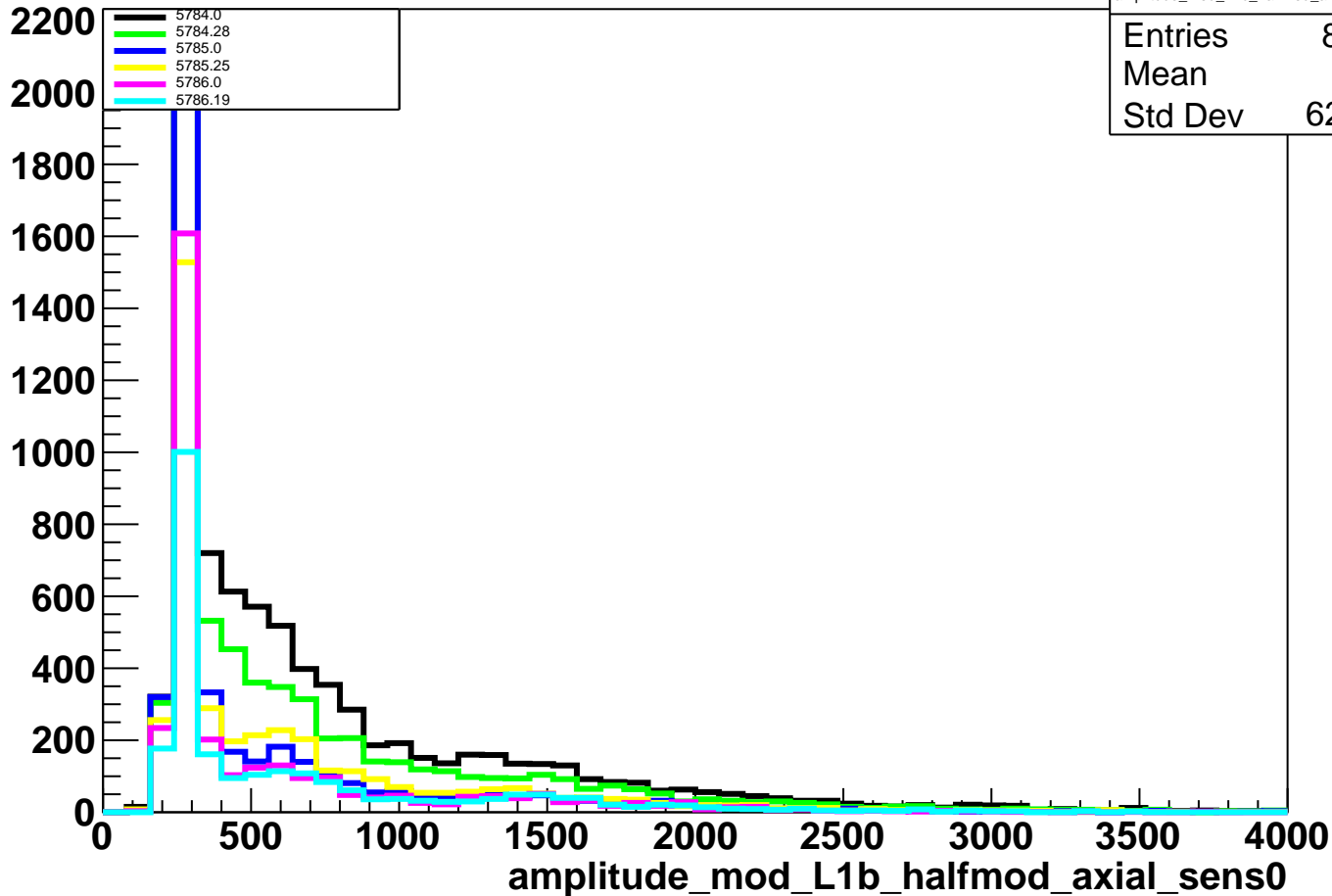


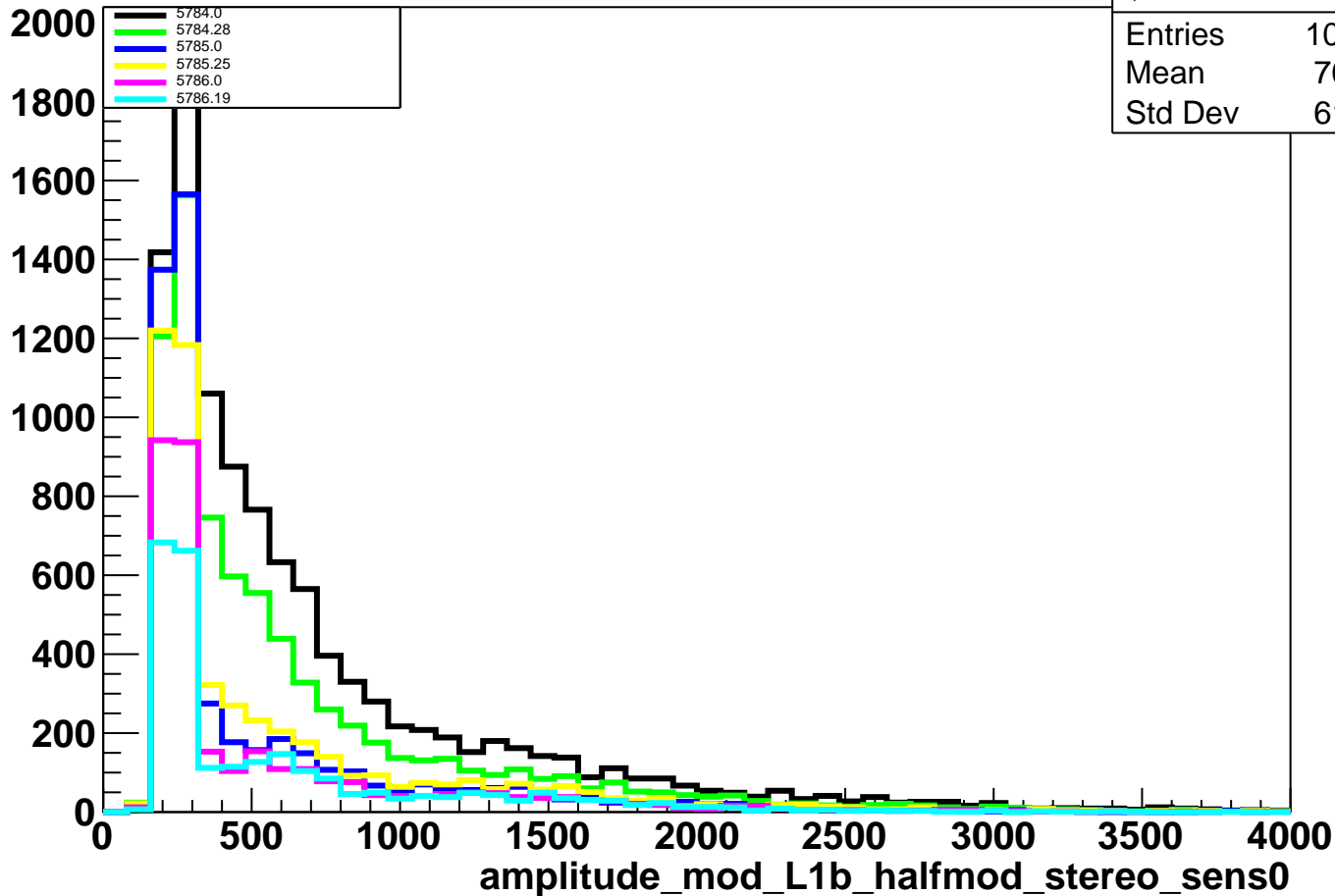
mod_L1b_halfmod_axial_sens0



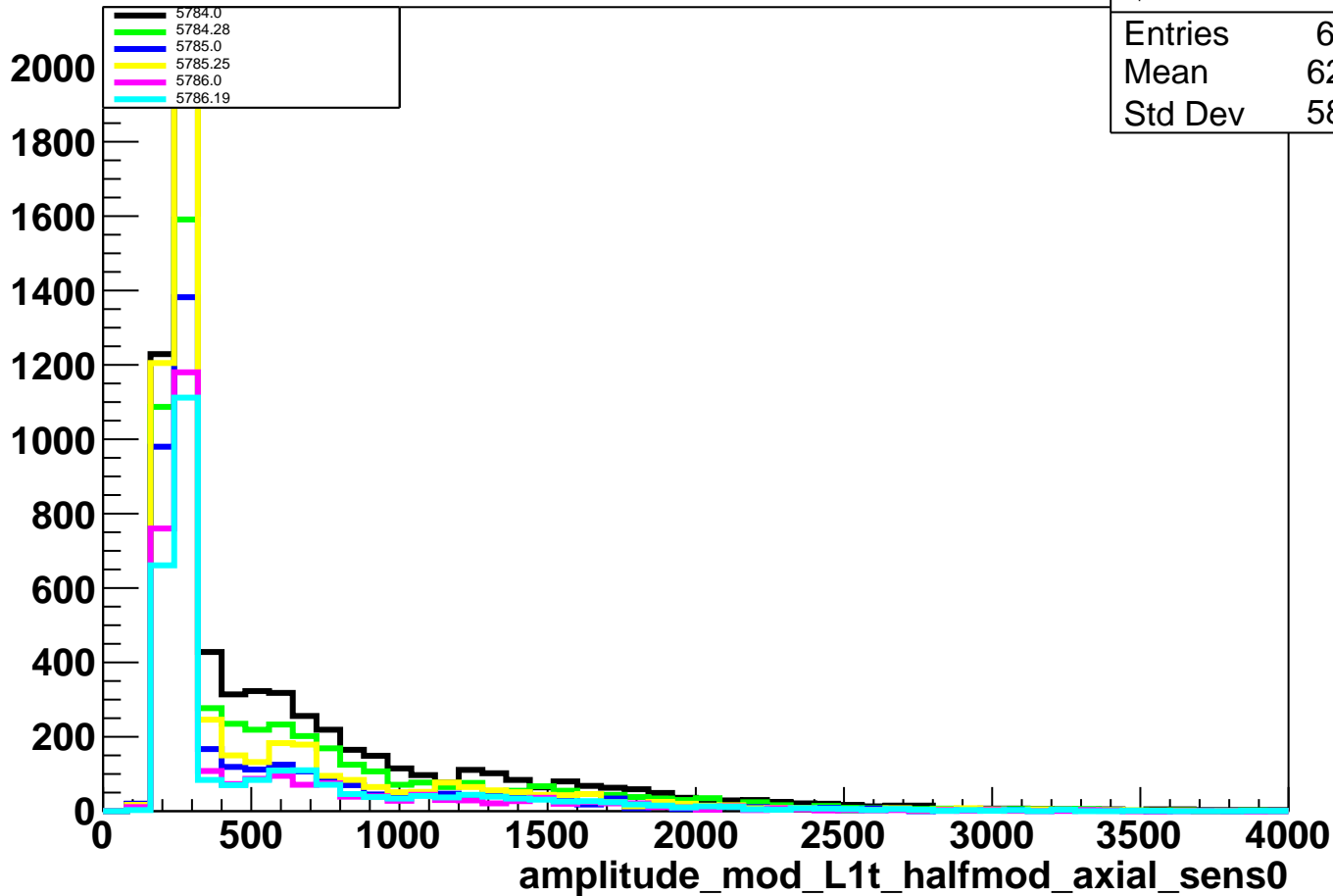
amplitude_mod_L1b_halfmod_axial_sens0

Entries	8141
Mean	748
Std Dev	620.6

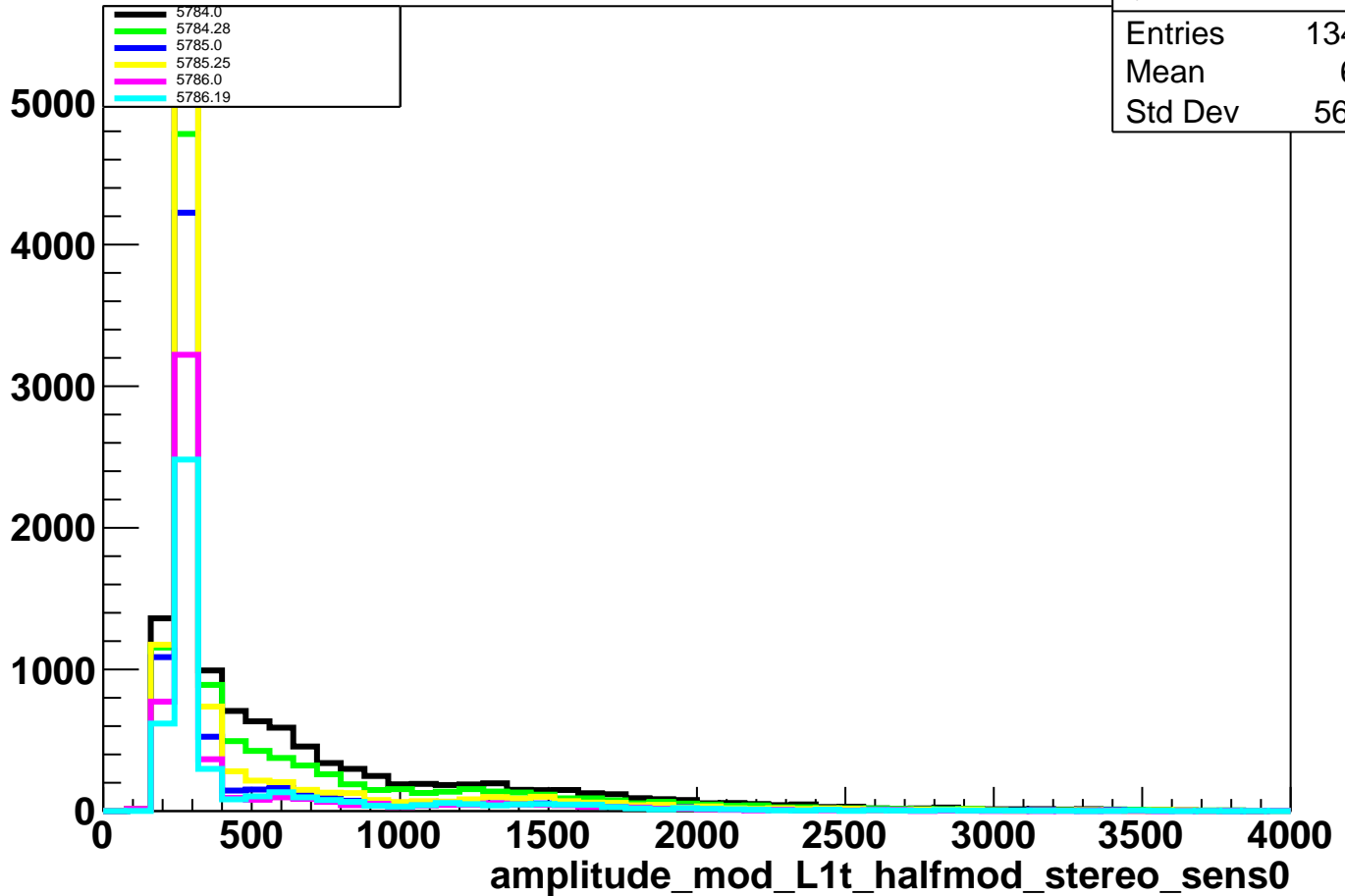
mod_L1b_halfmod_stereo_sens0



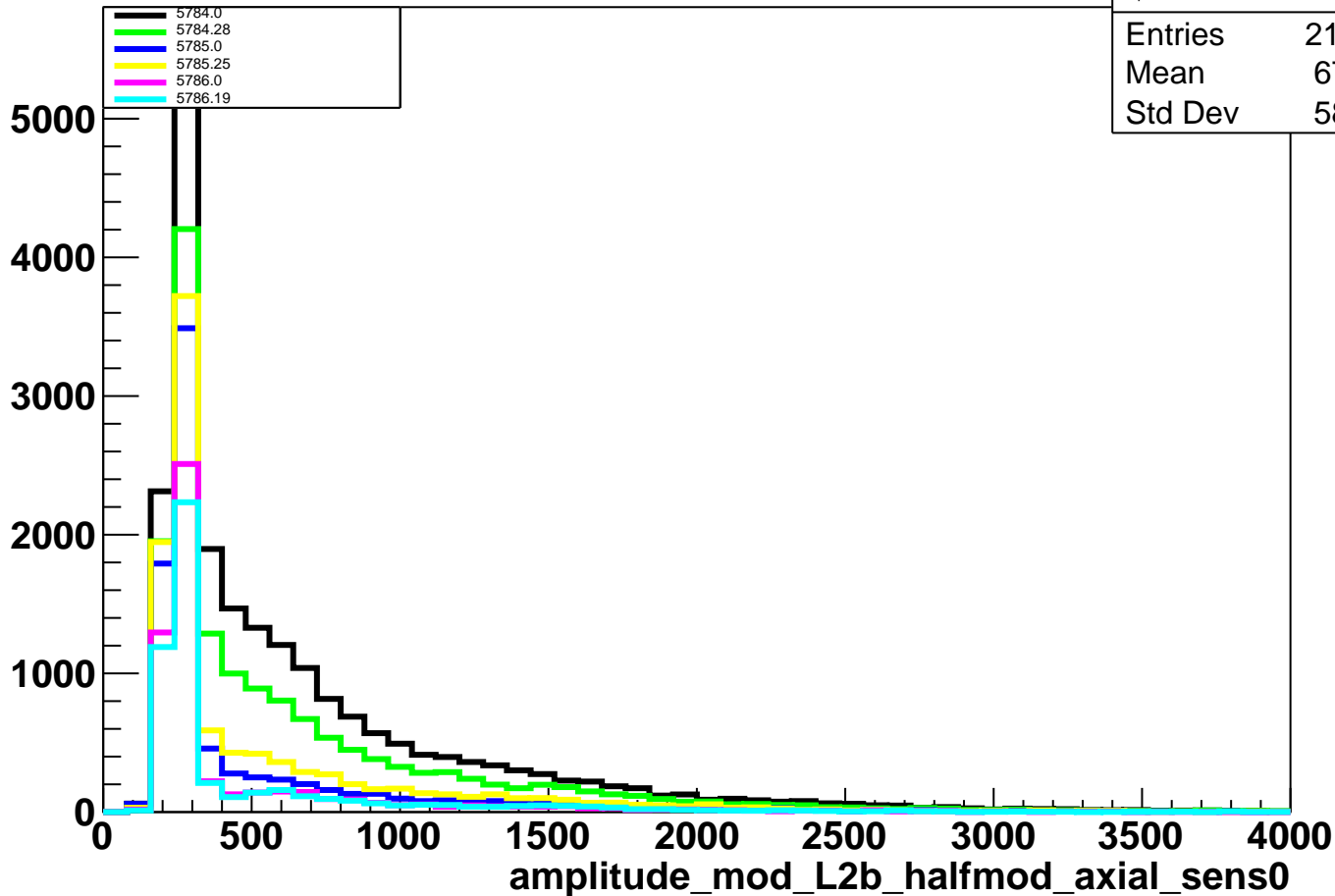
mod_L1t_halfmod_axial_sens0



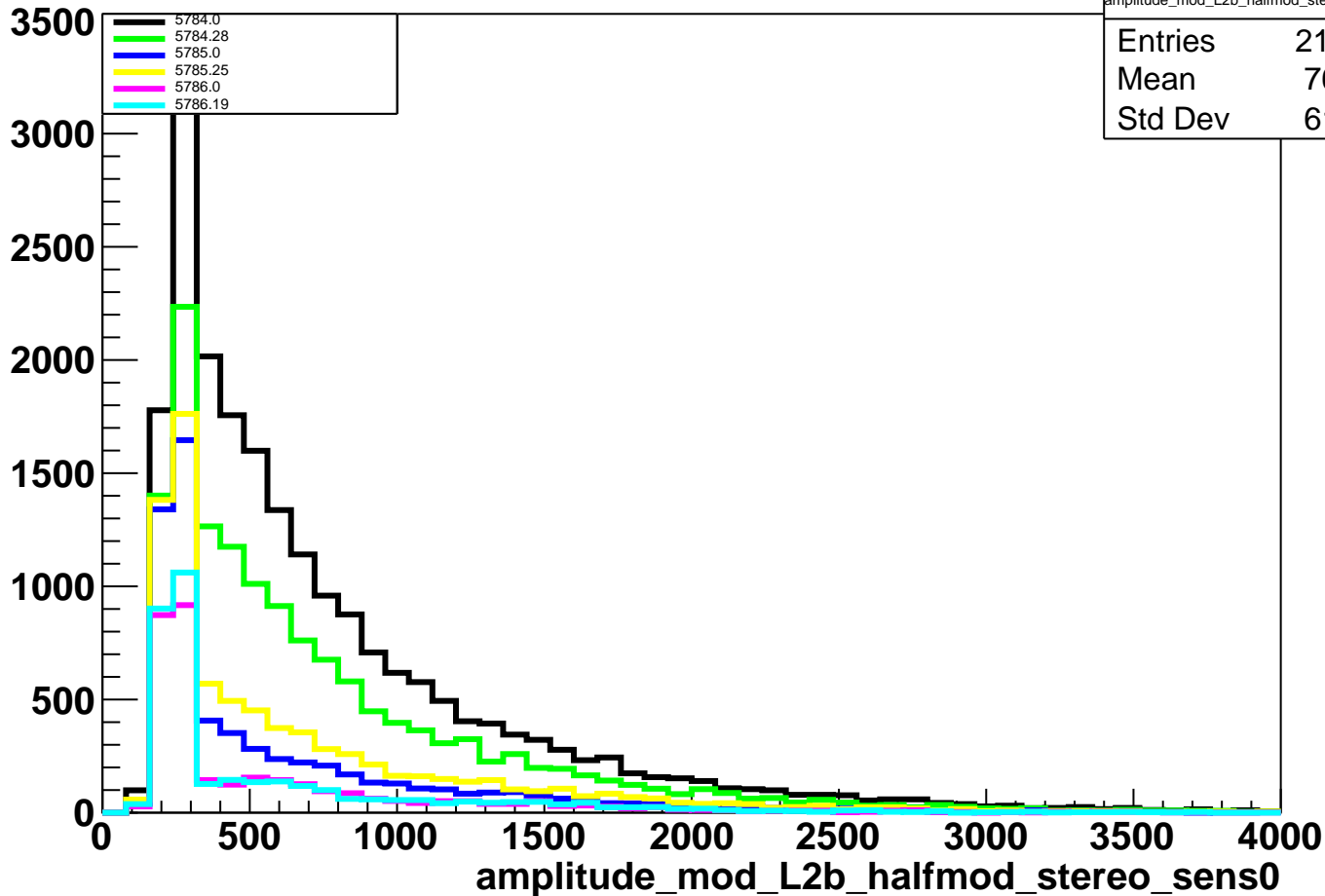
mod_L1t_halfmod_stereo_sens0



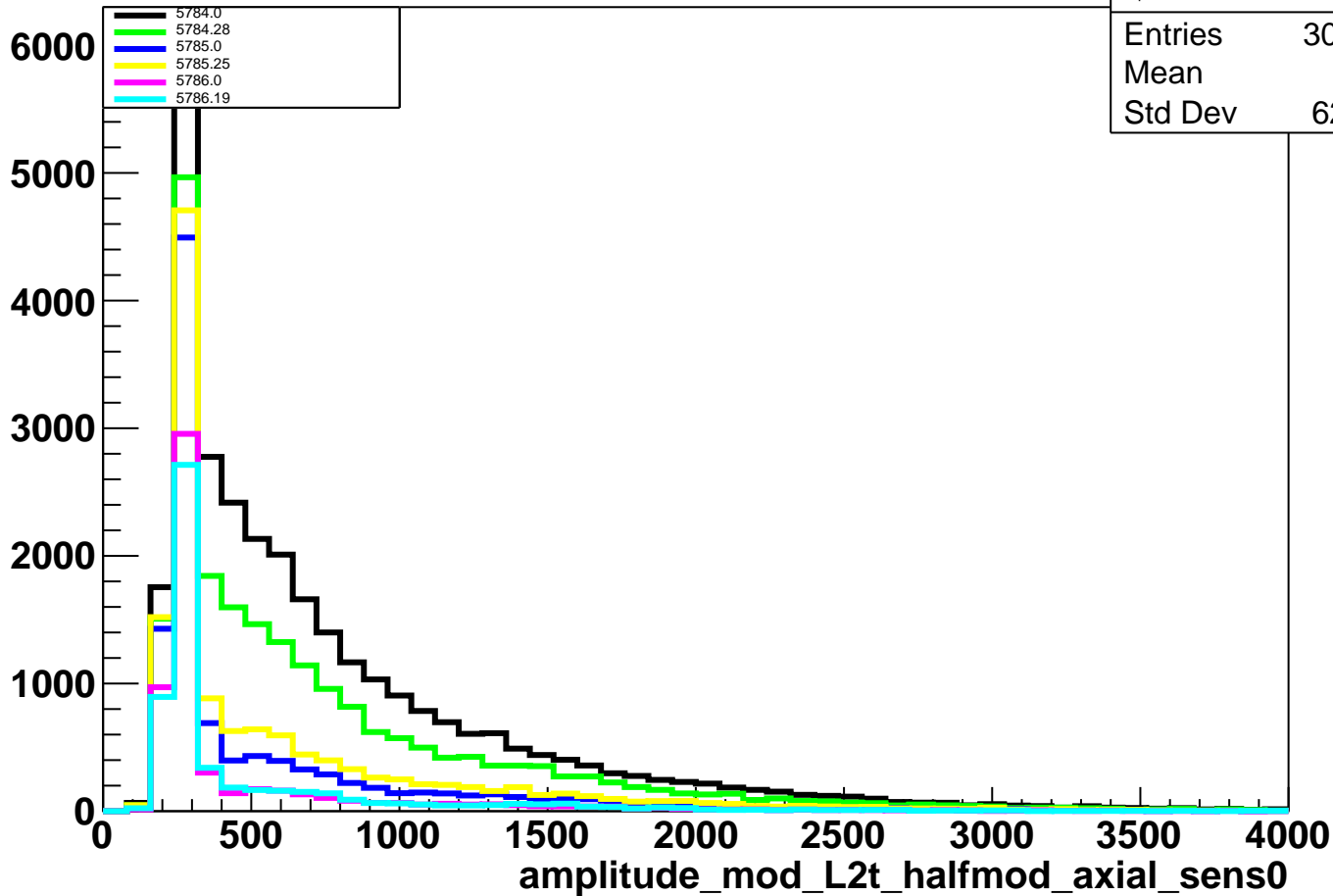
mod_L2b_halfmod_axial_sens0



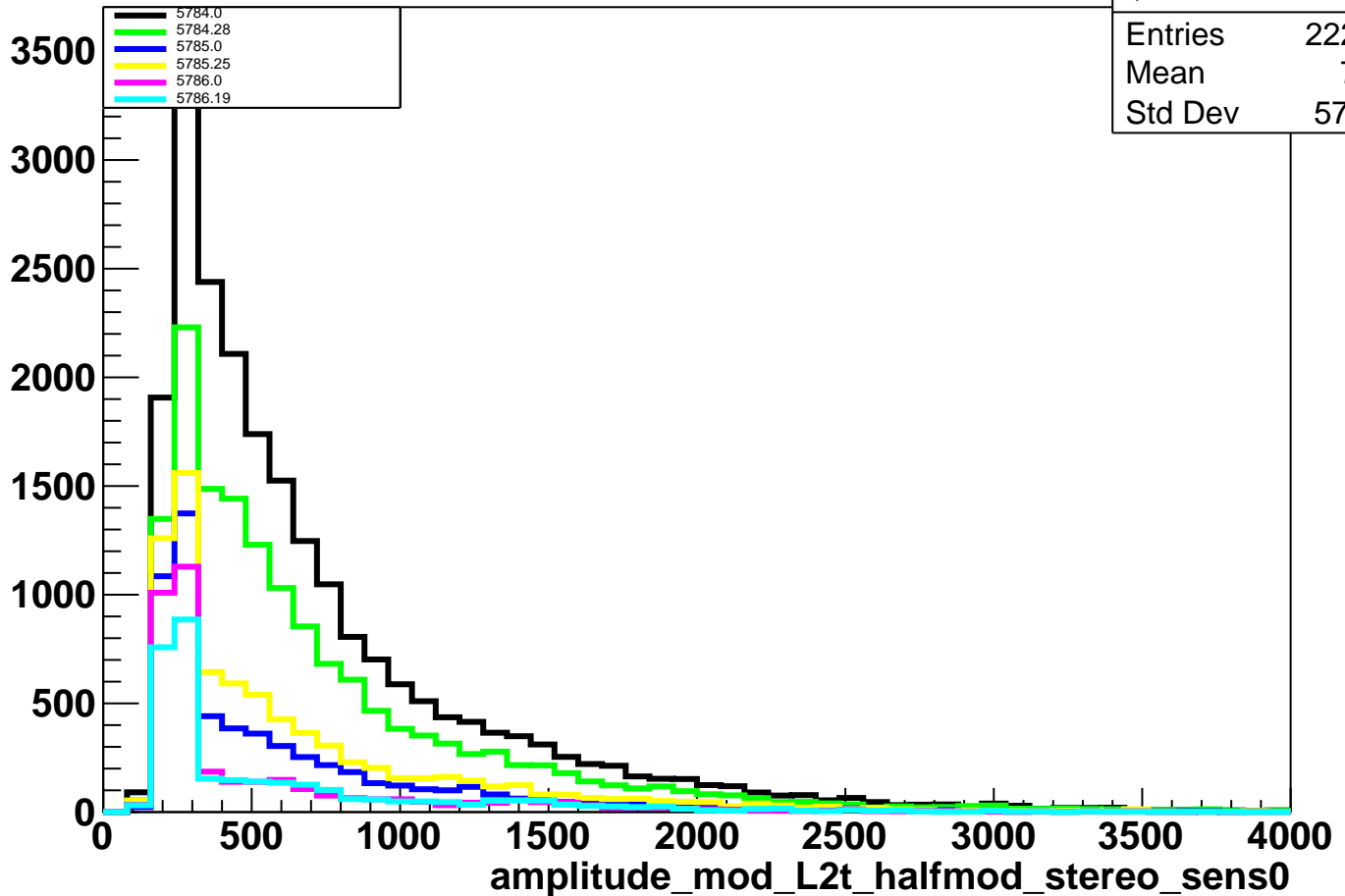
mod_L2b_halfmod_stereo_sens0



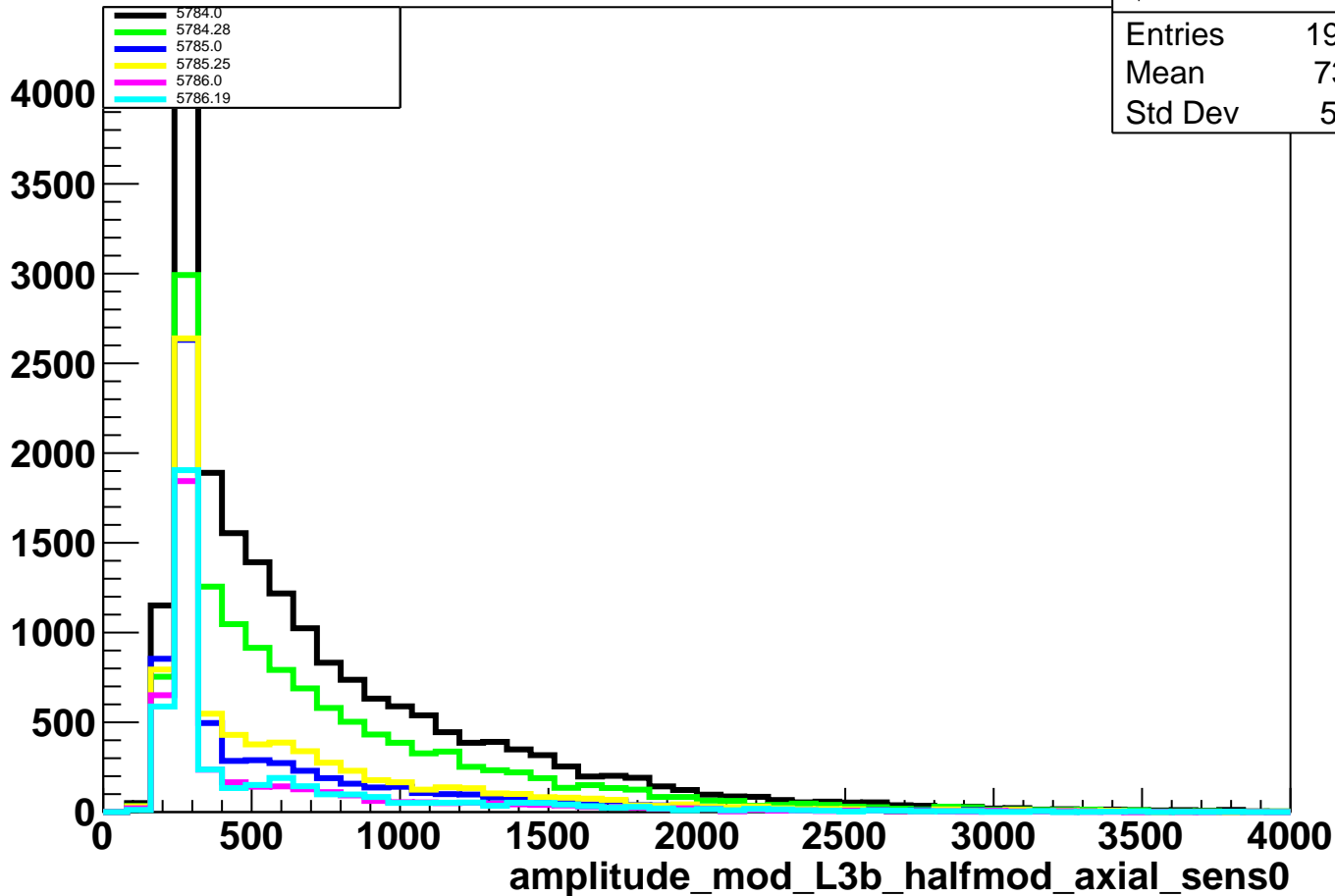
mod_L2t_halfmod_axial_sens0



mod_L2t_halfmod_stereo_sens0



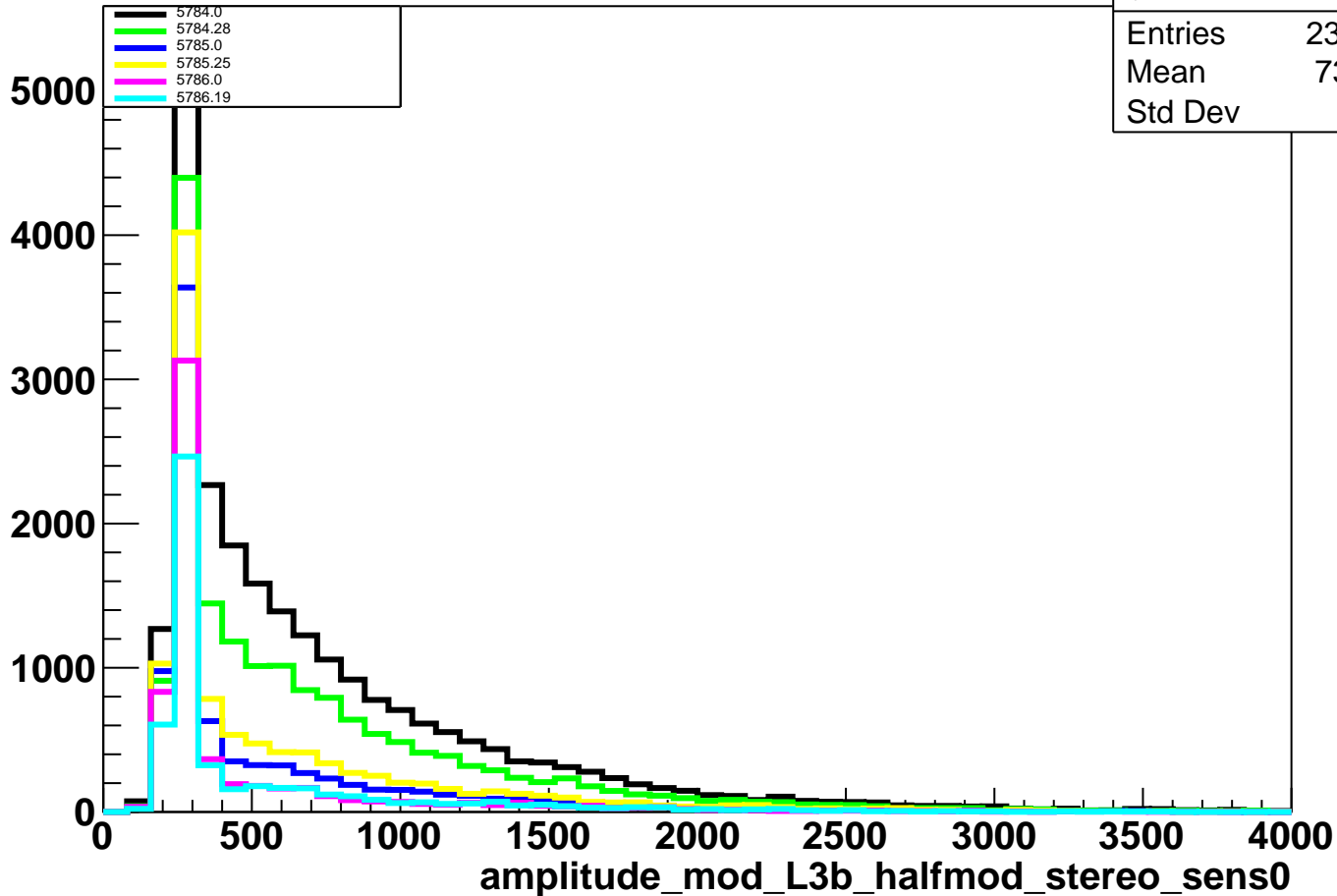
mod_L3b_halfmod_axial_sens0



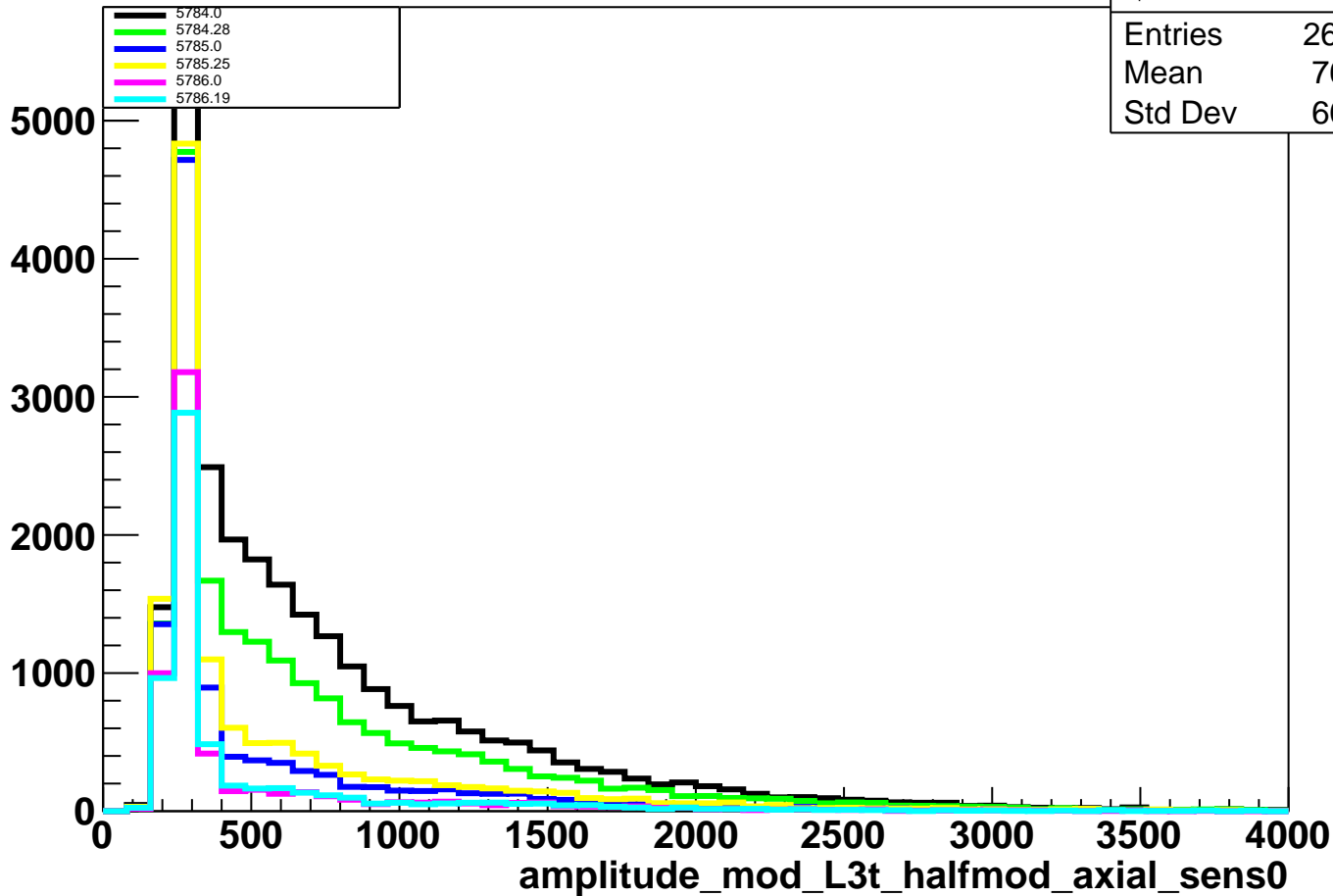
amplitude_mod_L3b_halfmod_axial_sens0

Entries	19707
Mean	732.2
Std Dev	576.1

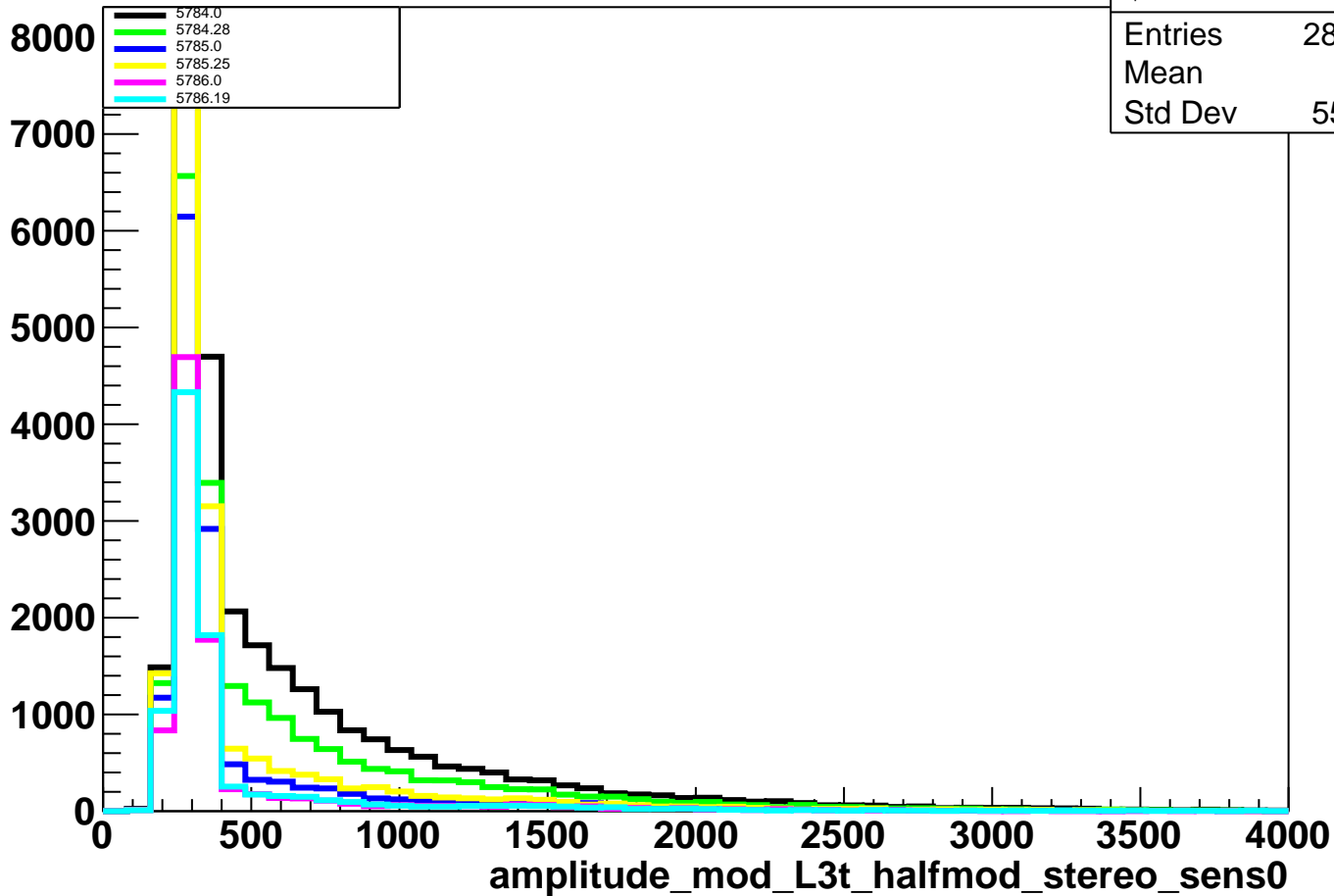
mod_L3b_halfmod_stereo_sens0



mod_L3t_halfmod_axial_sens0



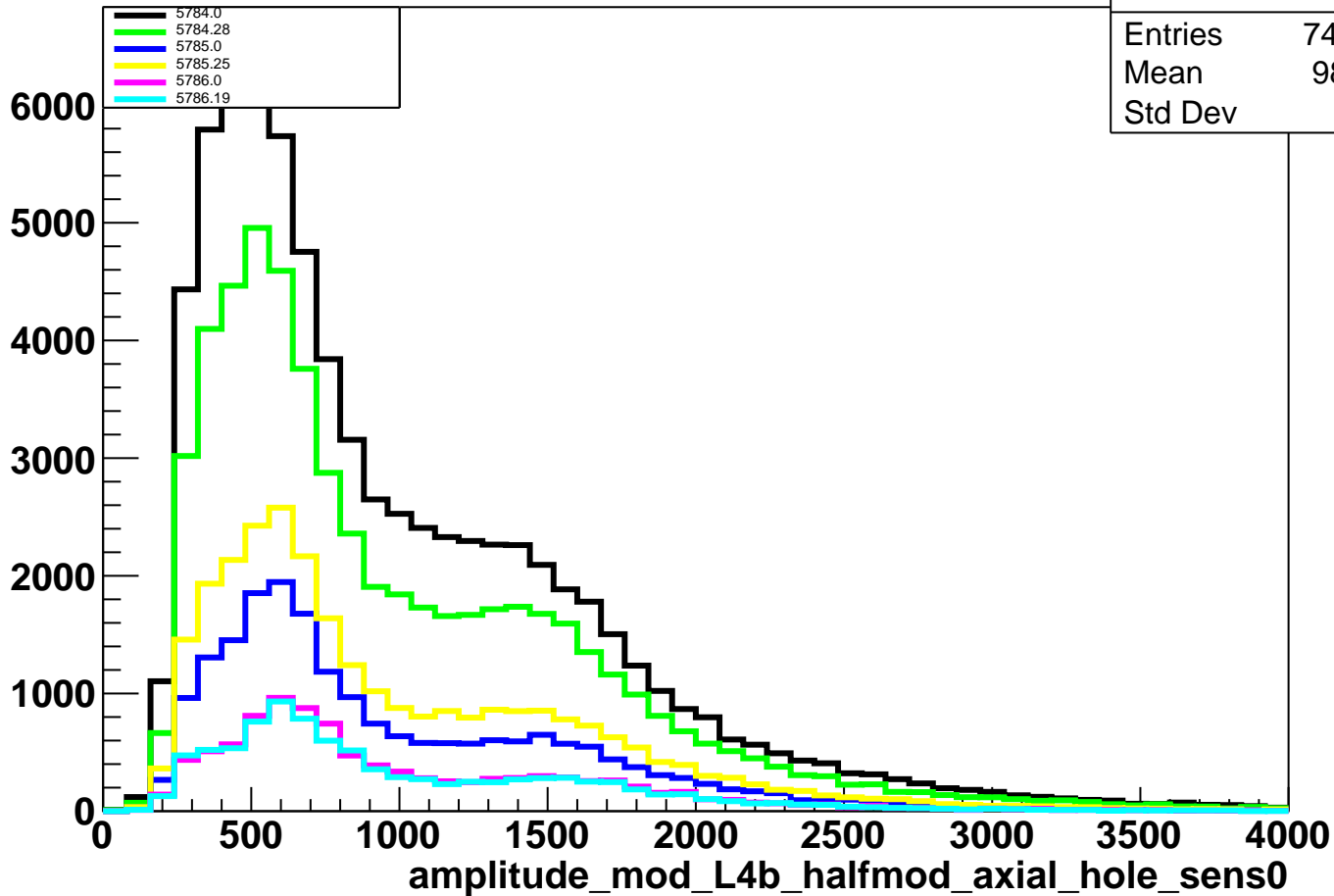
mod_L3t_halfmod_stereo_sens0



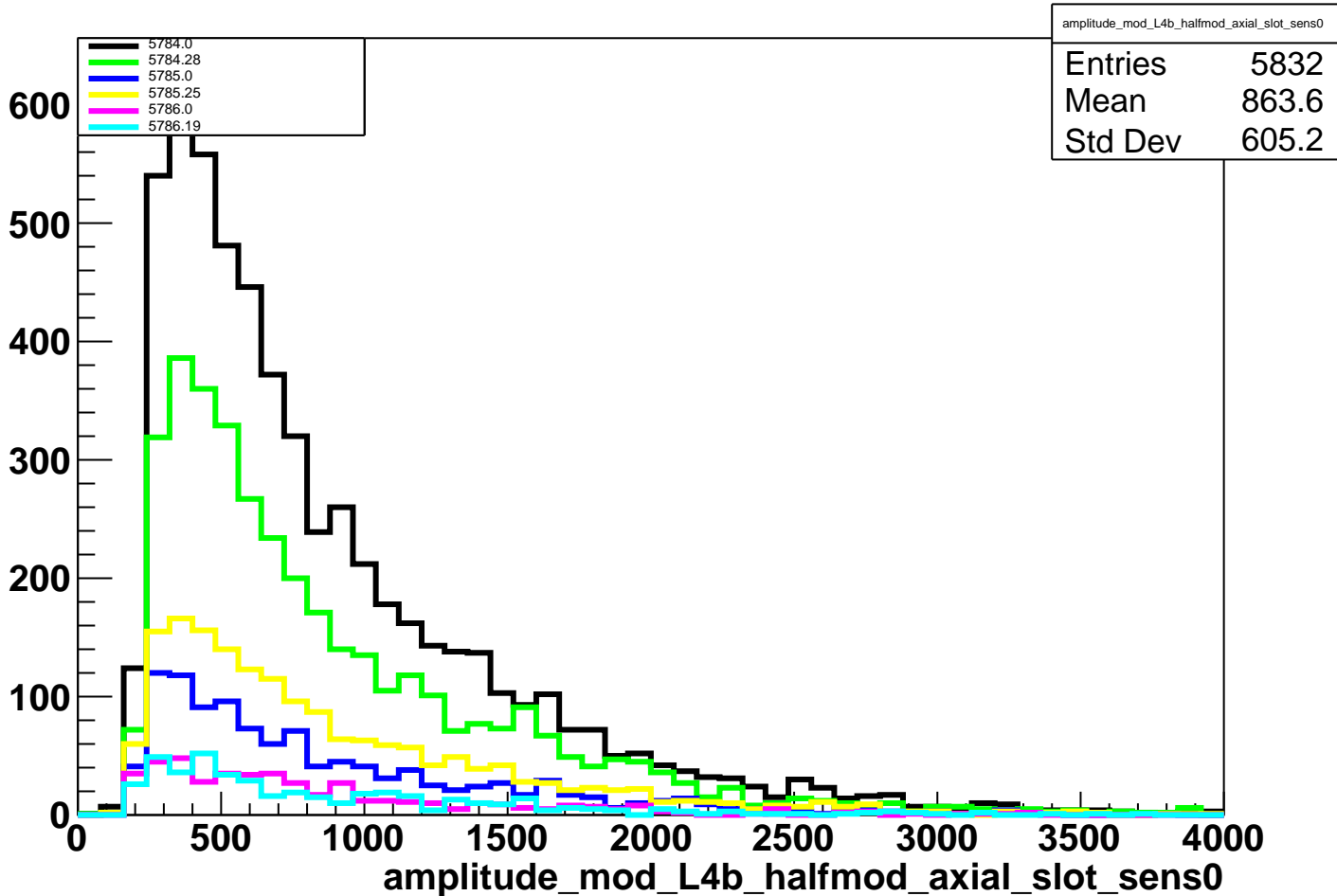
amplitude_mod_L3t_halfmod_stereo_sens0

Entries	28692
Mean	650
Std Dev	556.9

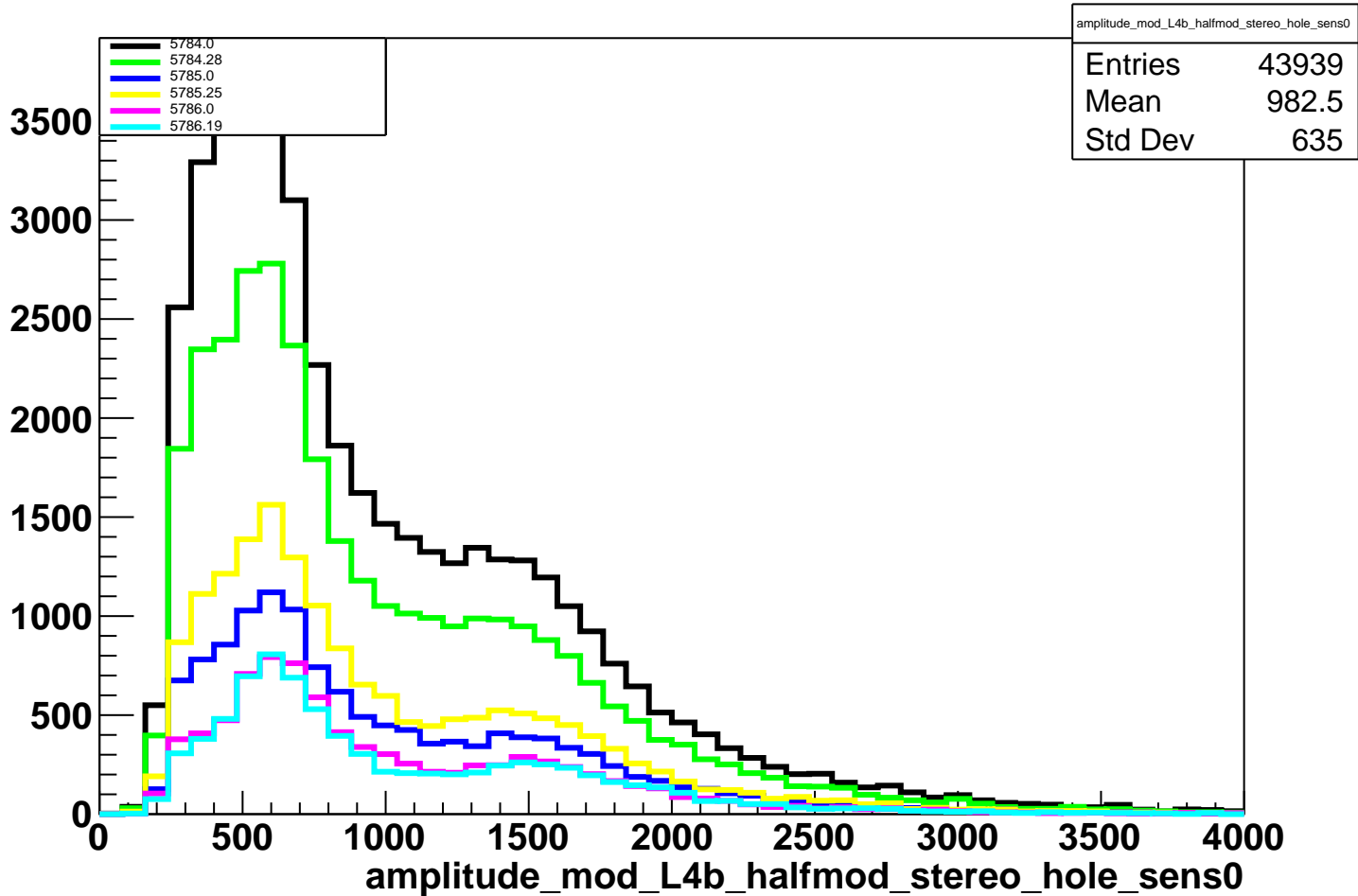
mod_L4b_halfmod_axial_hole_sens0



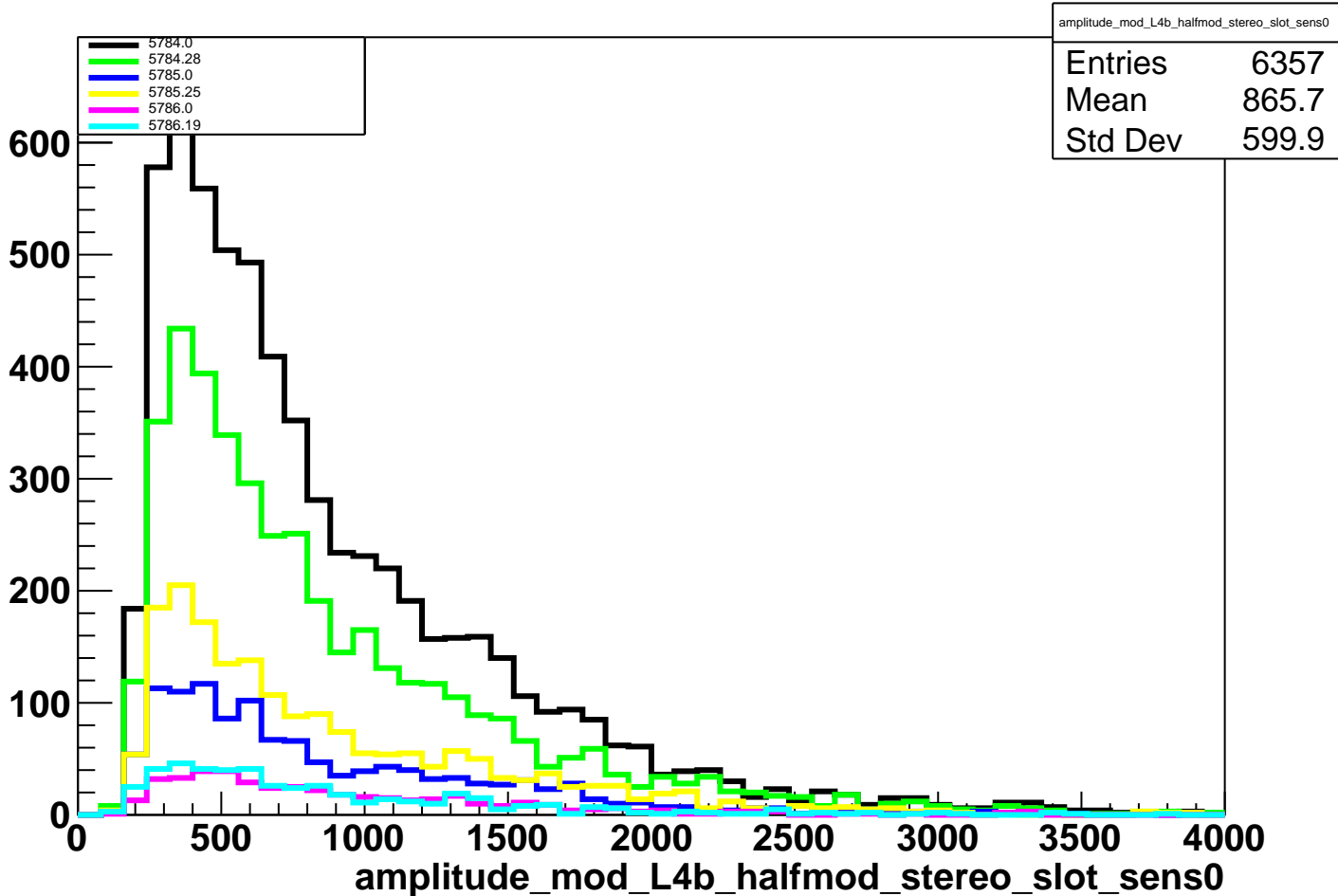
mod_L4b_halfmod_axial_slot_sens0



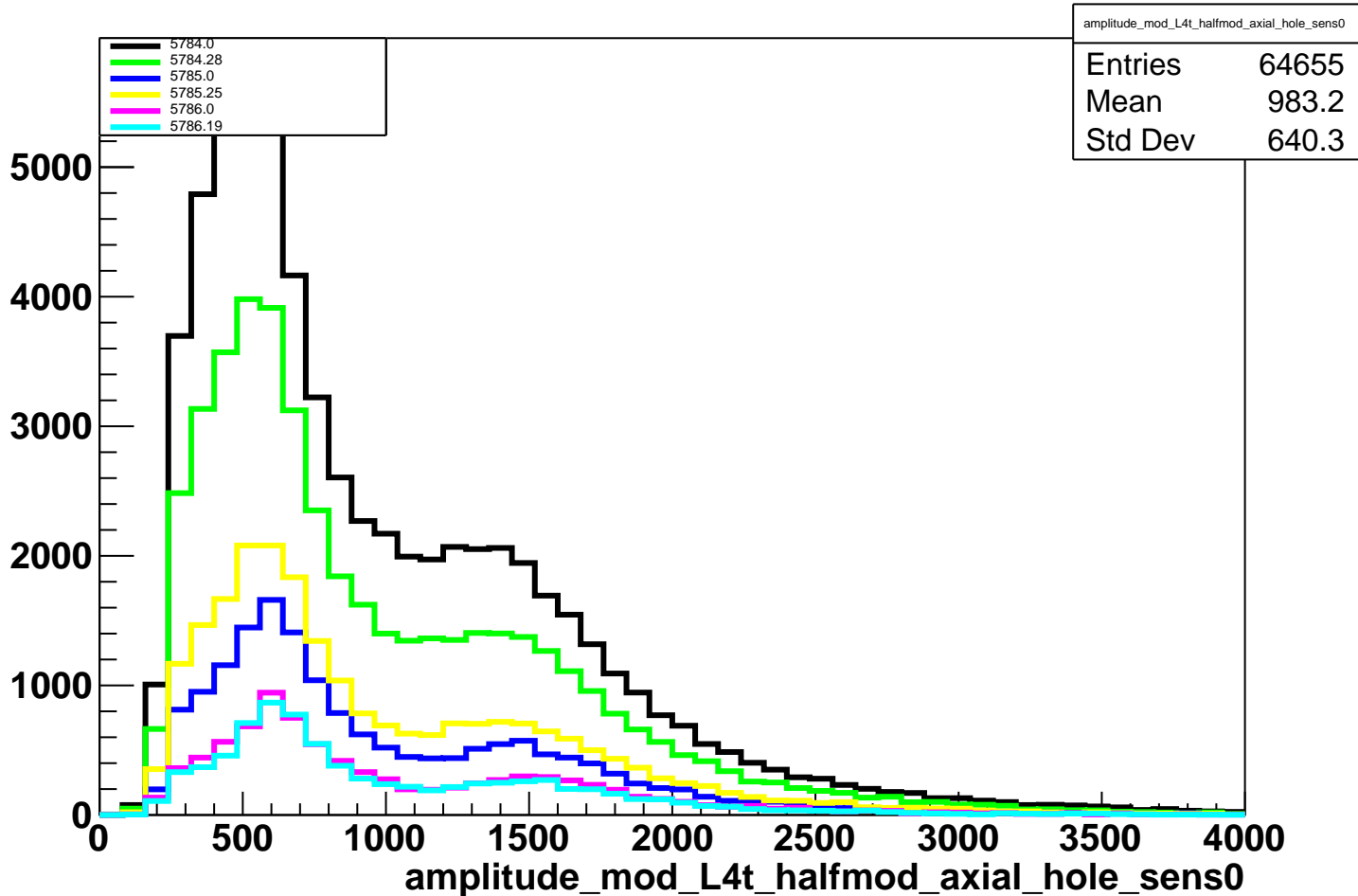
mod_L4b_halfmod_stereo_hole_sens0



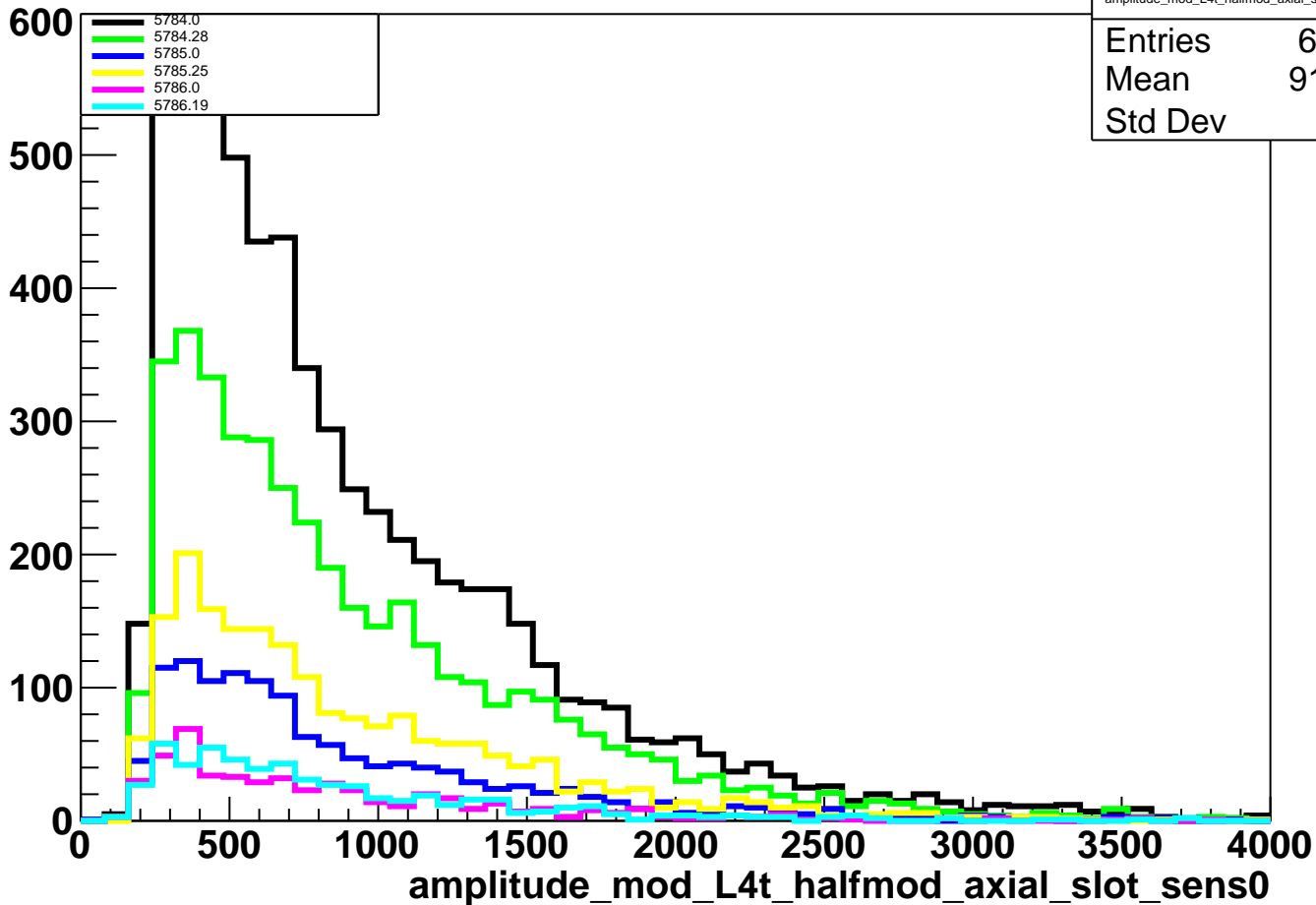
mod_L4b_halfmod_stereo_slot_sens0



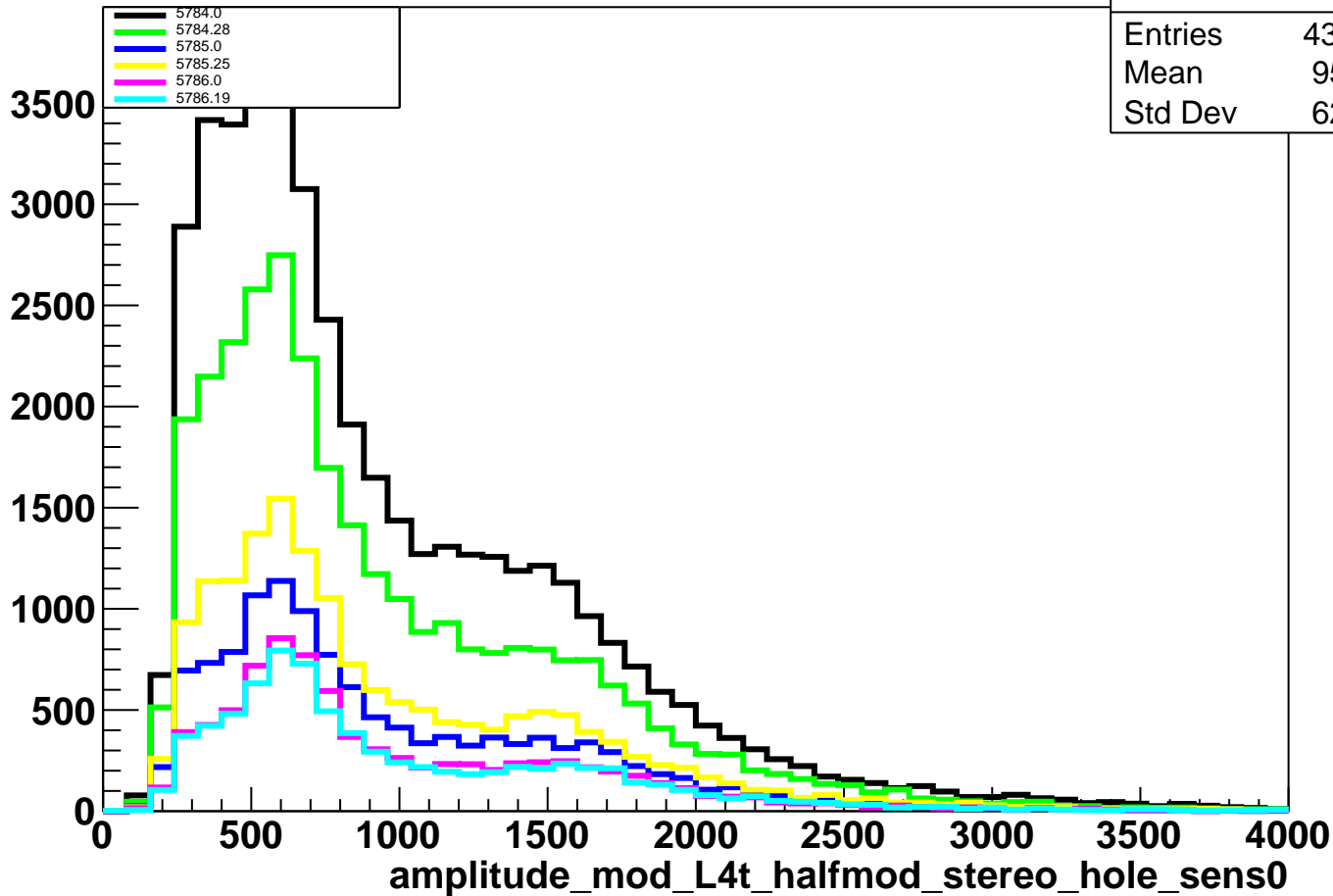
mod_L4t_halfmod_axial_hole_sens0



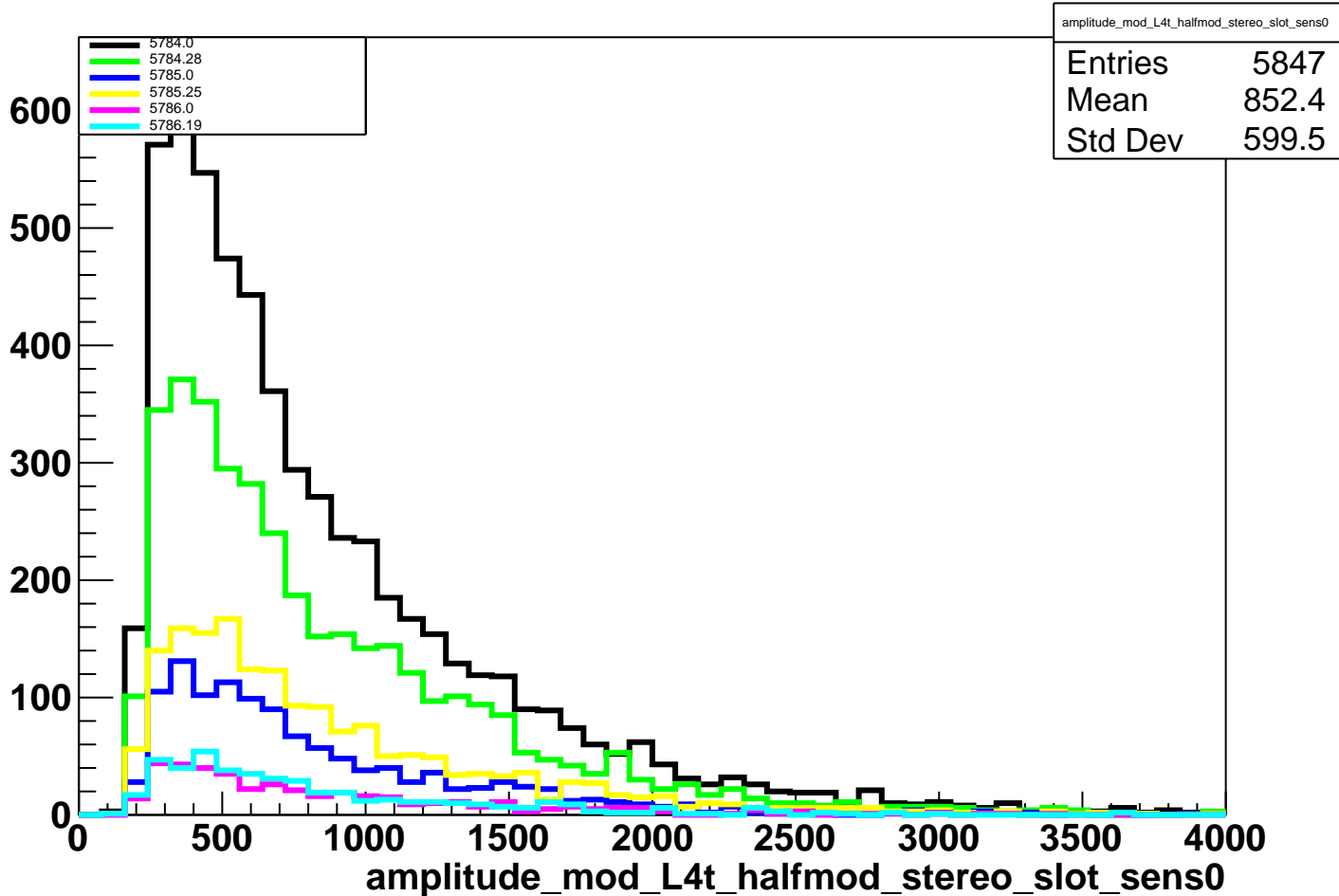
mod_L4t_halfmod_axial_slot_sens0



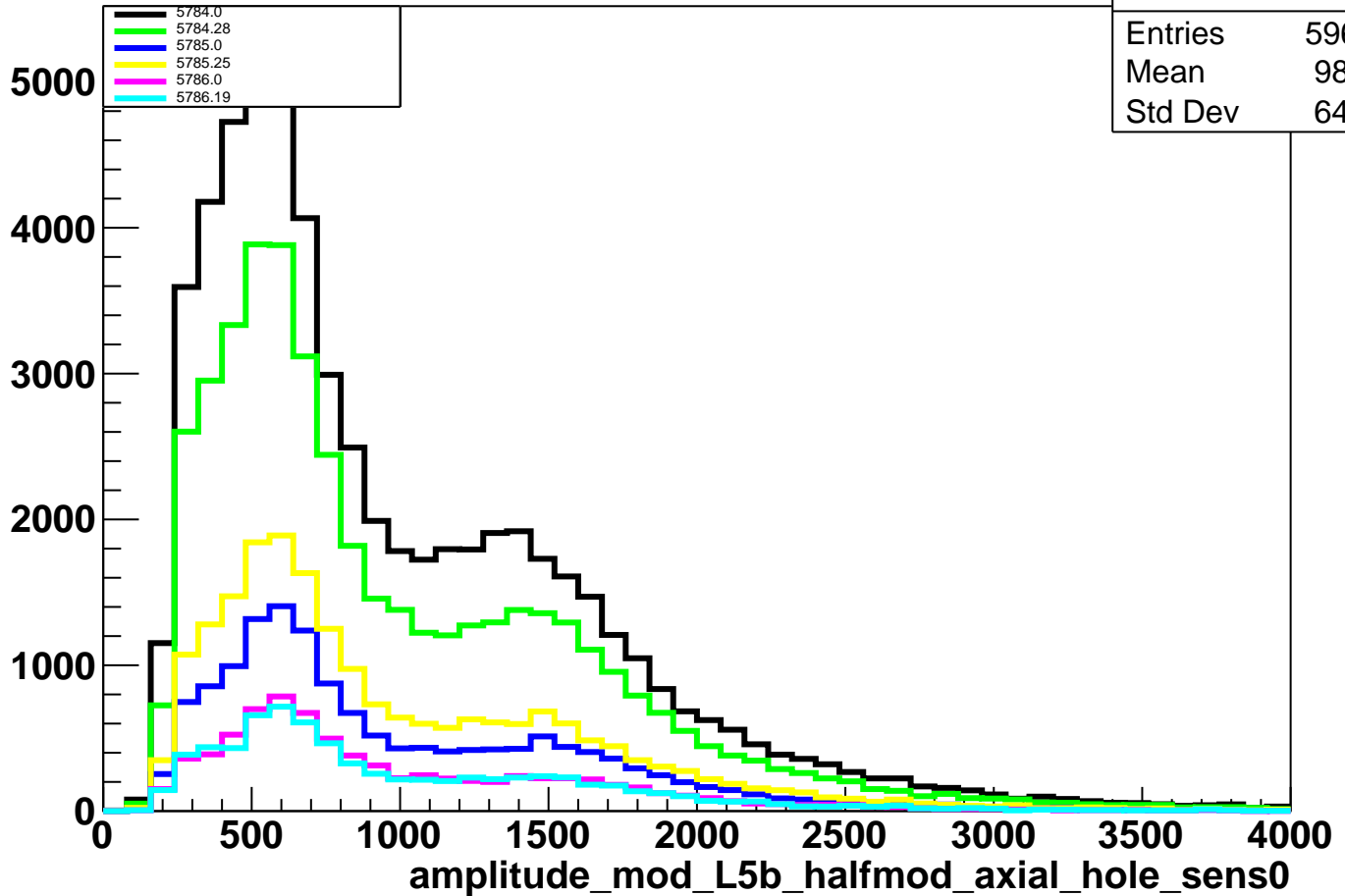
mod_L4t_halfmod_stereo_hole_sens0



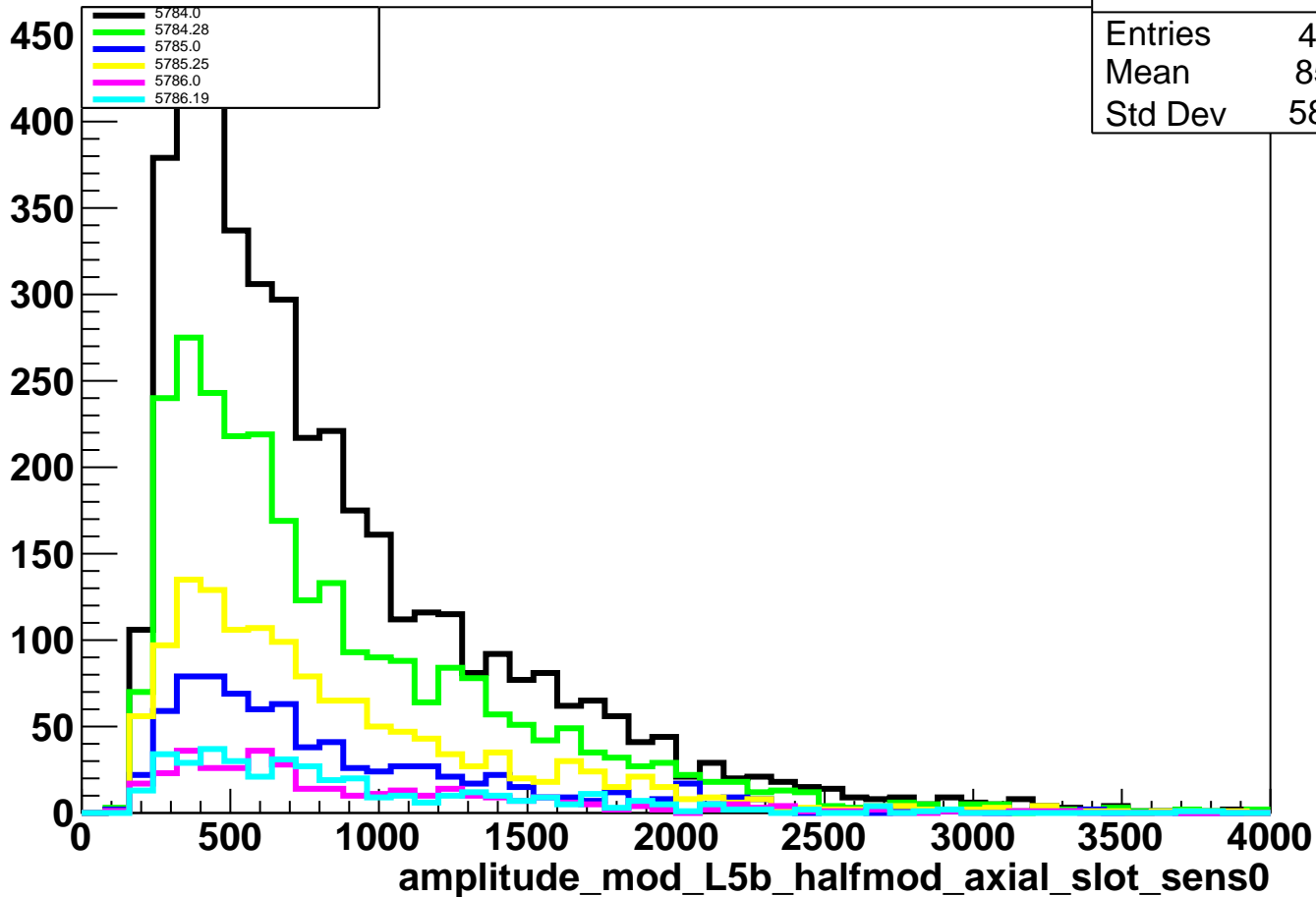
mod_L4t_halfmod_stereo_slot_sens0



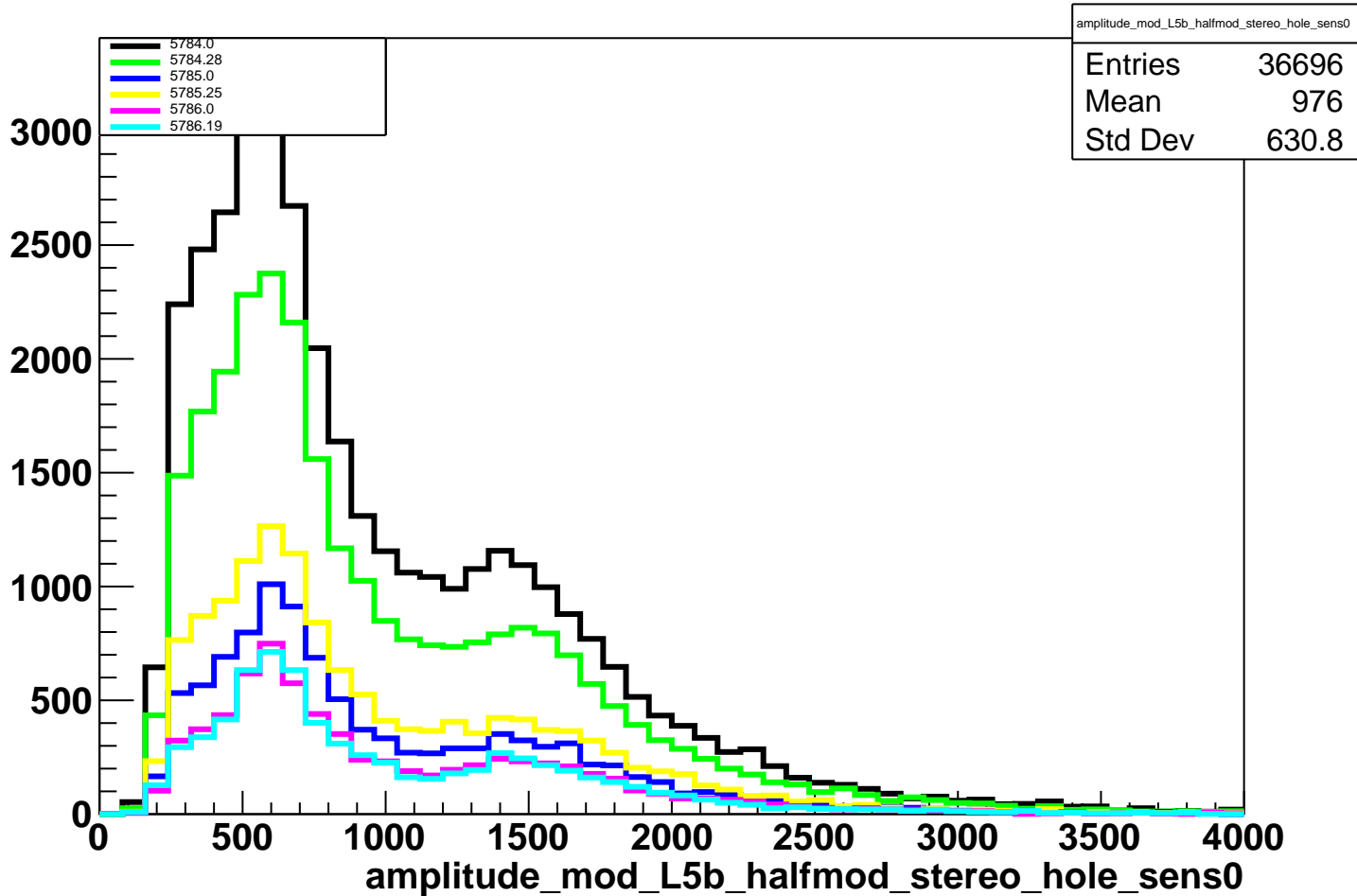
mod_L5b_halfmod_axial_hole_sens0



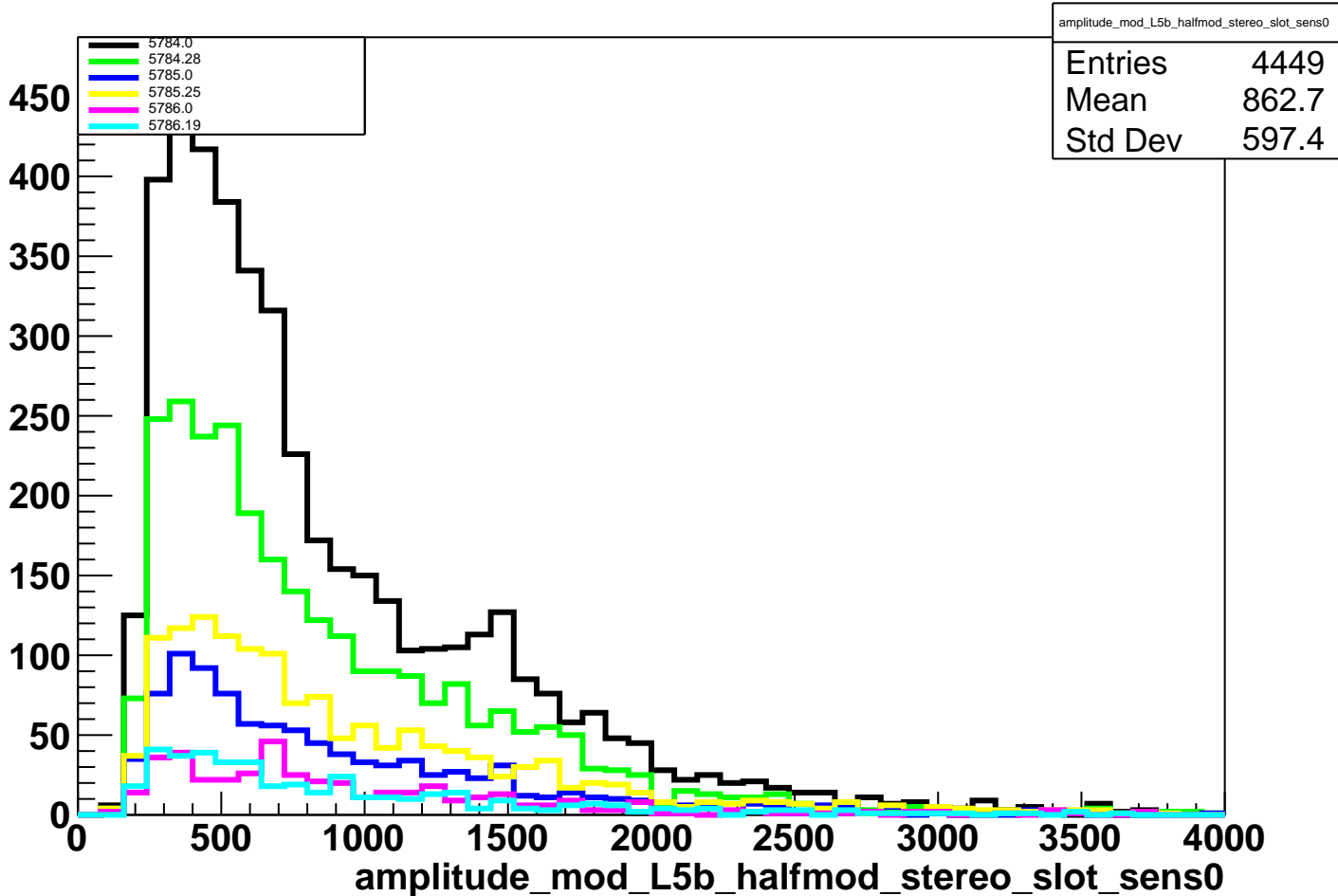
mod_L5b_halfmod_axial_slot_sens0



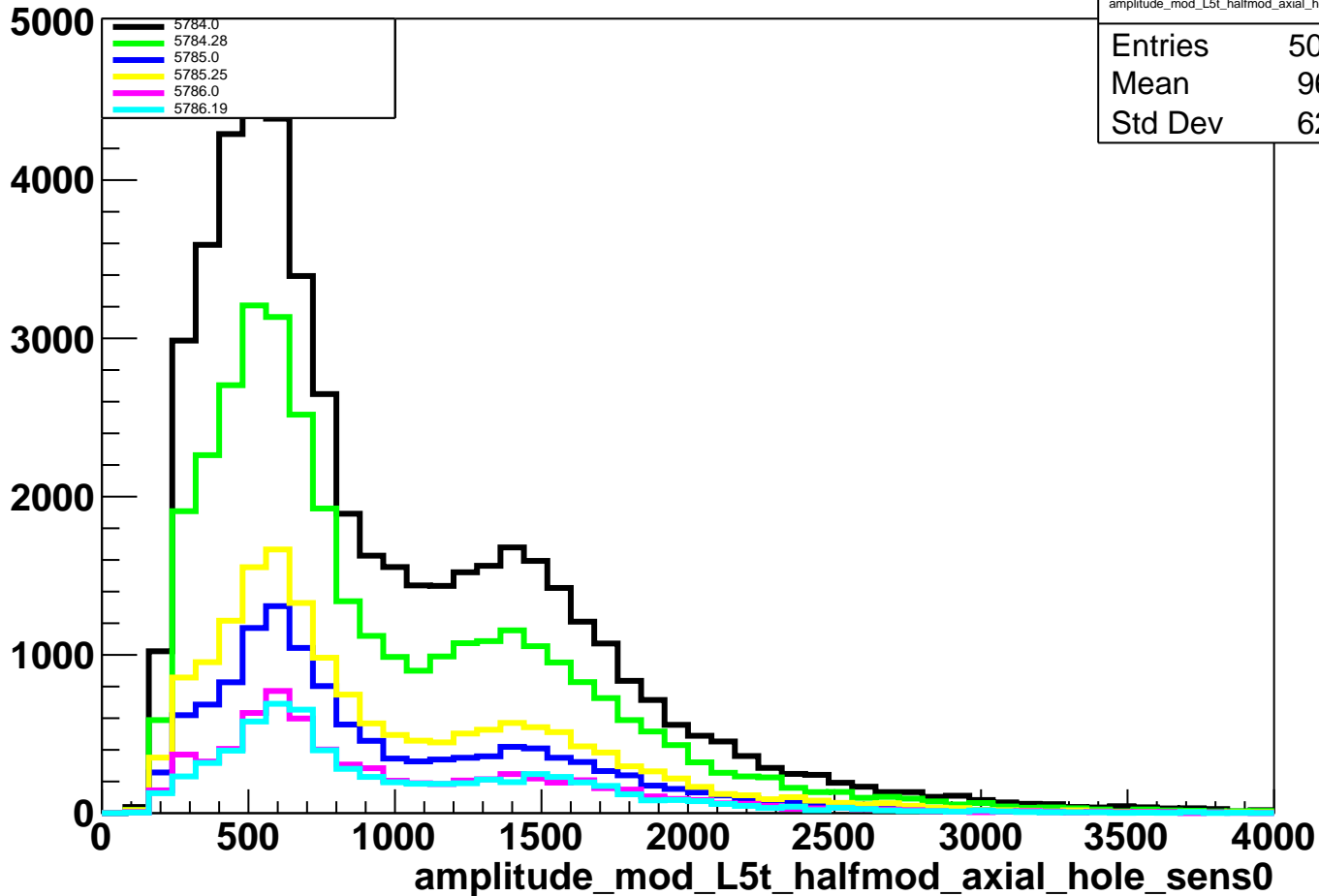
mod_L5b_halfmod_stereo_hole_sens0



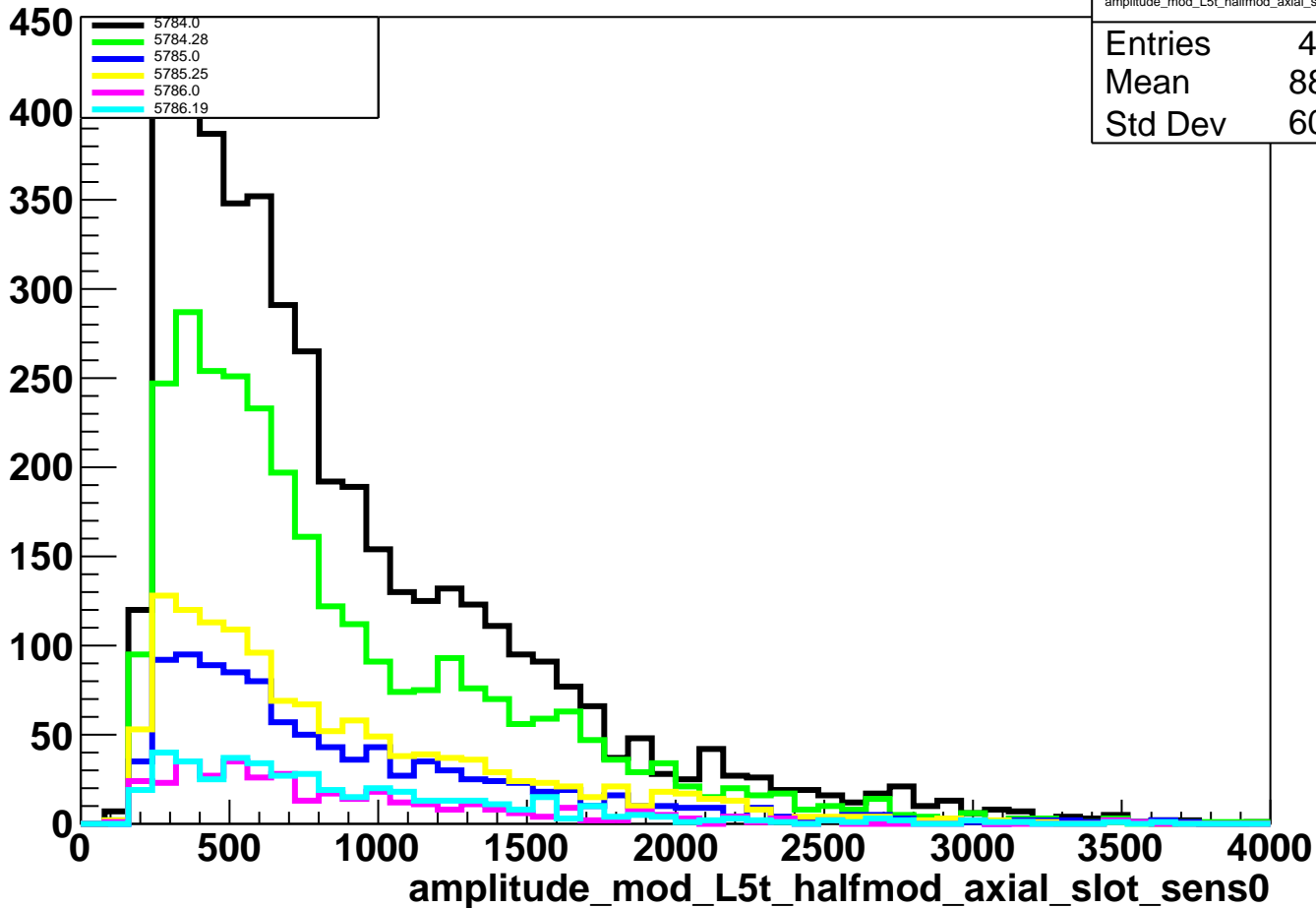
mod_L5b_halfmod_stereo_slot_sens0



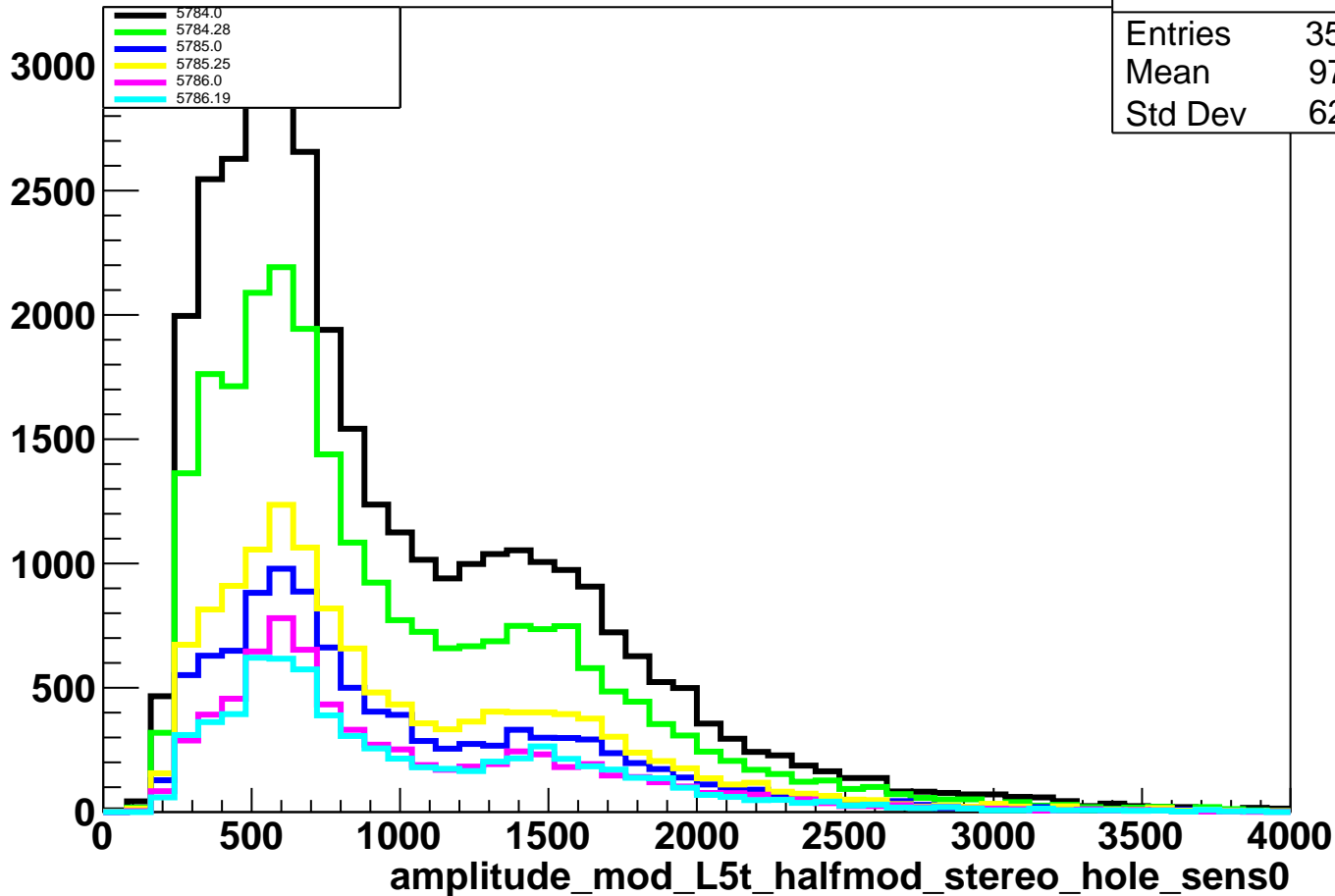
mod_L5t_halfmod_axial_hole_sens0



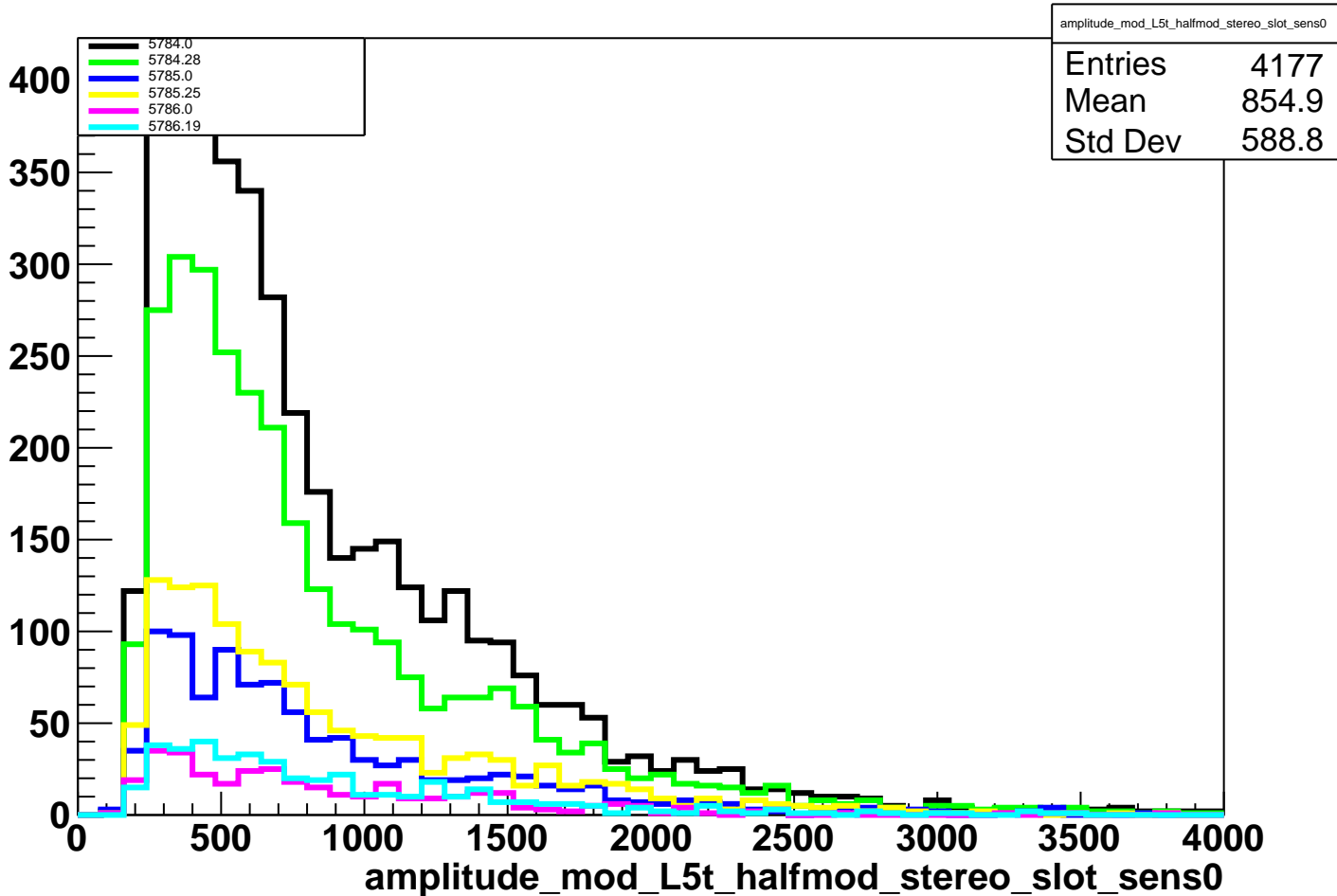
mod_L5t_halfmod_axial_slot_sens0



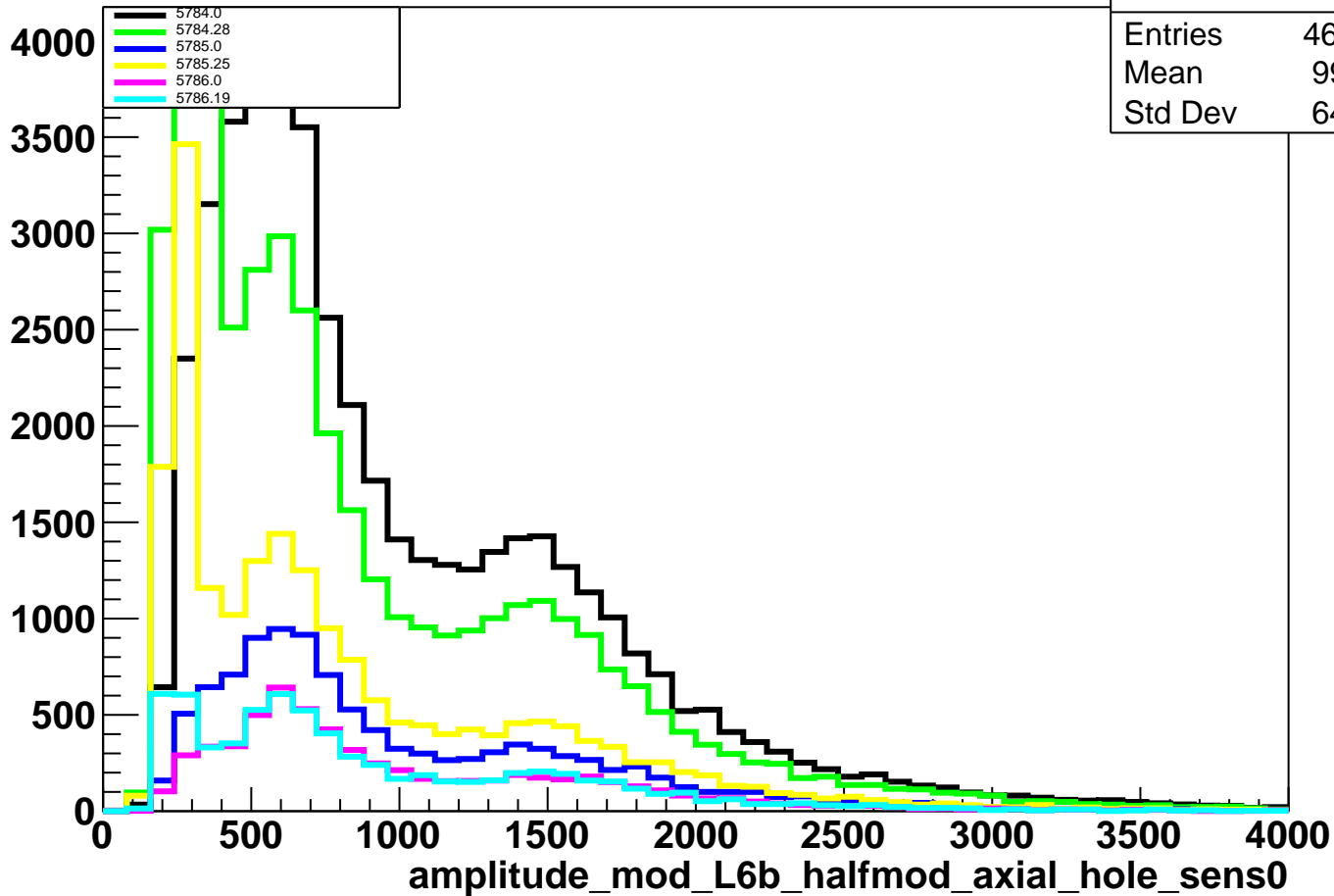
mod_L5t_halfmod_stereo_hole_sens0



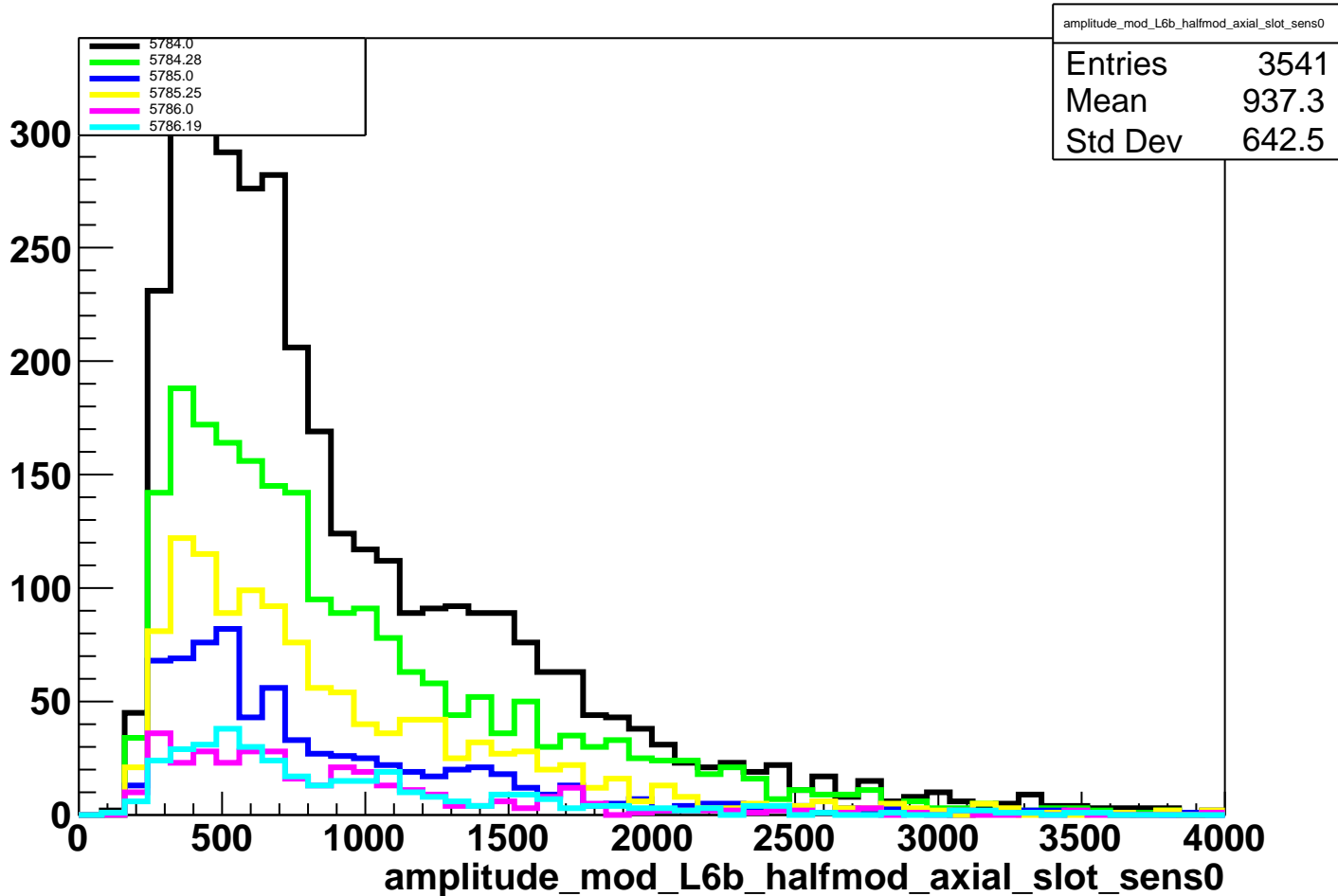
mod_L5t_halfmod_stereo_slot_sens0



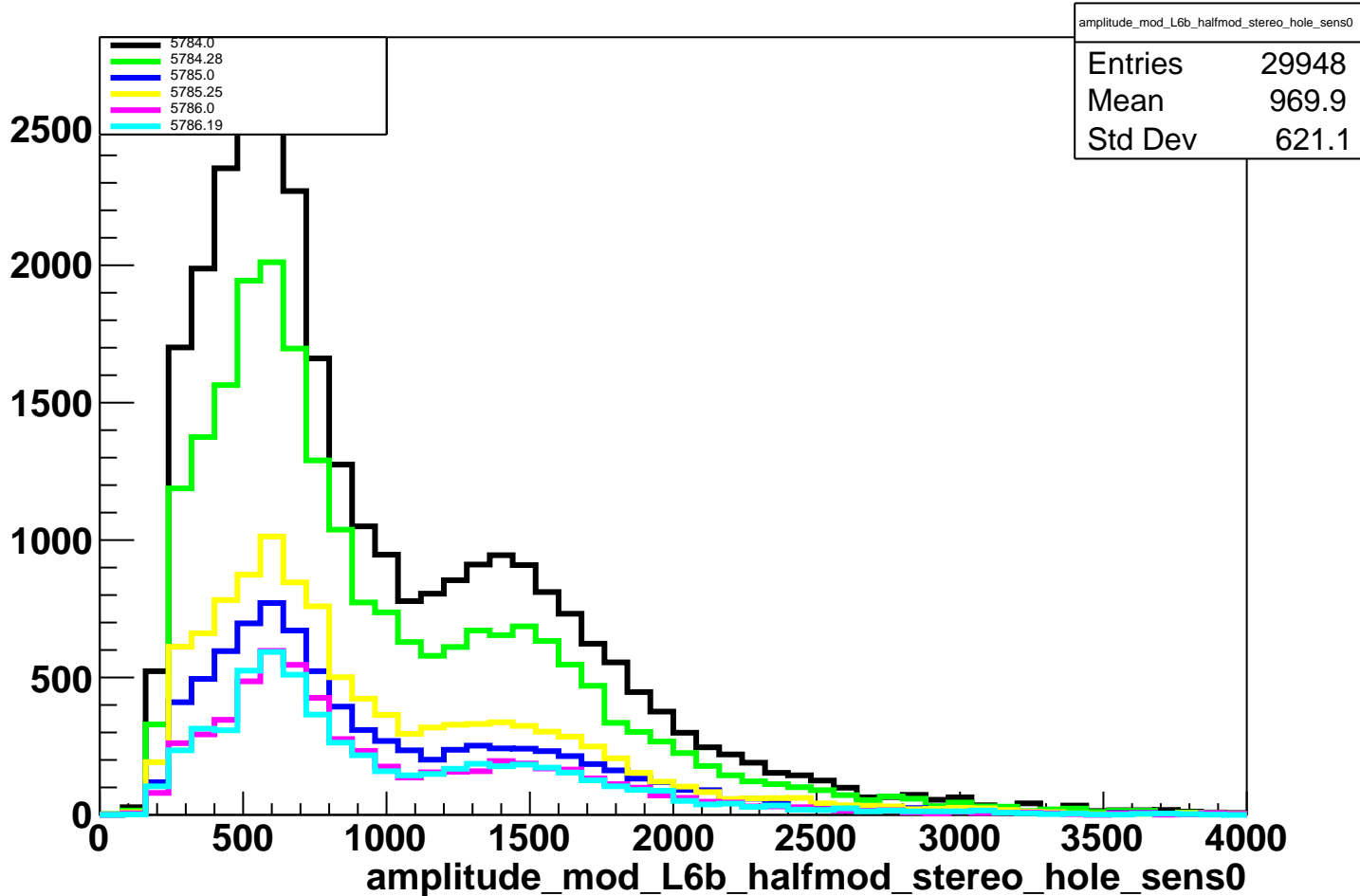
mod_L6b_halfmod_axial_hole_sens0



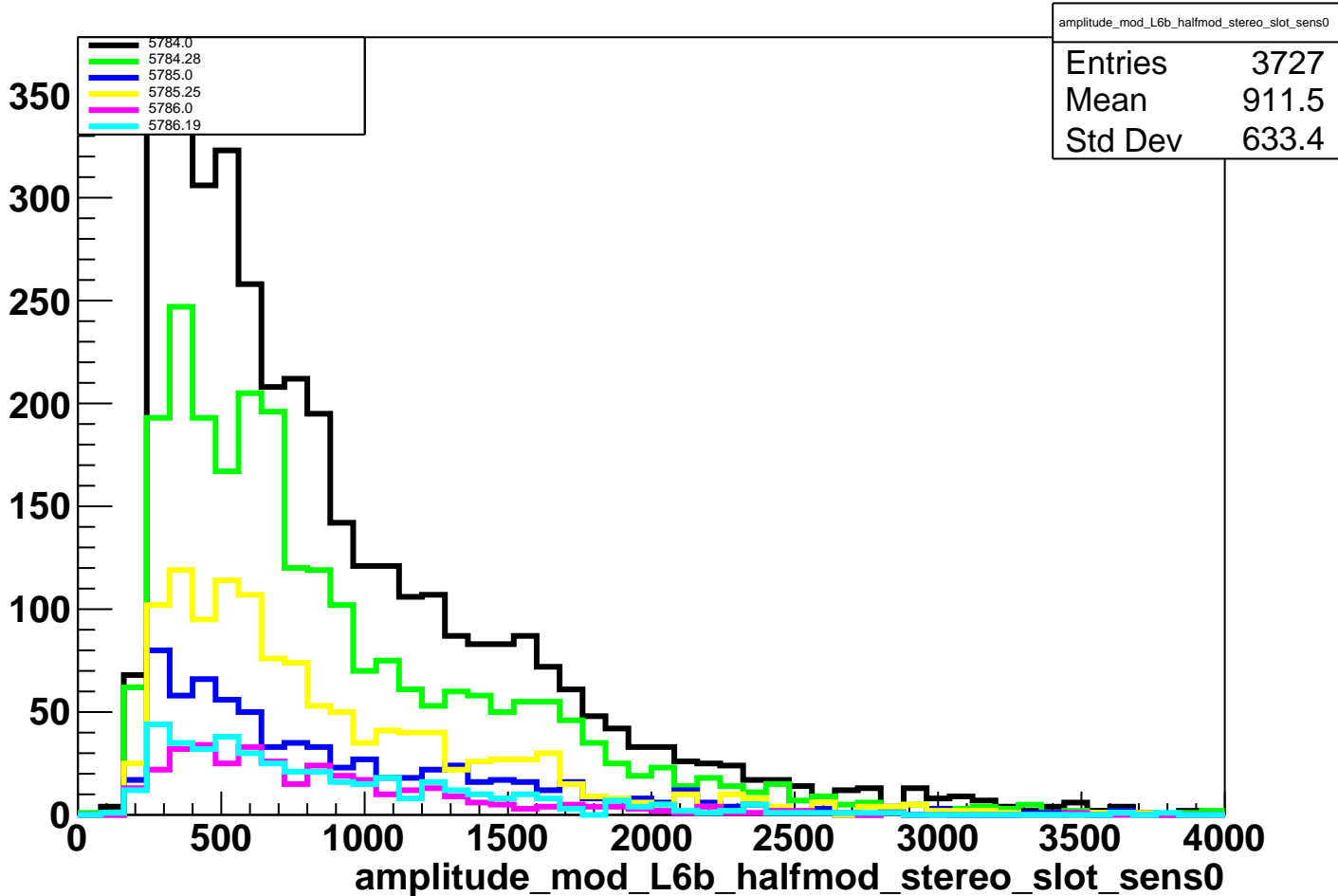
mod_L6b_halfmod_axial_slot_sens0



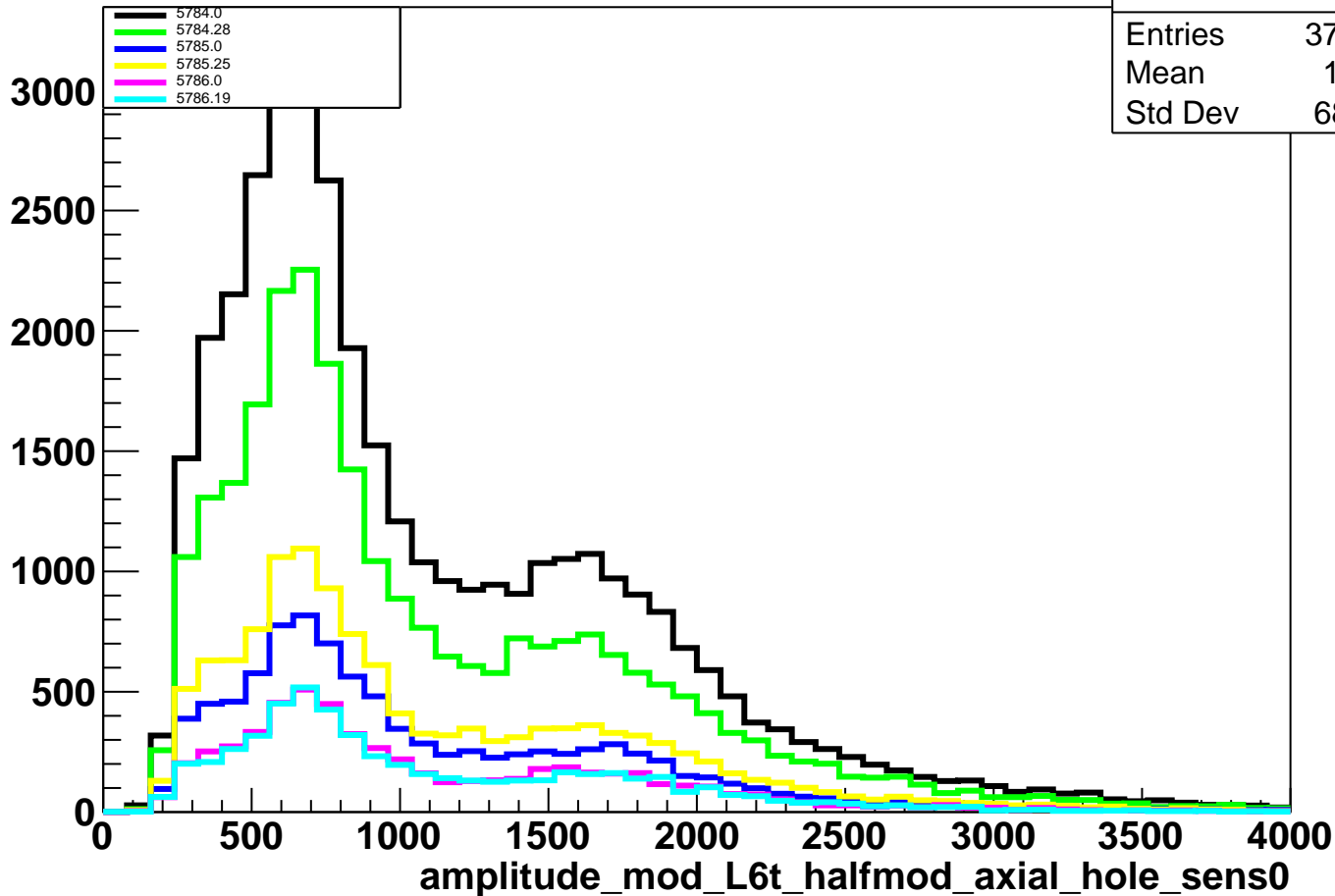
mod_L6b_halfmod_stereo_hole_sens0



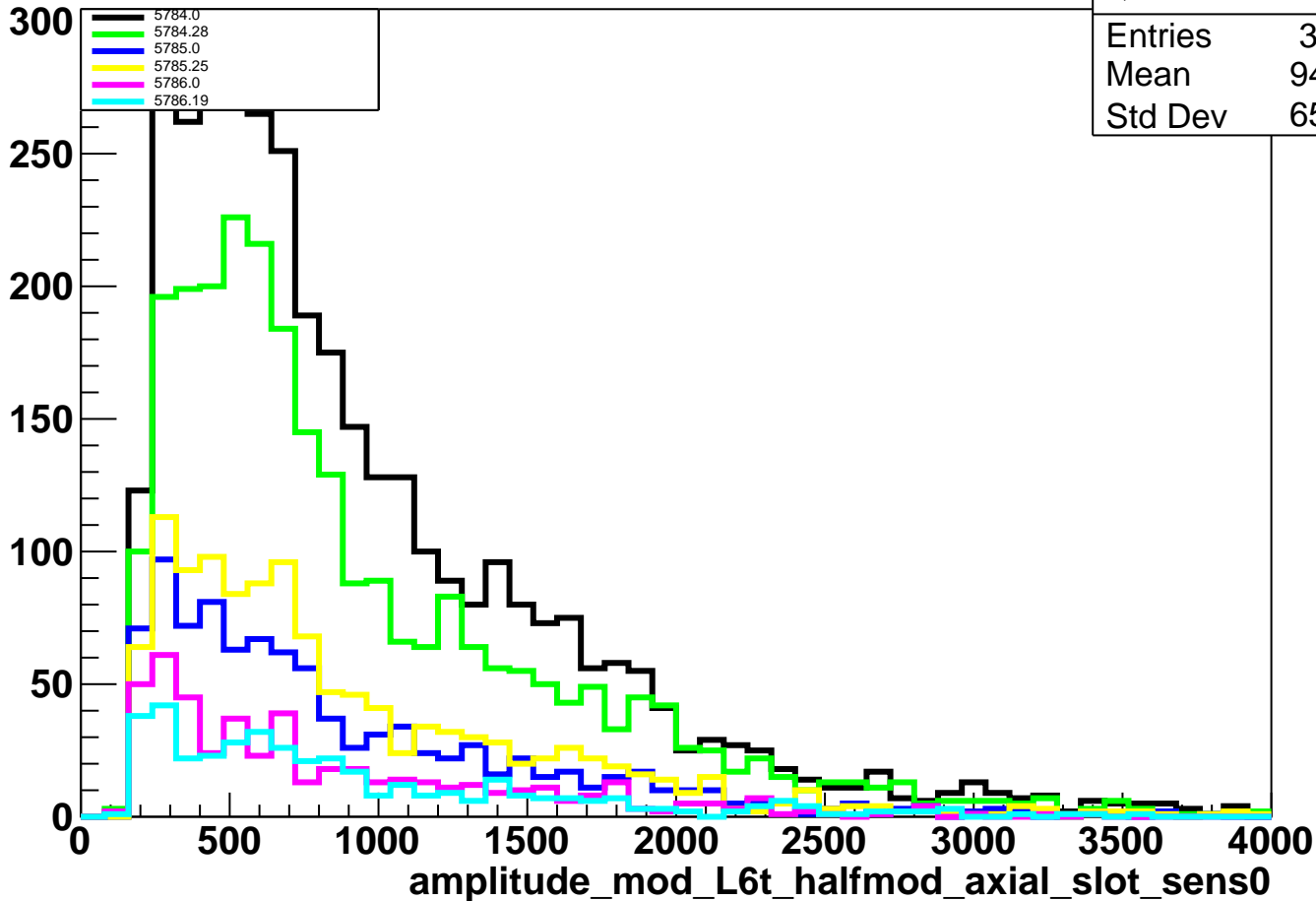
mod_L6b_halfmod_stereo_slot_sens0



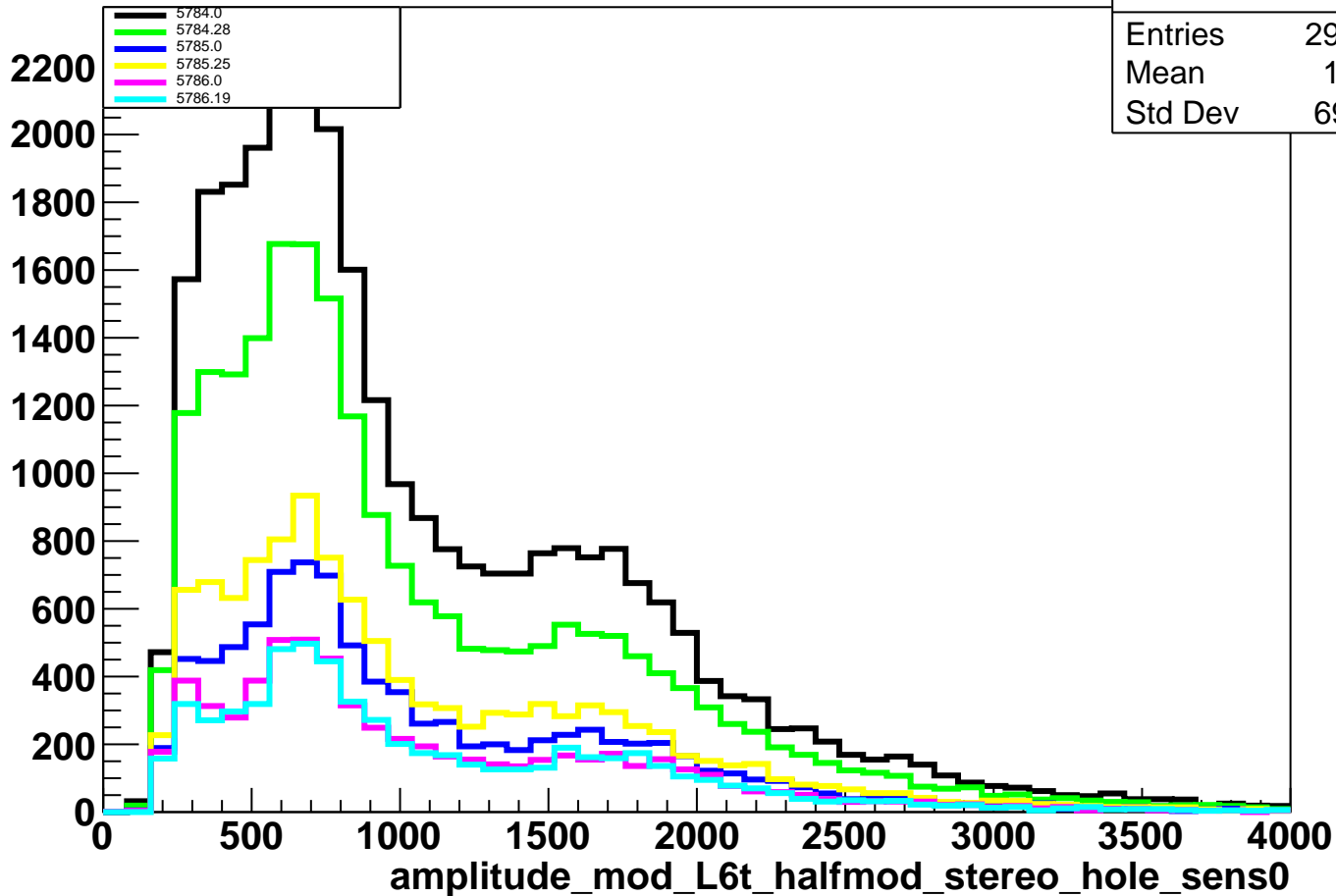
mod_L6t_halfmod_axial_hole_sens0



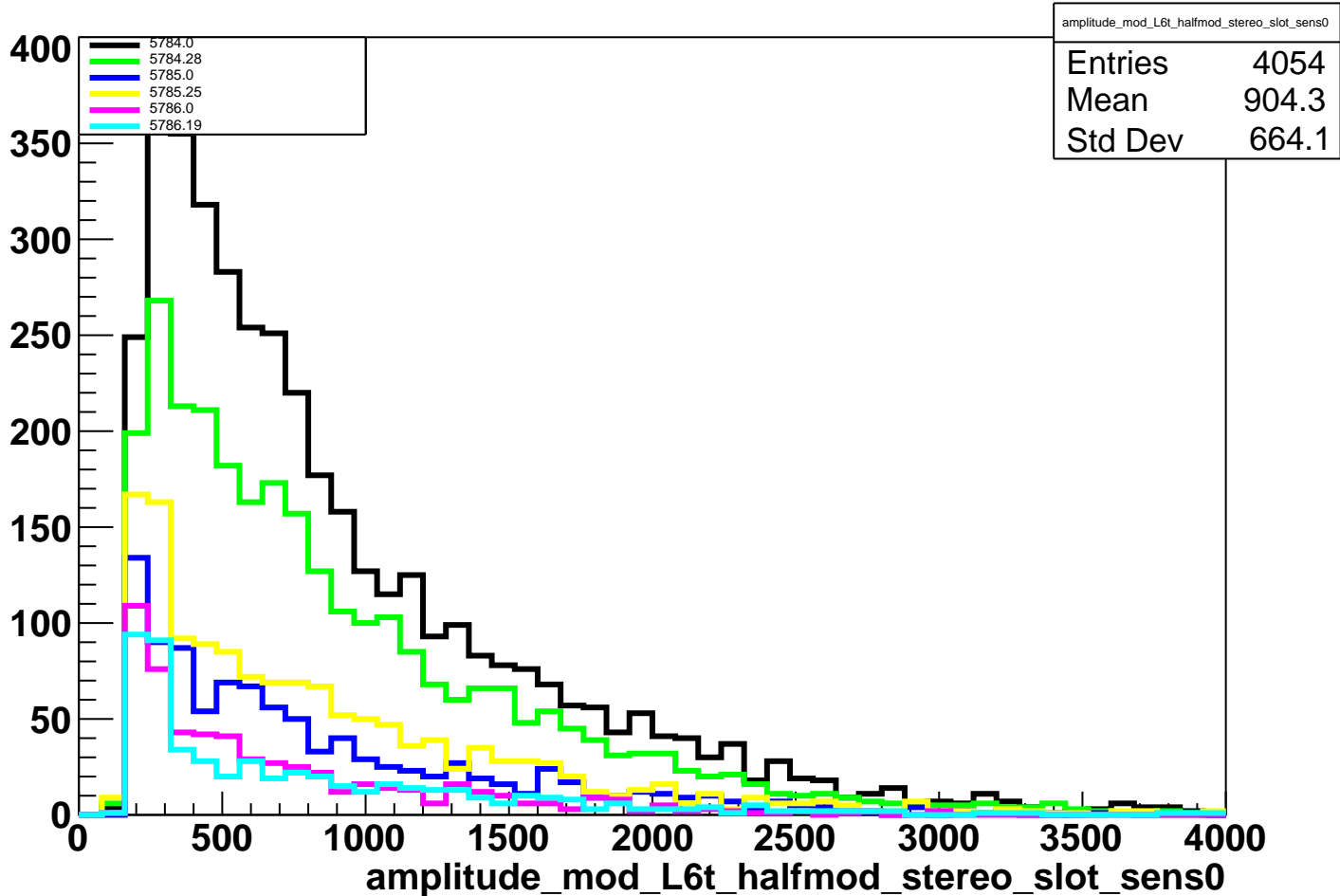
mod_L6t_halfmod_axial_slot_sens0



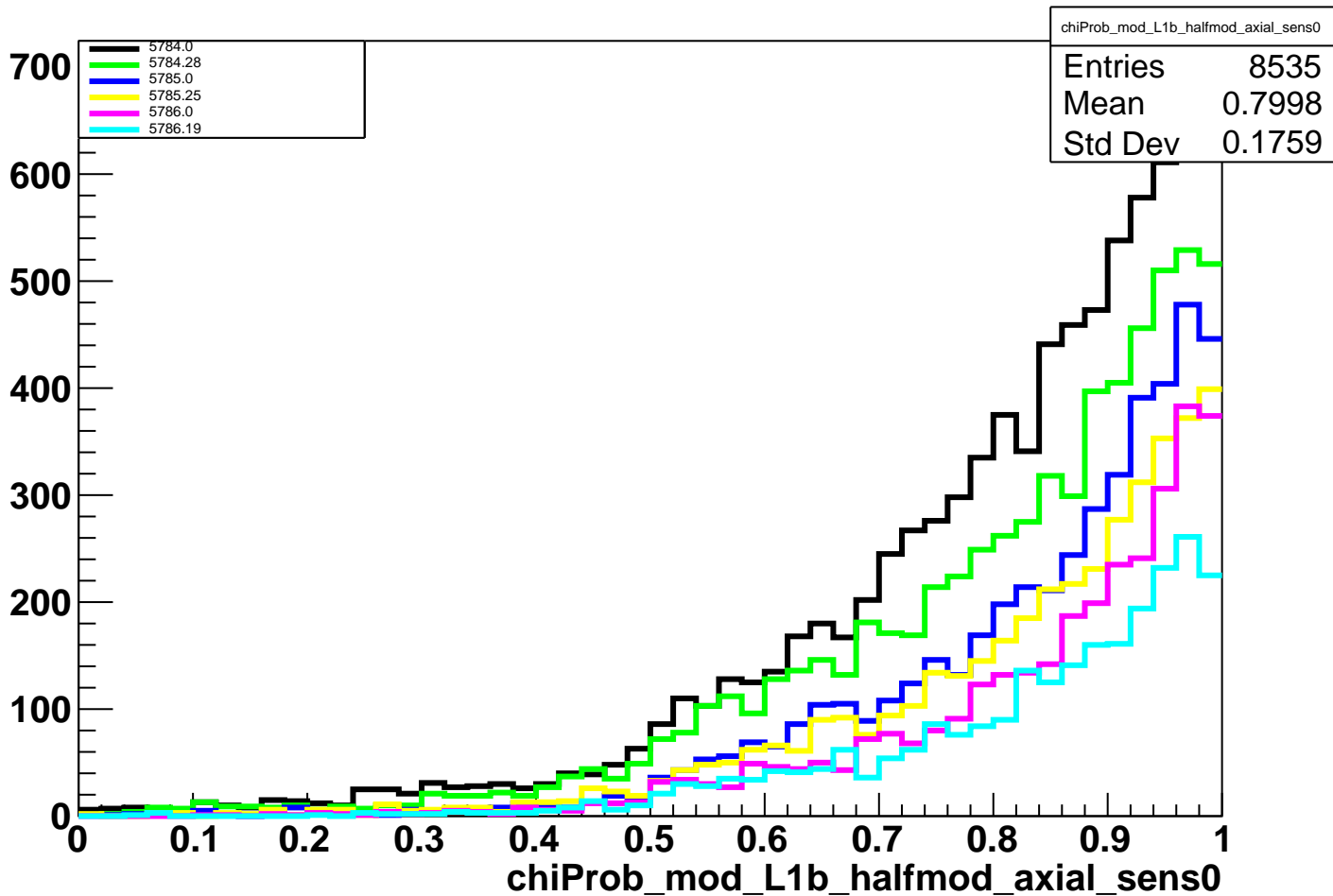
mod_L6t_halfmod_stereo_hole_sens0



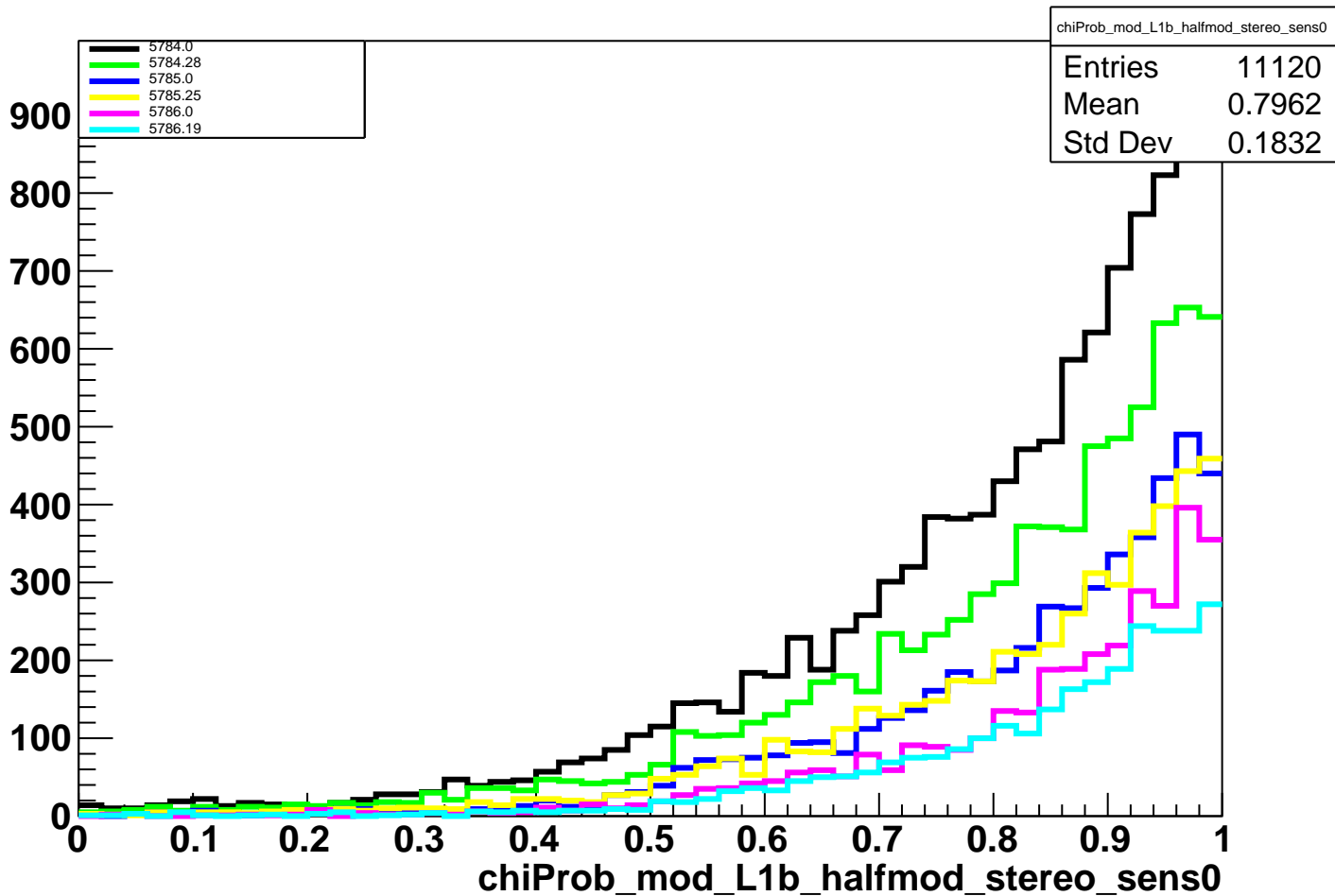
mod_L6t_halfmod_stereo_slot_sens0



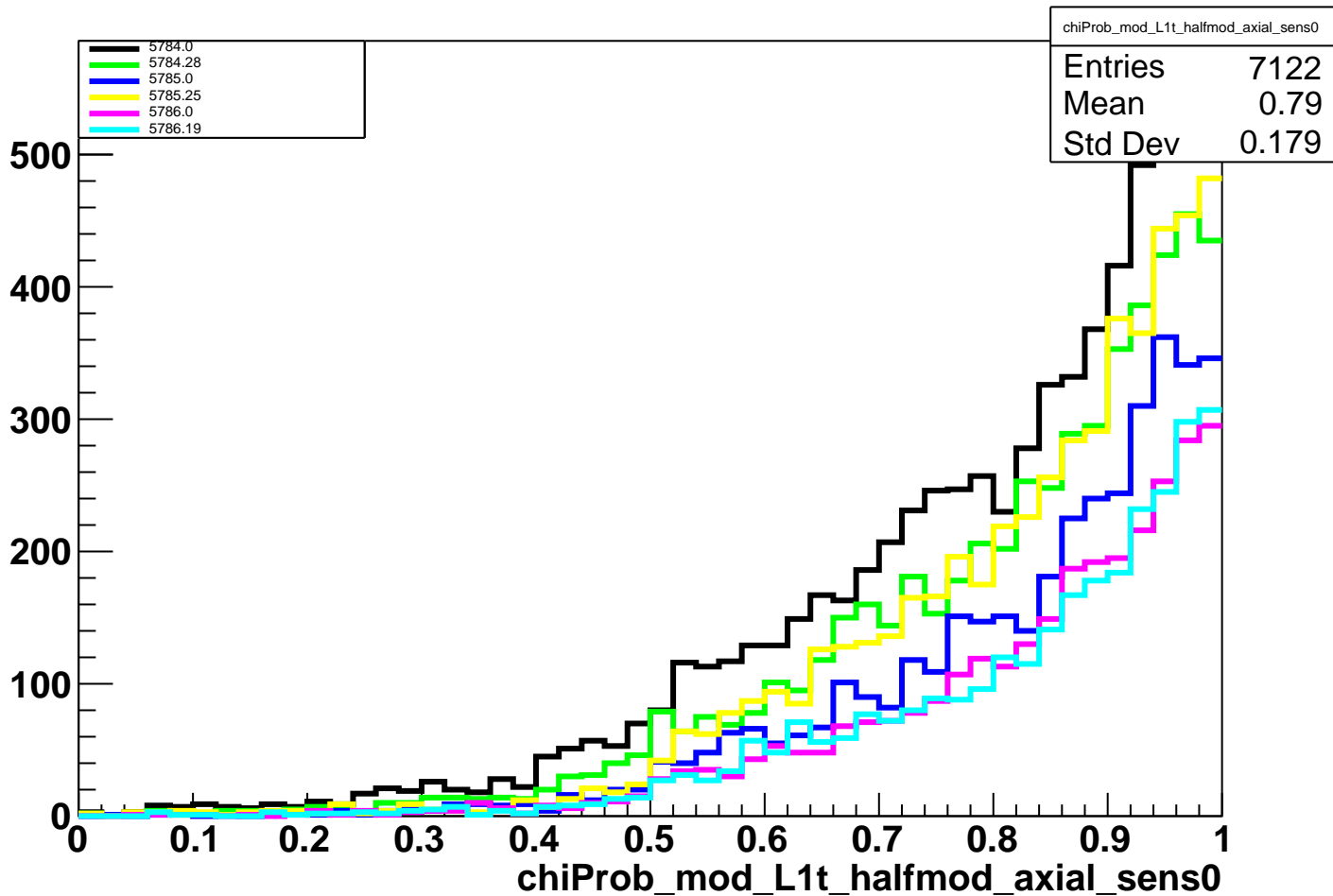
mod_L1b_halfmod_axial_sens0



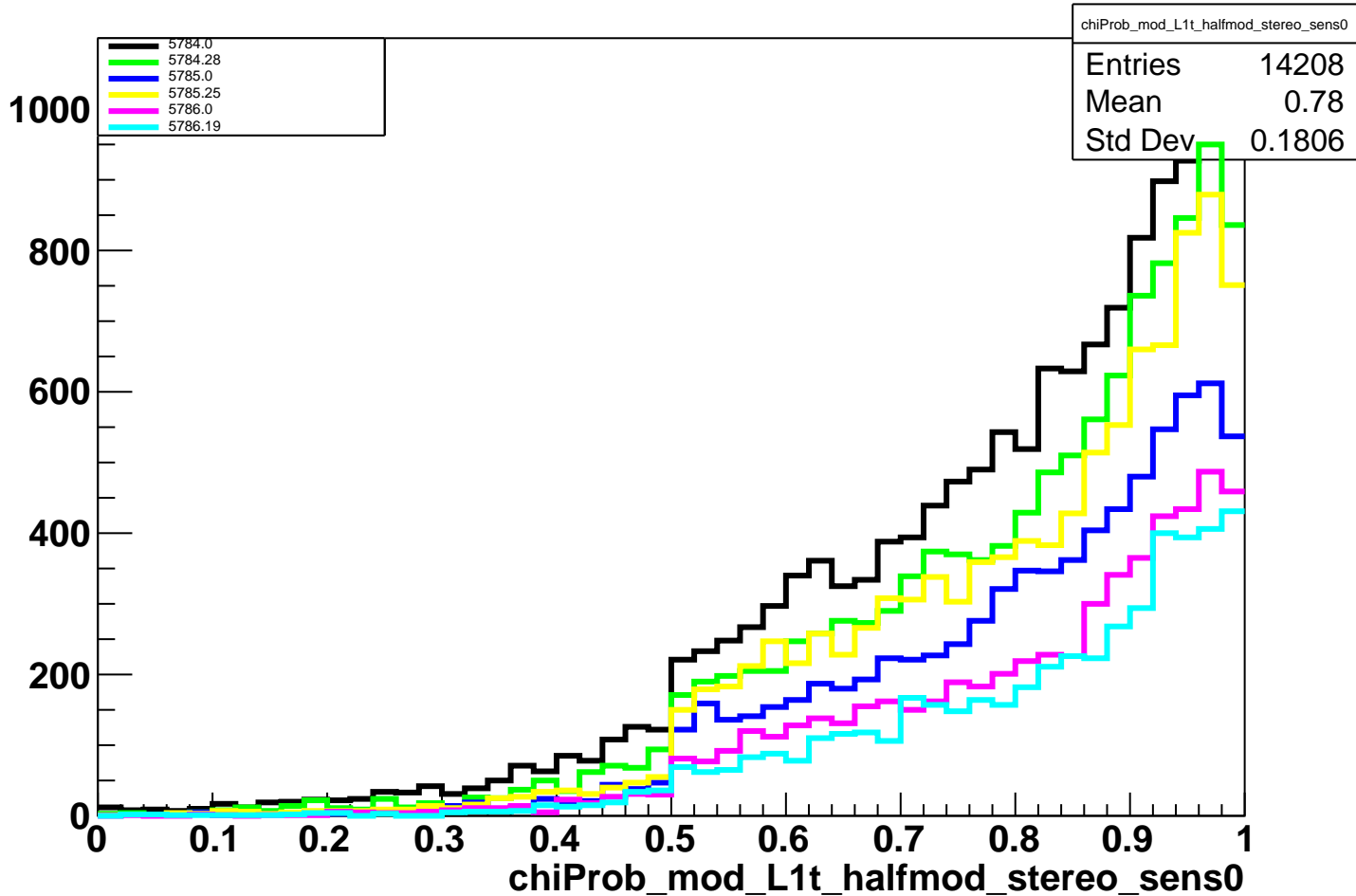
mod_L1b_halfmod_stereo_sens0



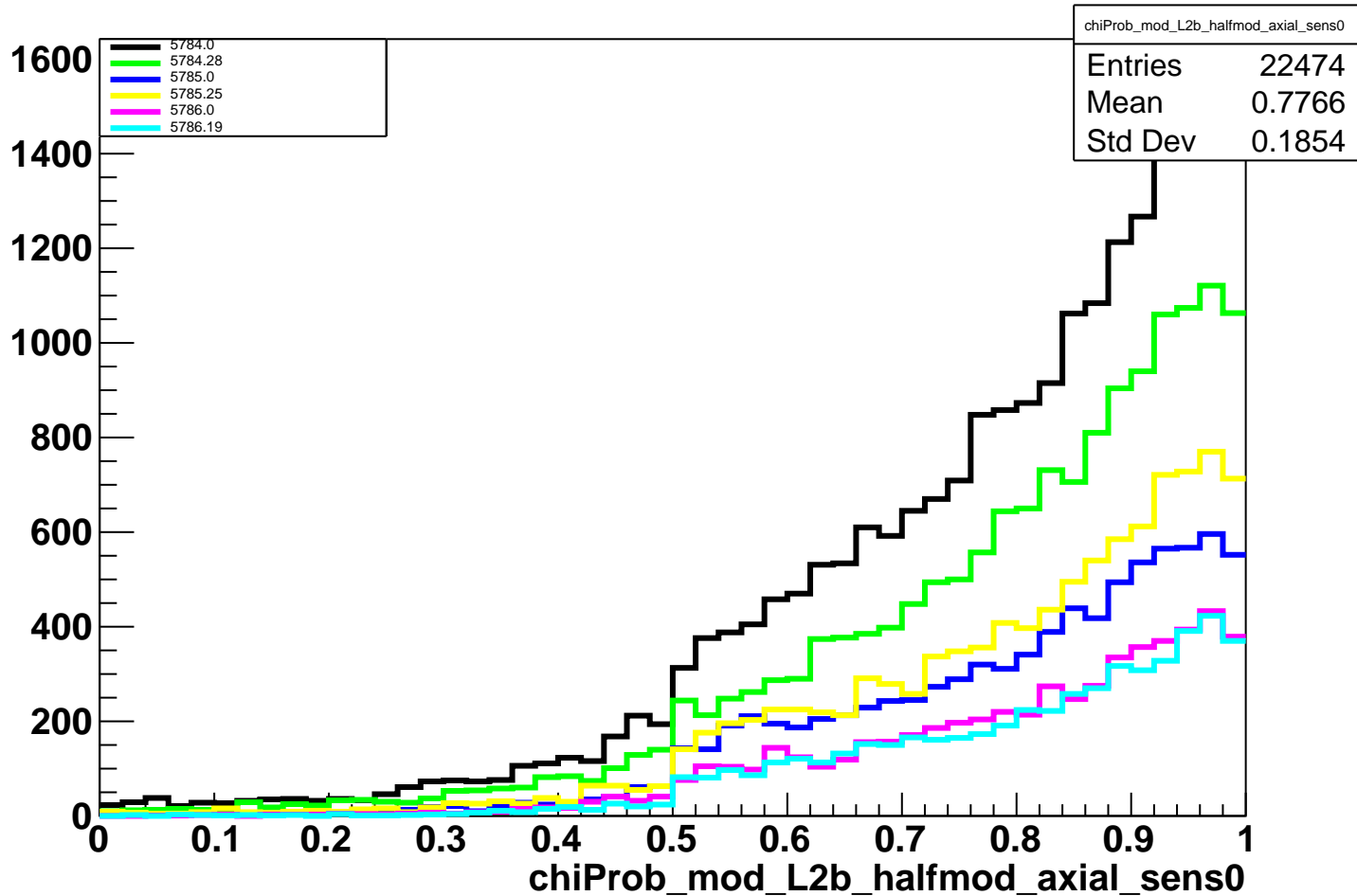
mod_L1t_halfmod_axial_sens0



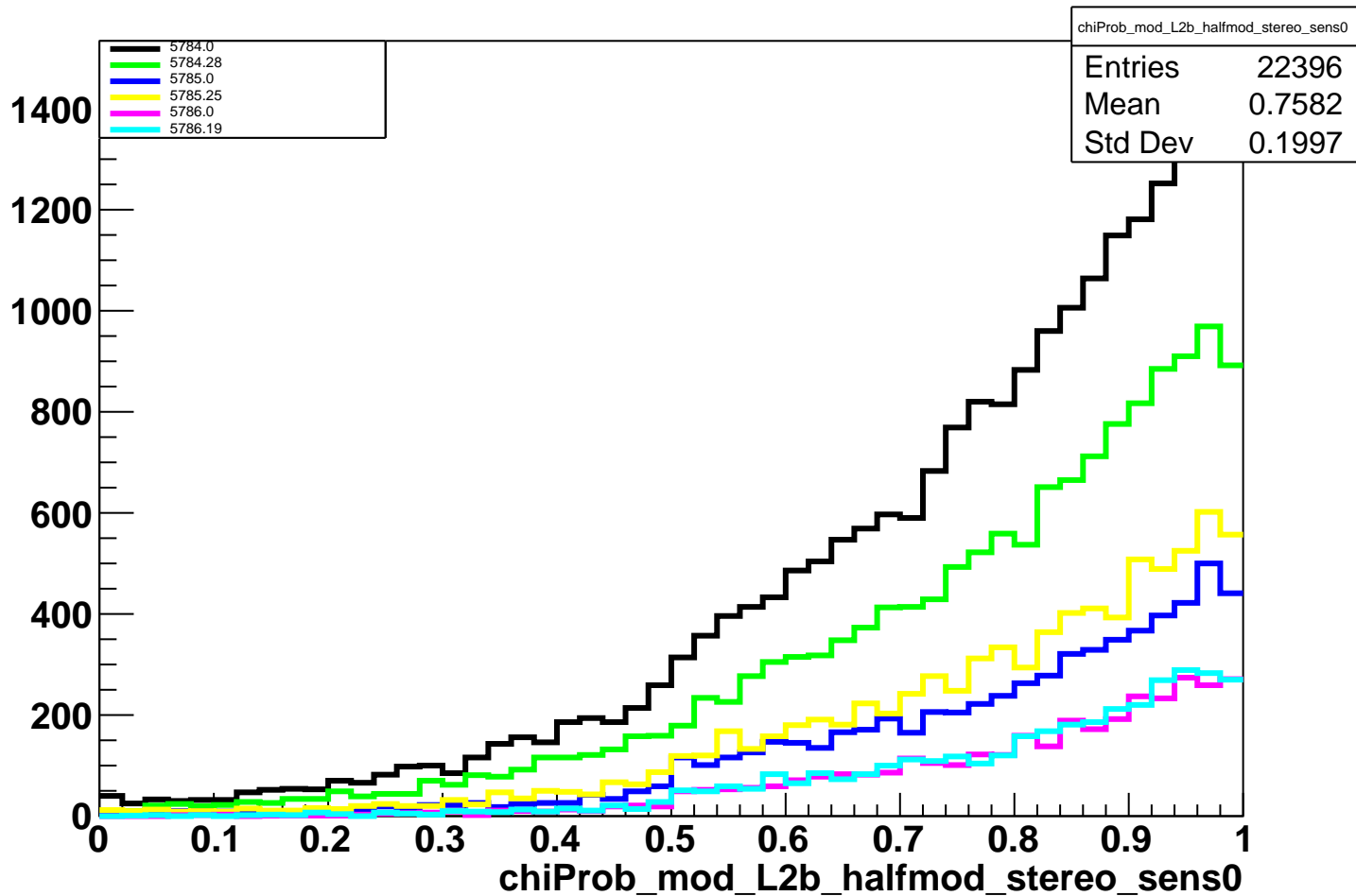
mod_L1t_halfmod_stereo_sens0



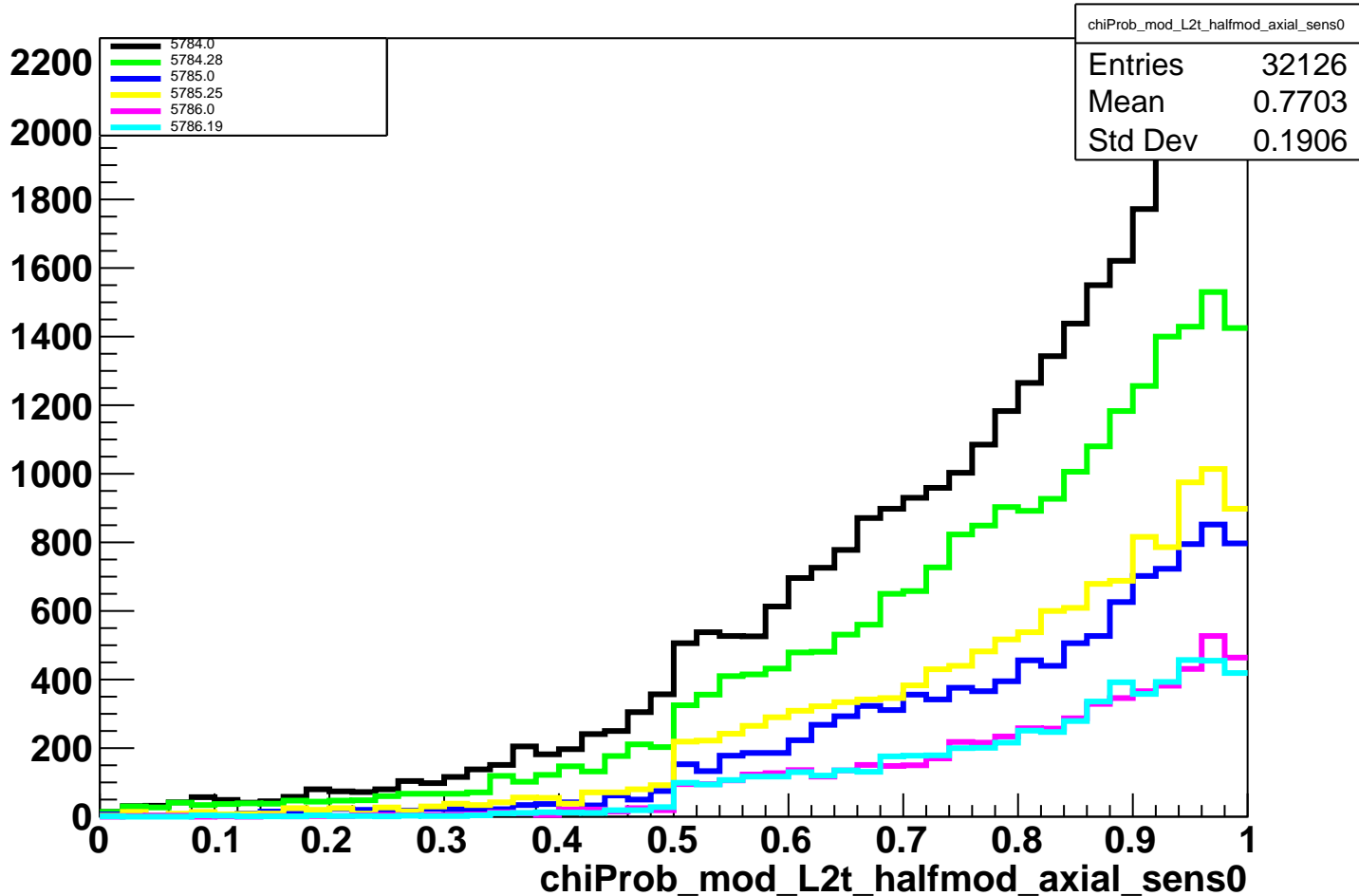
mod_L2b_halfmod_axial_sens0



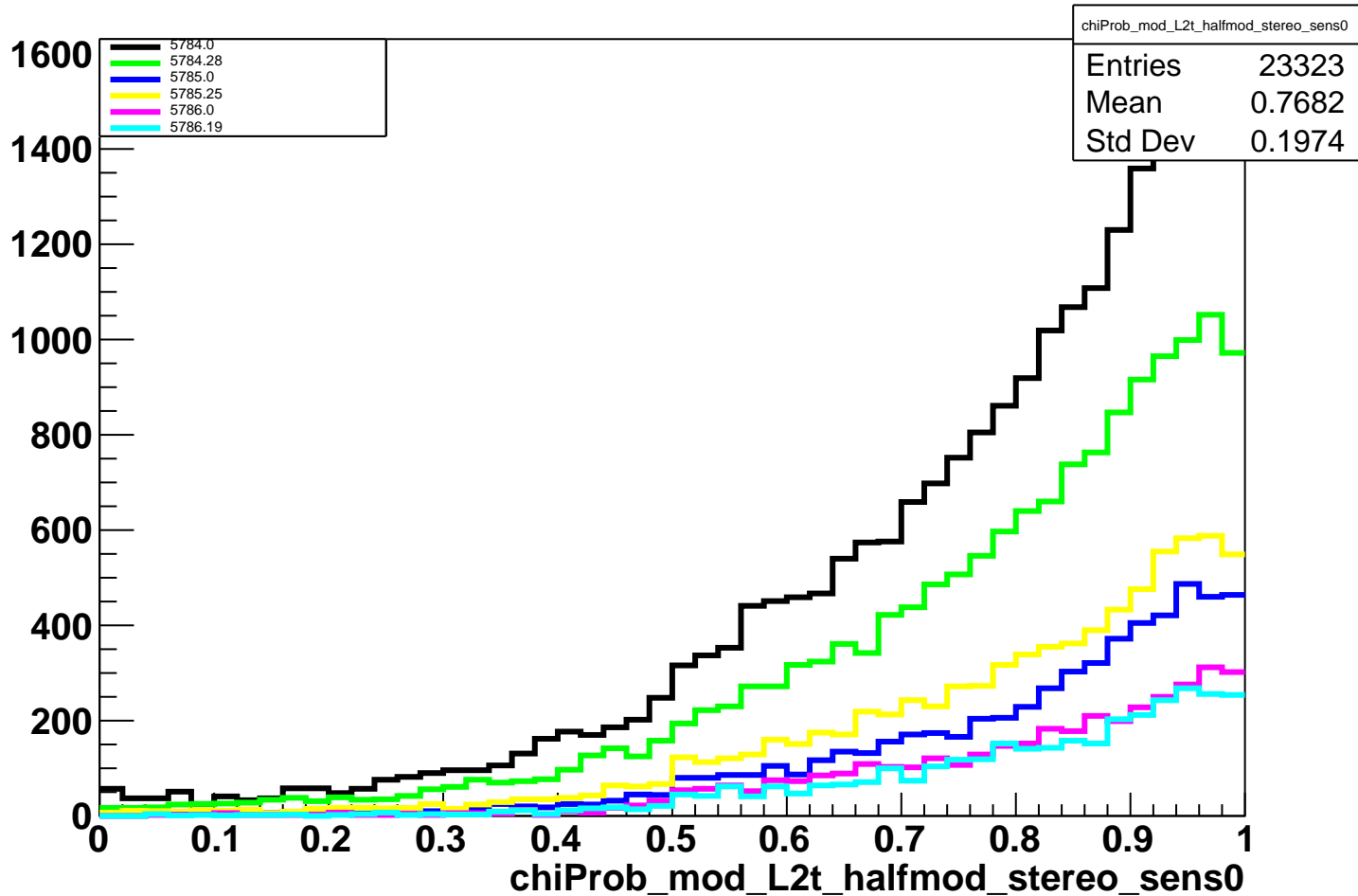
mod_L2b_halfmod_stereo_sens0



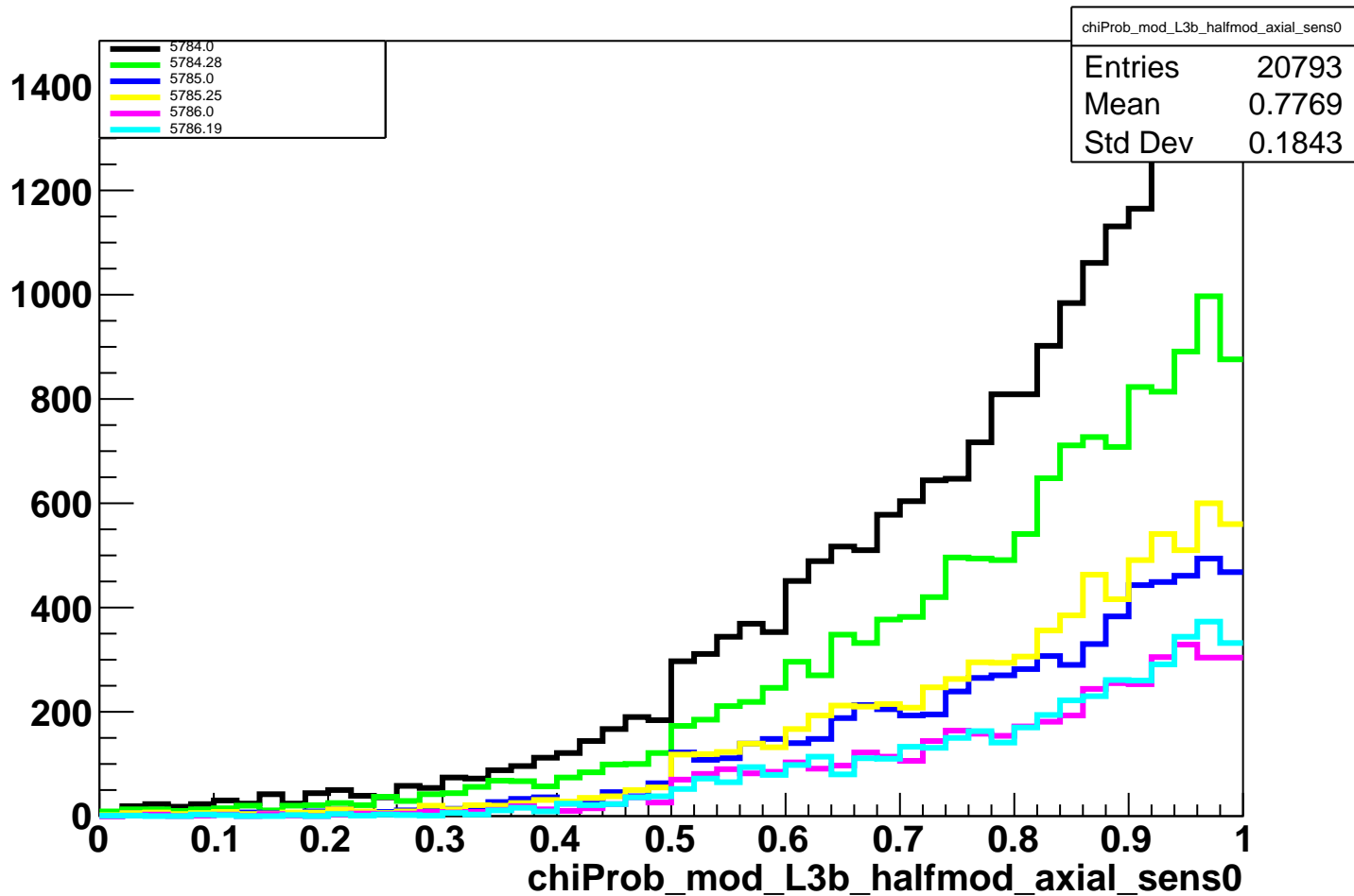
mod_L2t_halfmod_axial_sens0



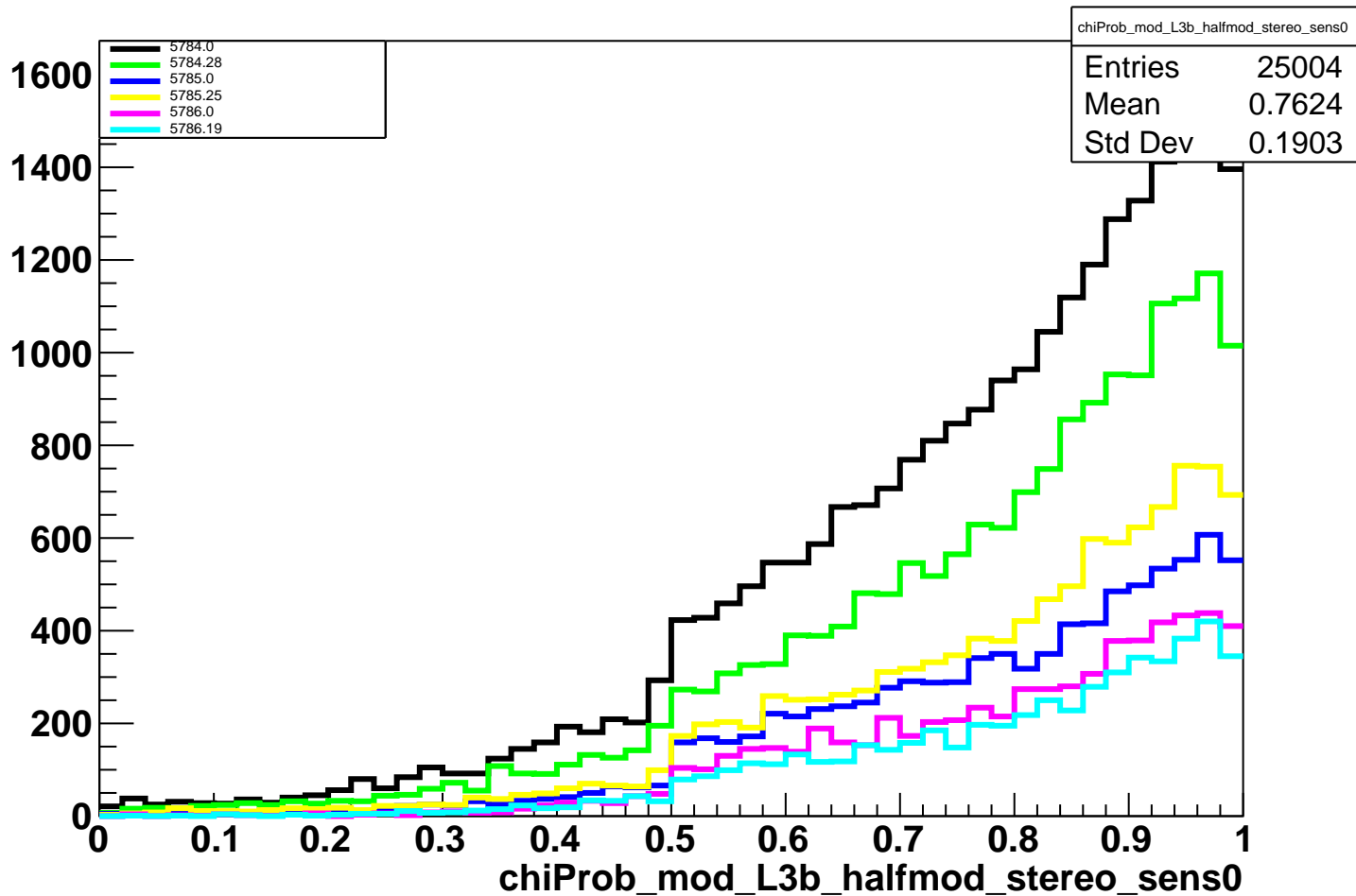
mod_L2t_halfmod_stereo_sens0



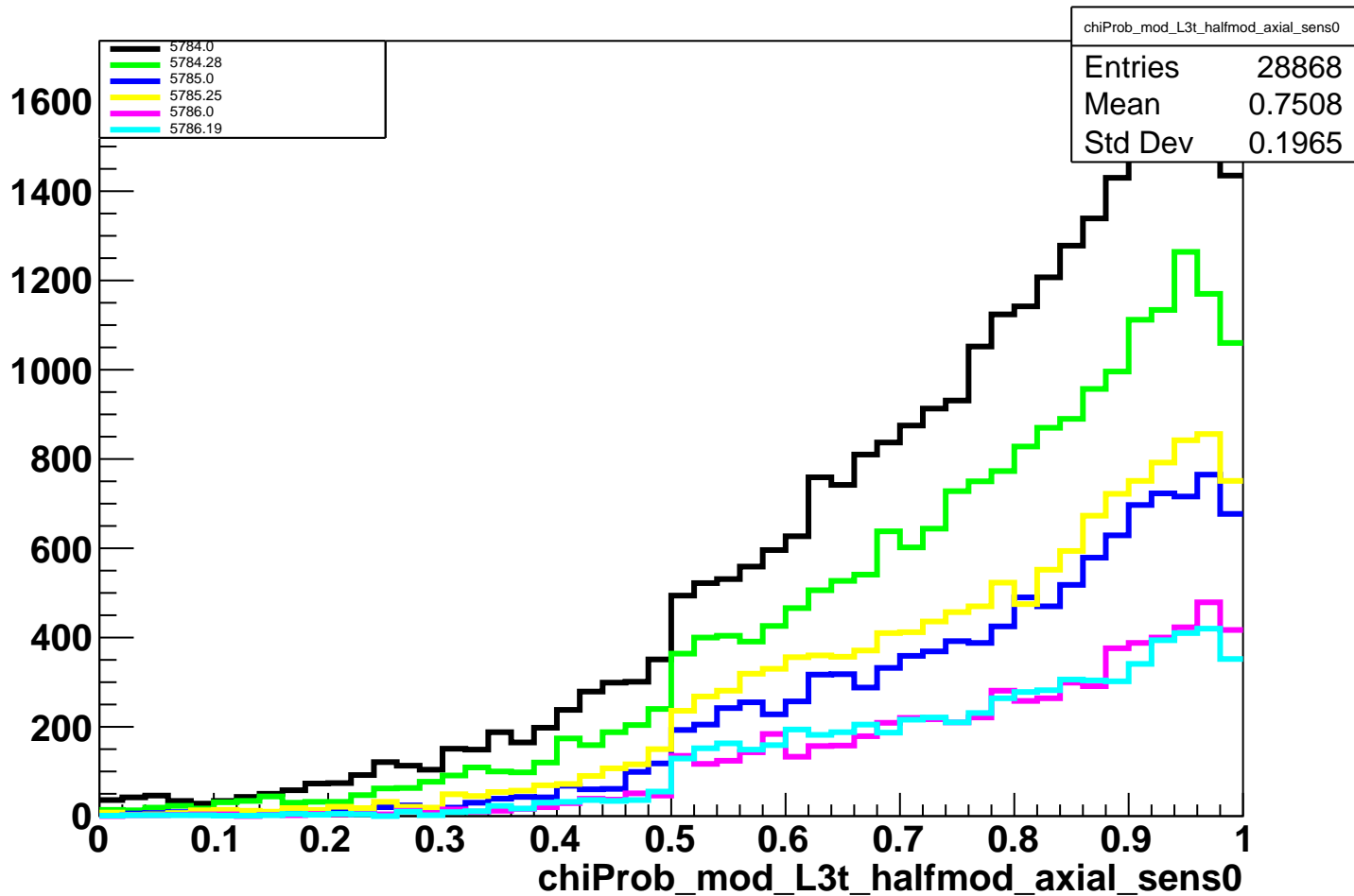
mod_L3b_halfmod_axial_sens0



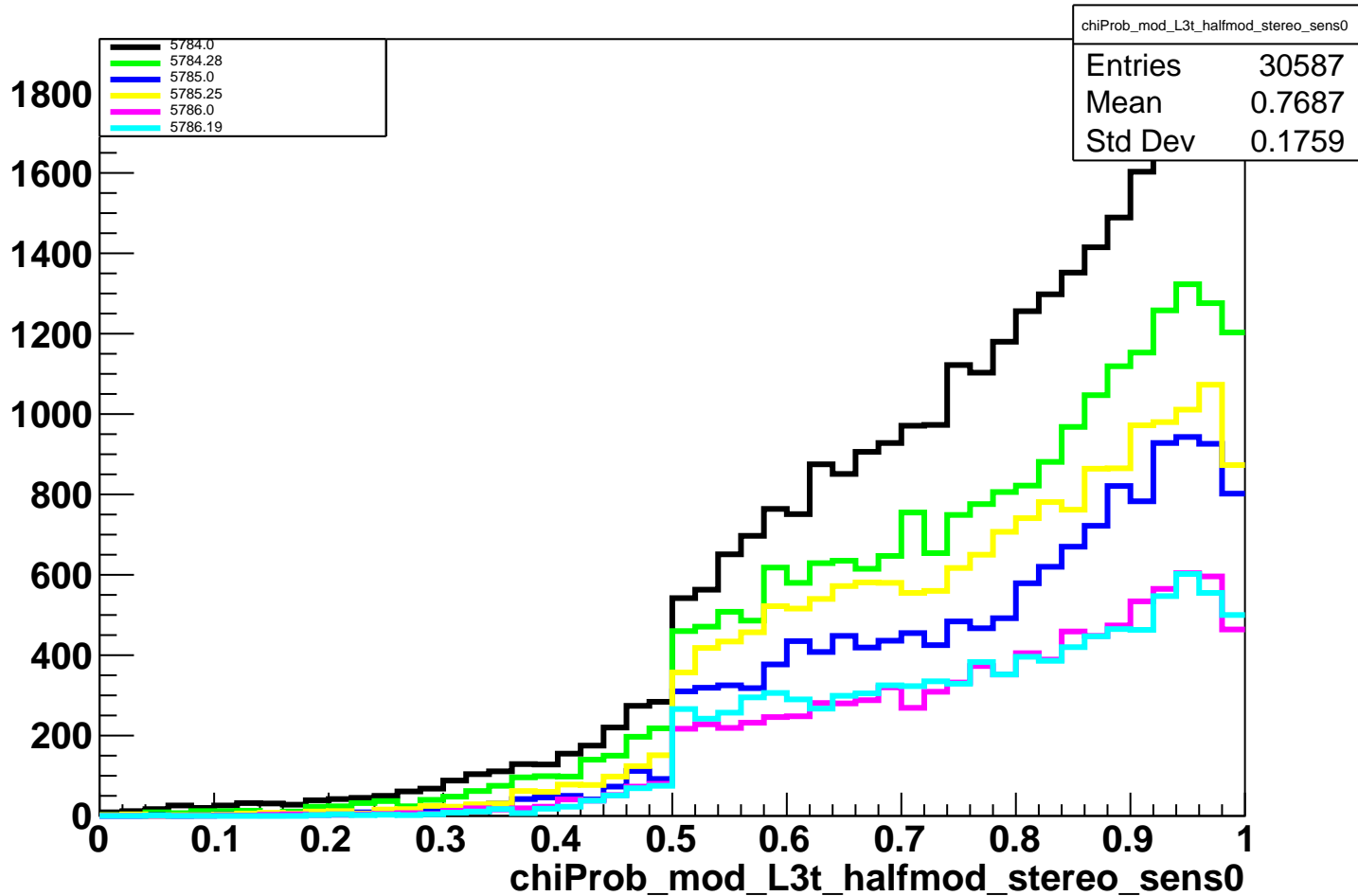
mod_L3b_halfmod_stereo_sens0



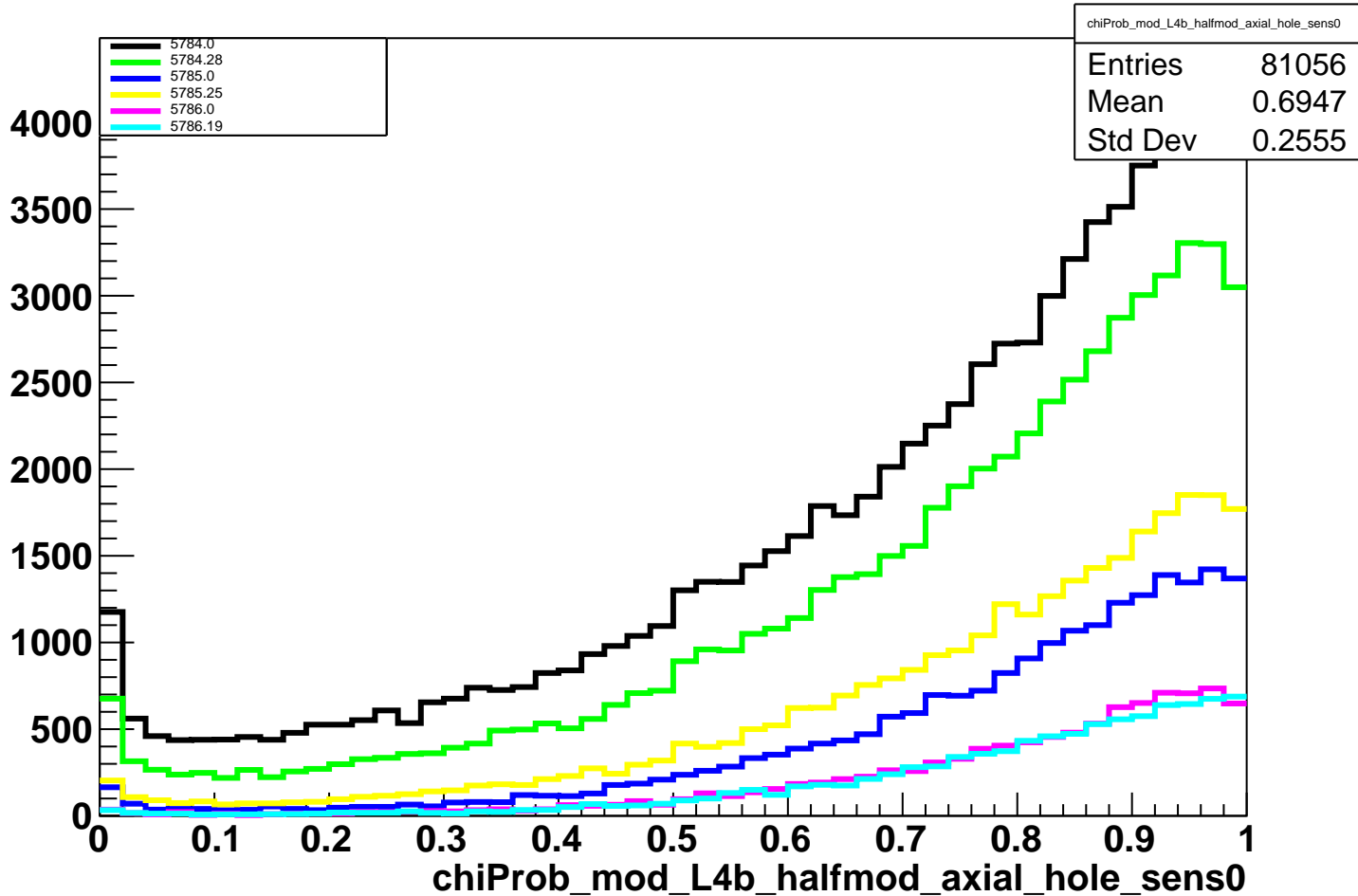
mod_L3t_halfmod_axial_sens0



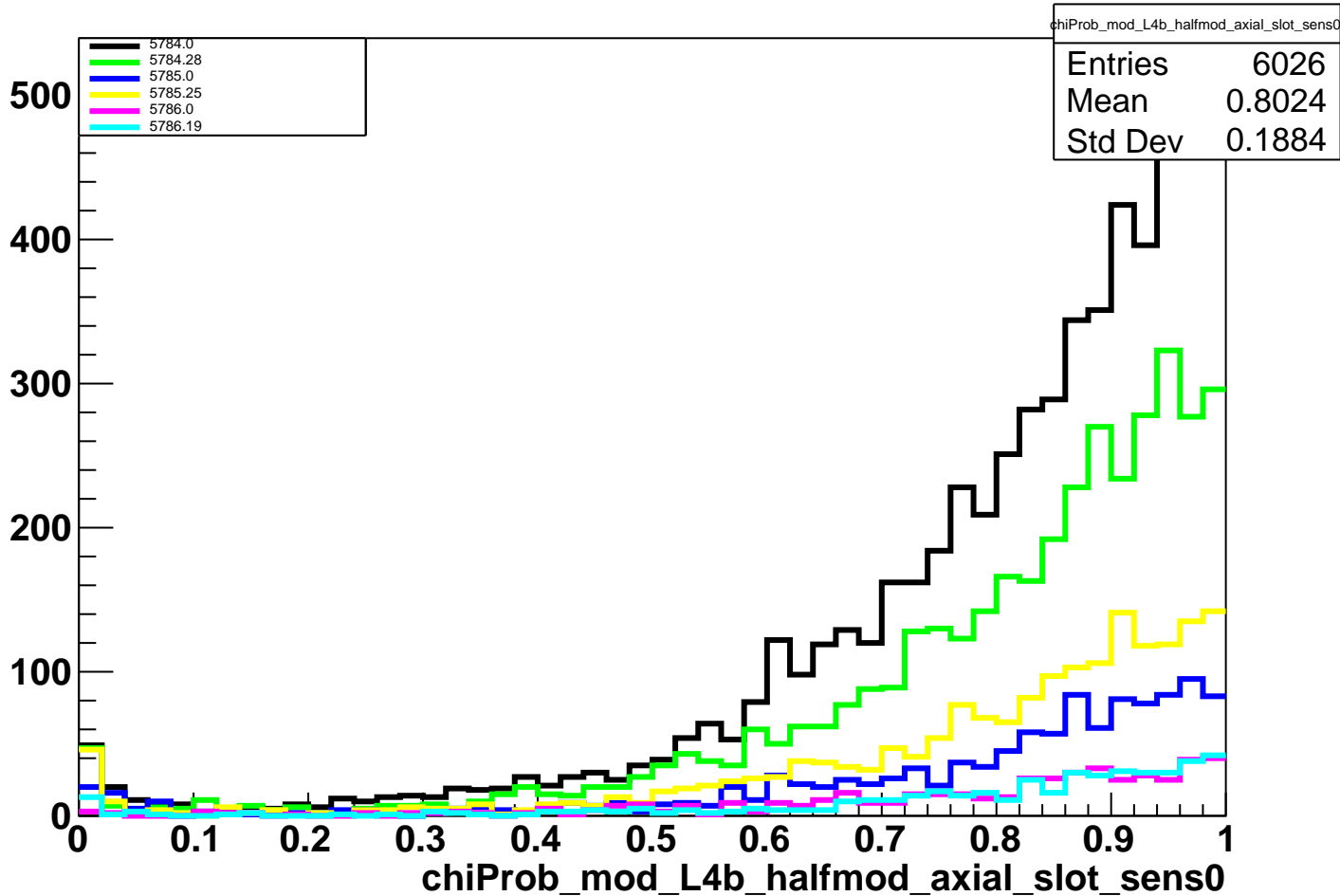
mod_L3t_halfmod_stereo_sens0



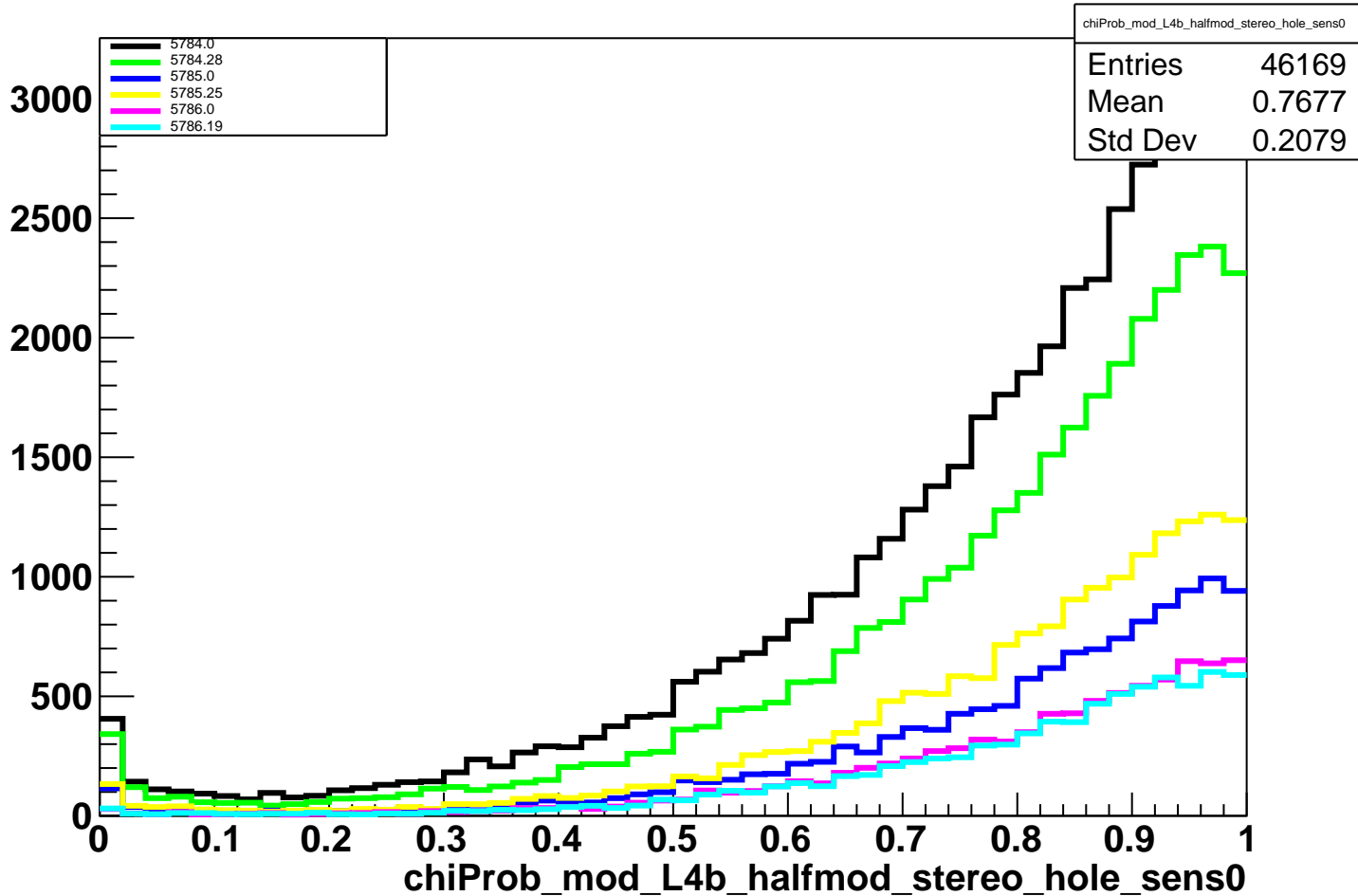
mod_L4b_halfmod_axial_hole_sens0



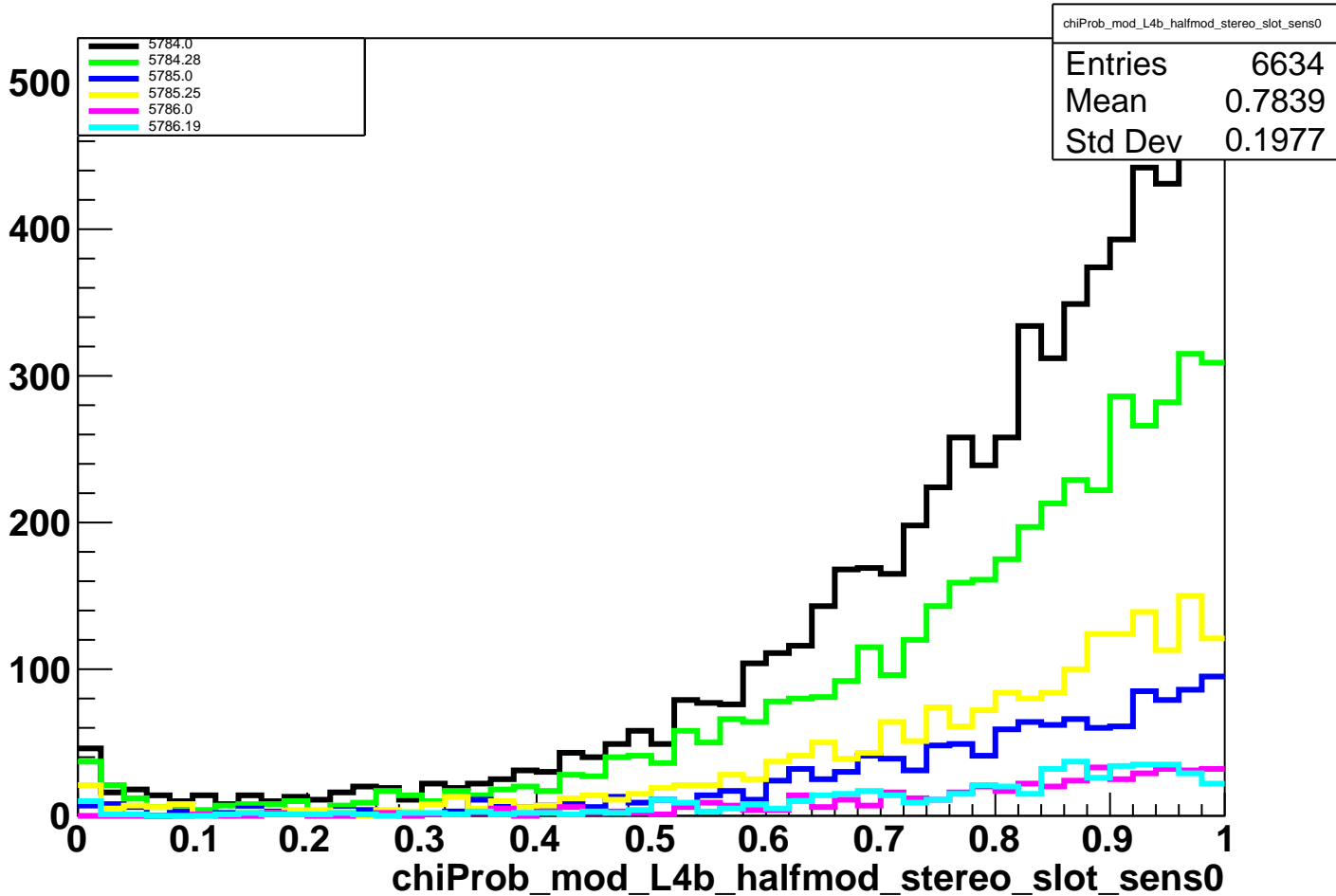
mod_L4b_halfmod_axial_slot_sens0



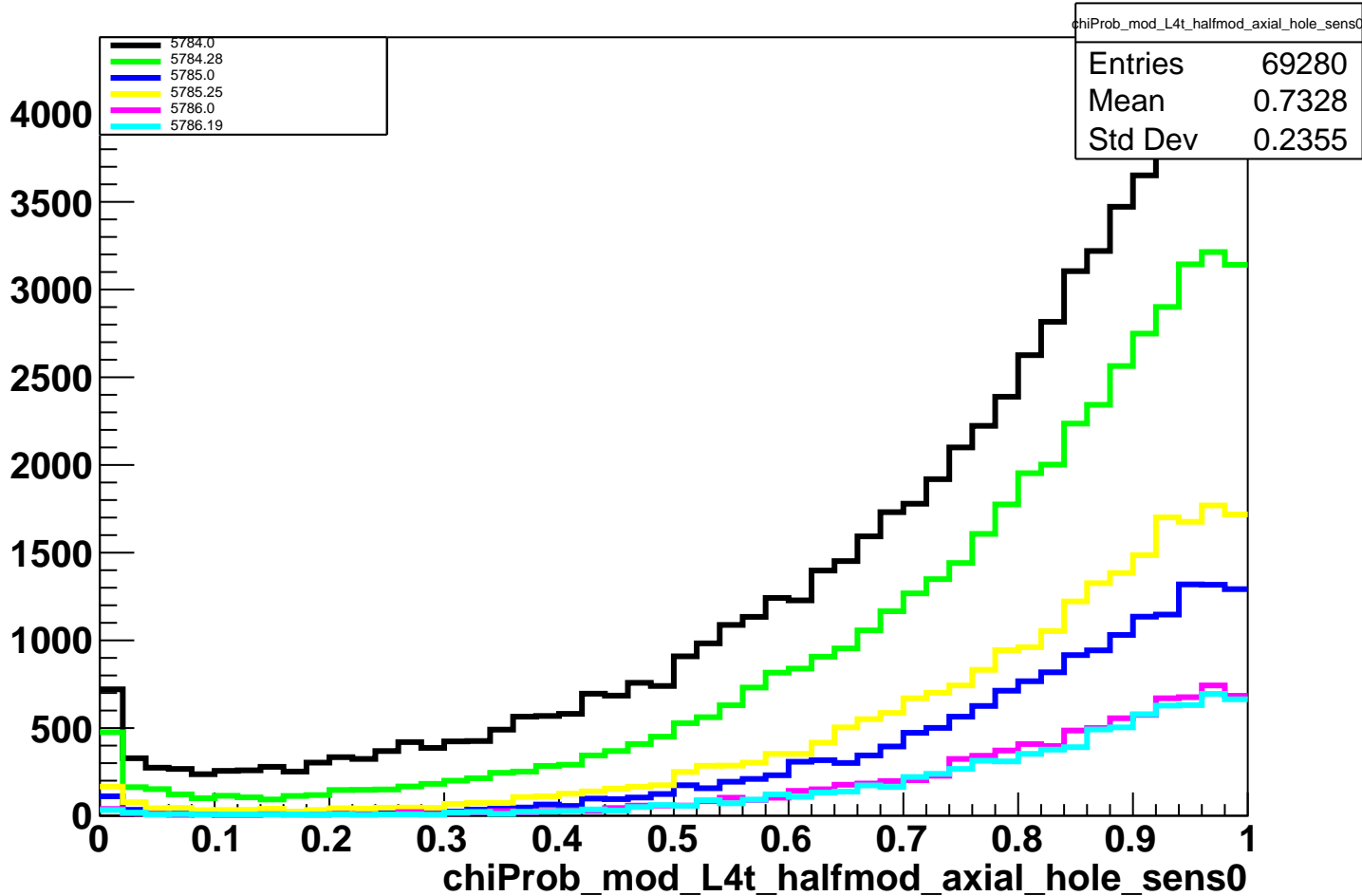
mod_L4b_halfmod_stereo_hole_sens0



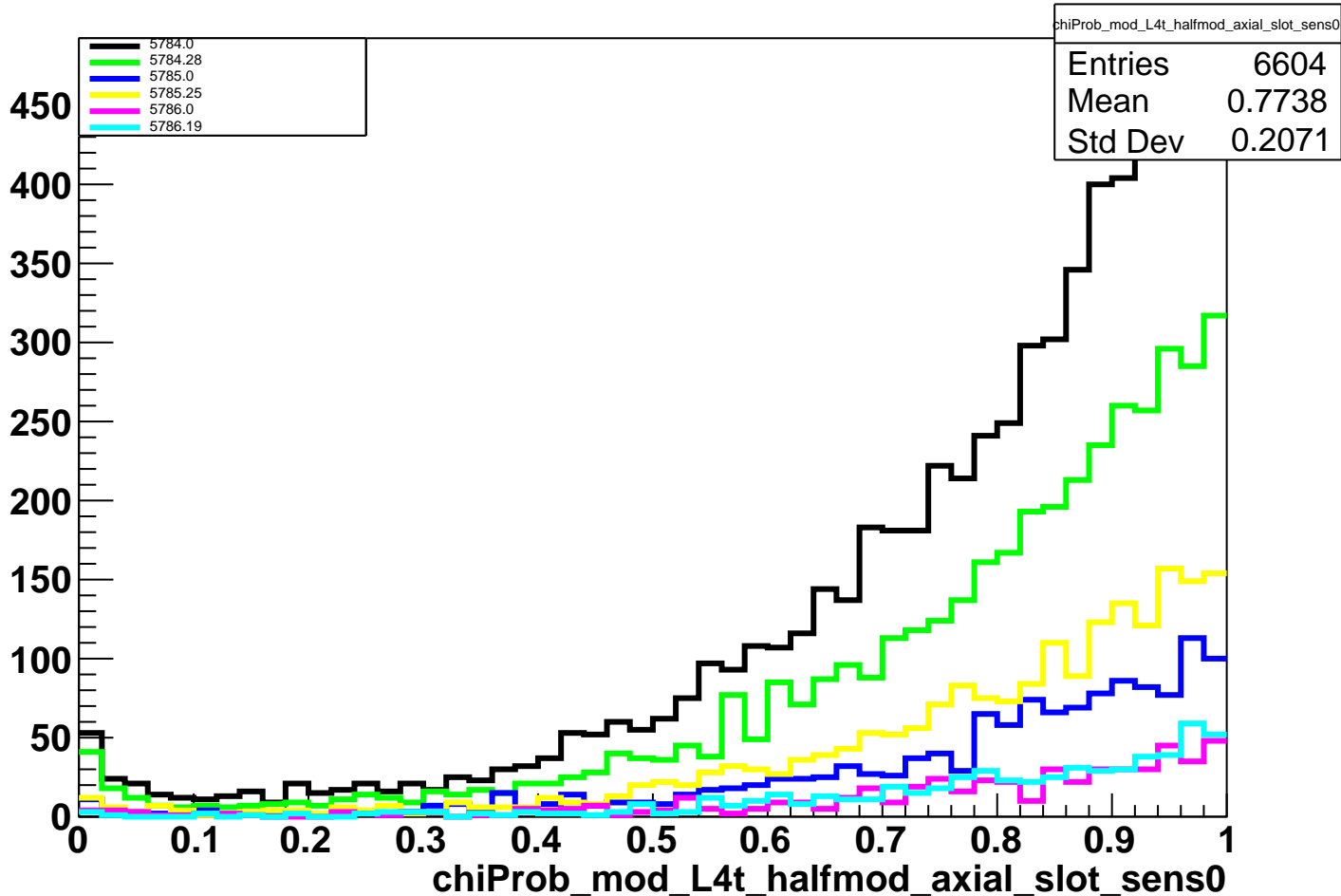
mod_L4b_halfmod_stereo_slot_sens0



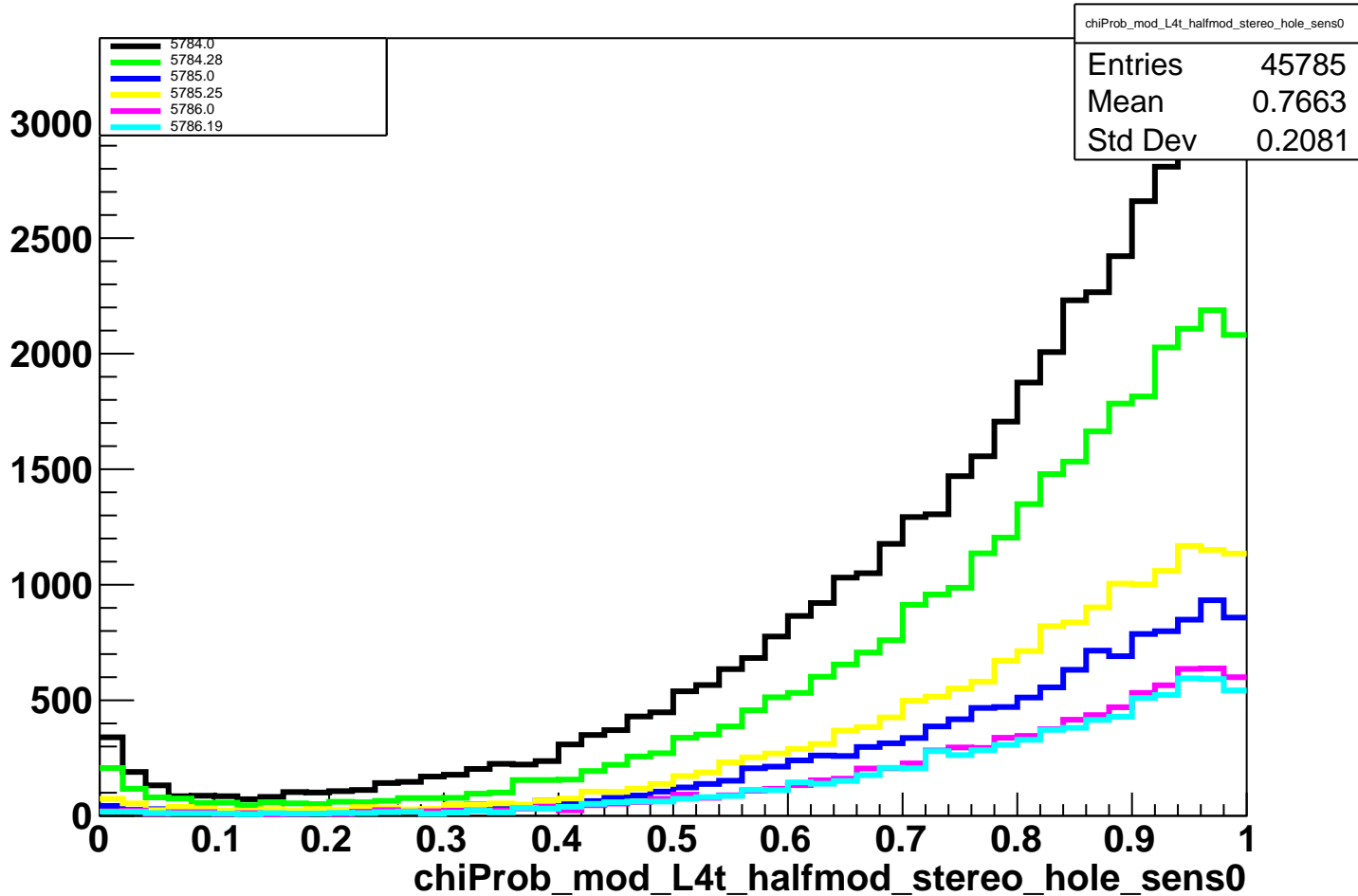
mod_L4t_halfmod_axial_hole_sens0



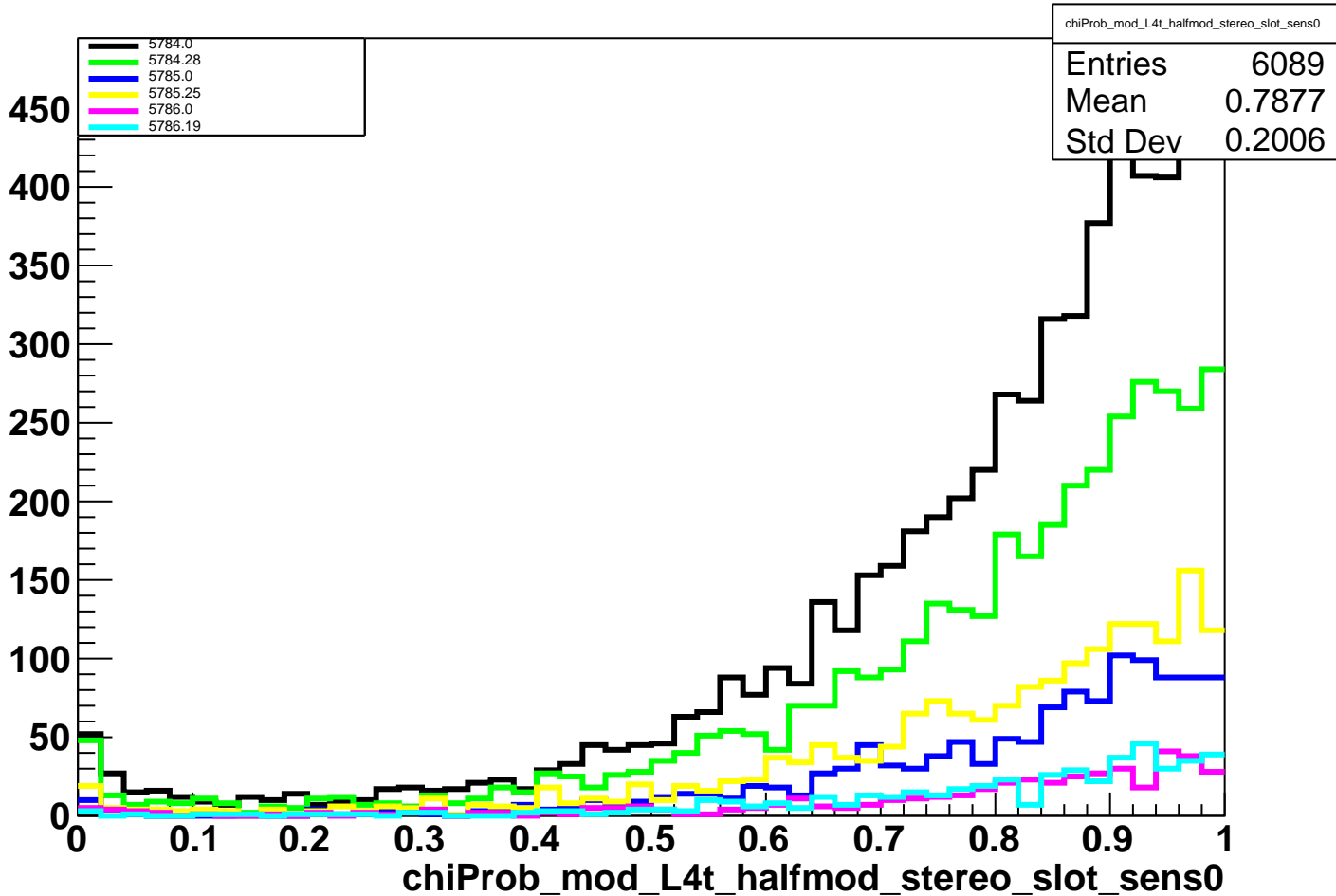
mod_L4t_halfmod_axial_slot_sens0



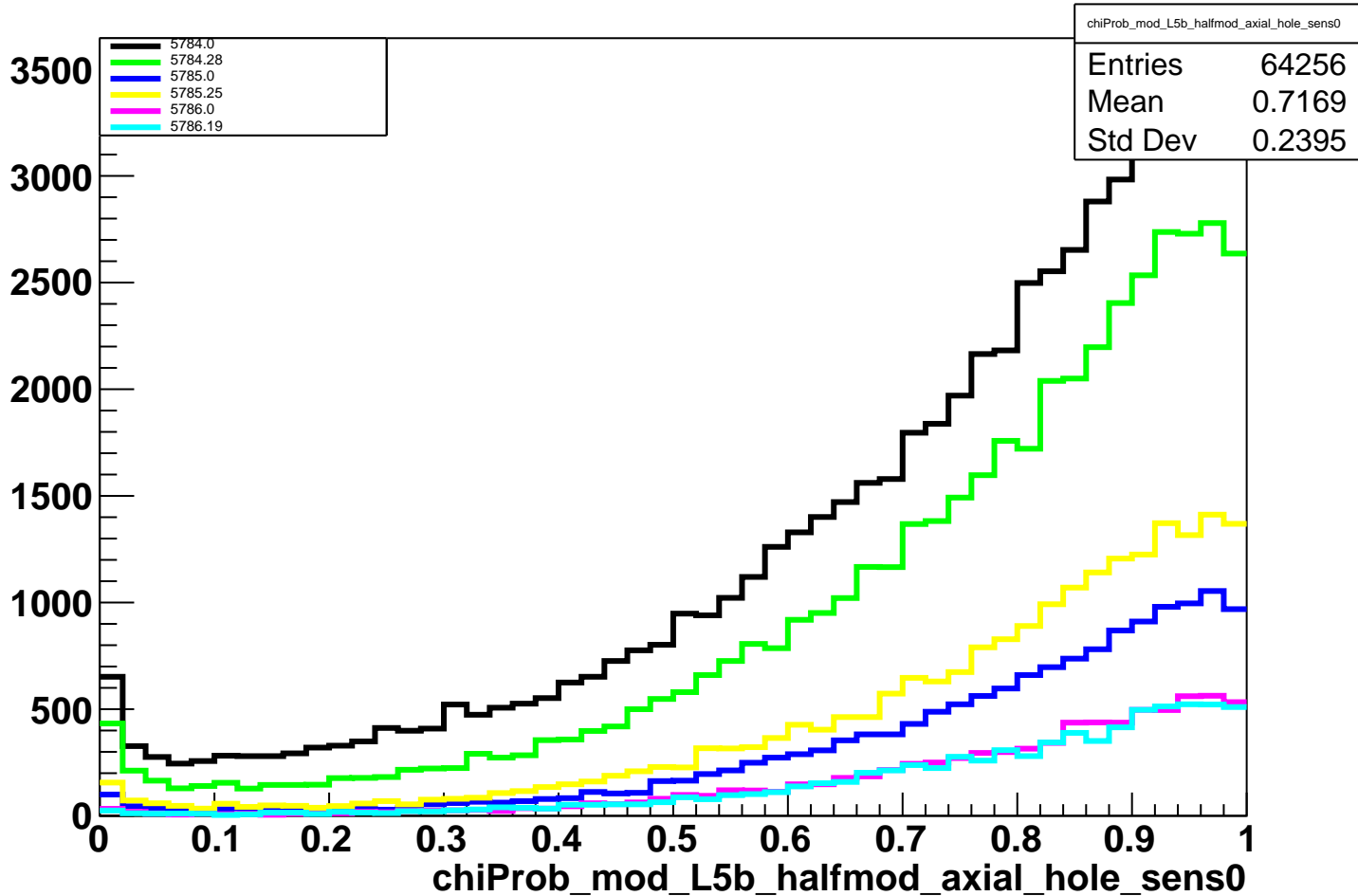
mod_L4t_halfmod_stereo_hole_sens0



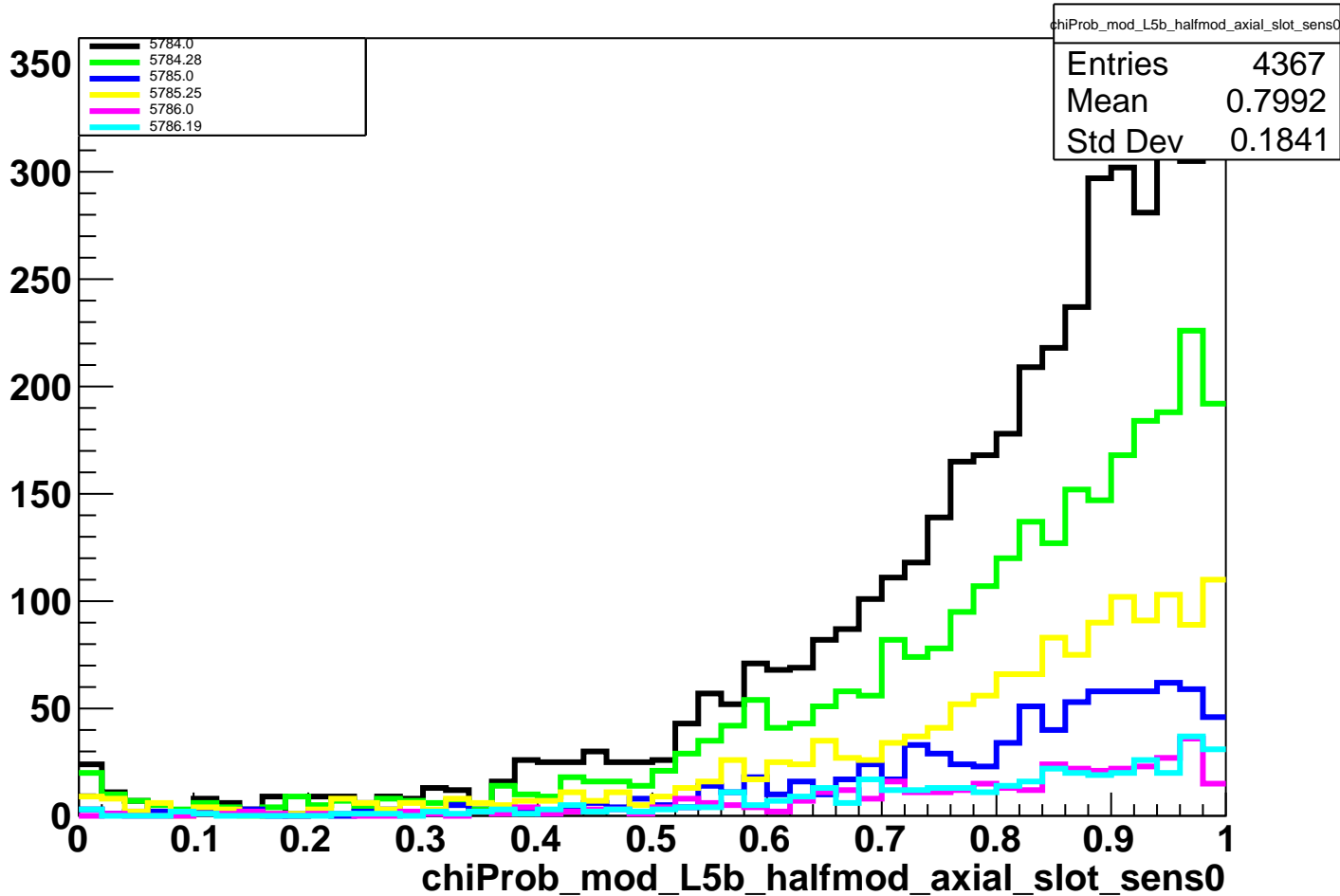
mod_L4t_halfmod_stereo_slot_sens0



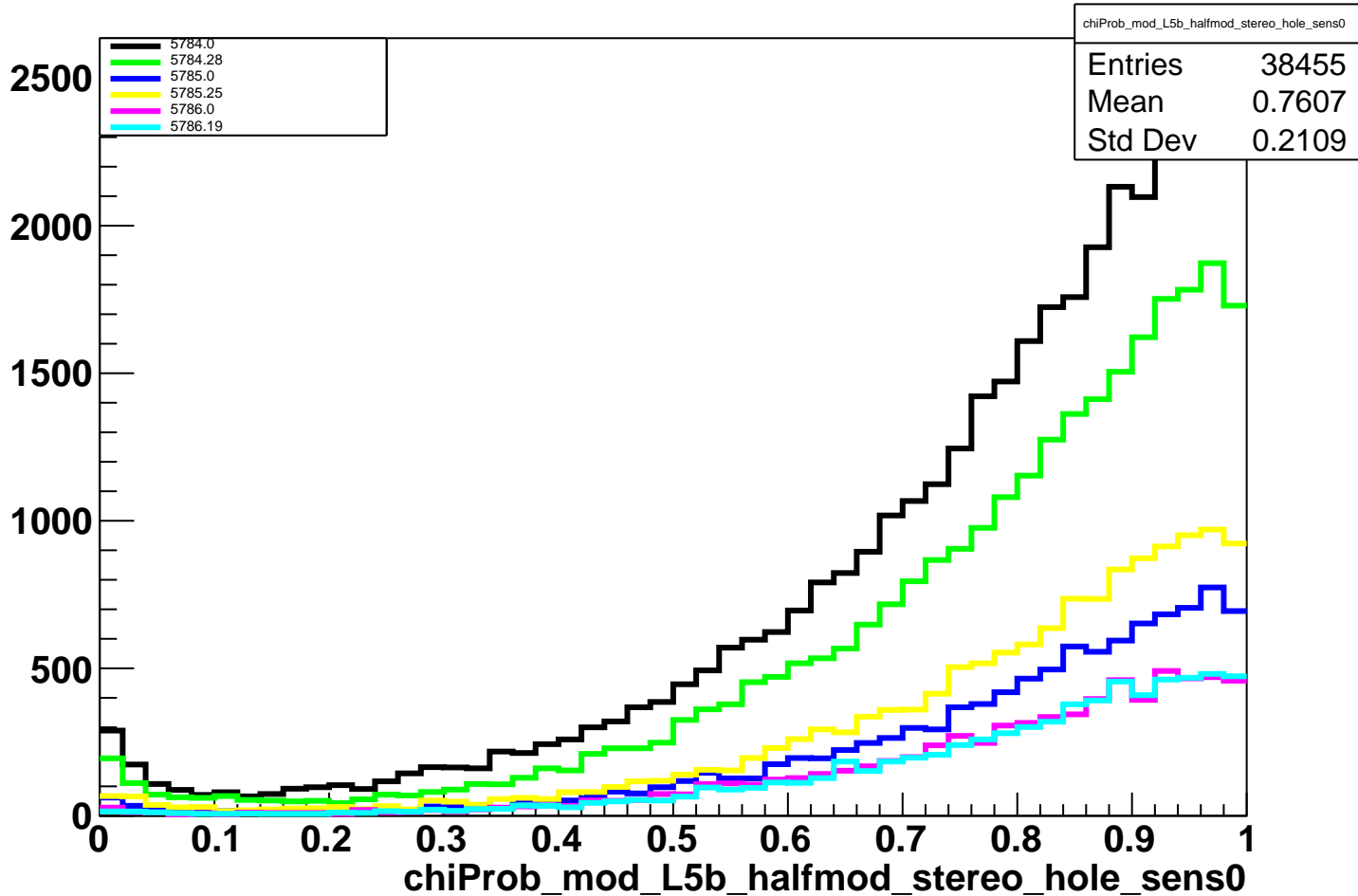
mod_L5b_halfmod_axial_hole_sens0



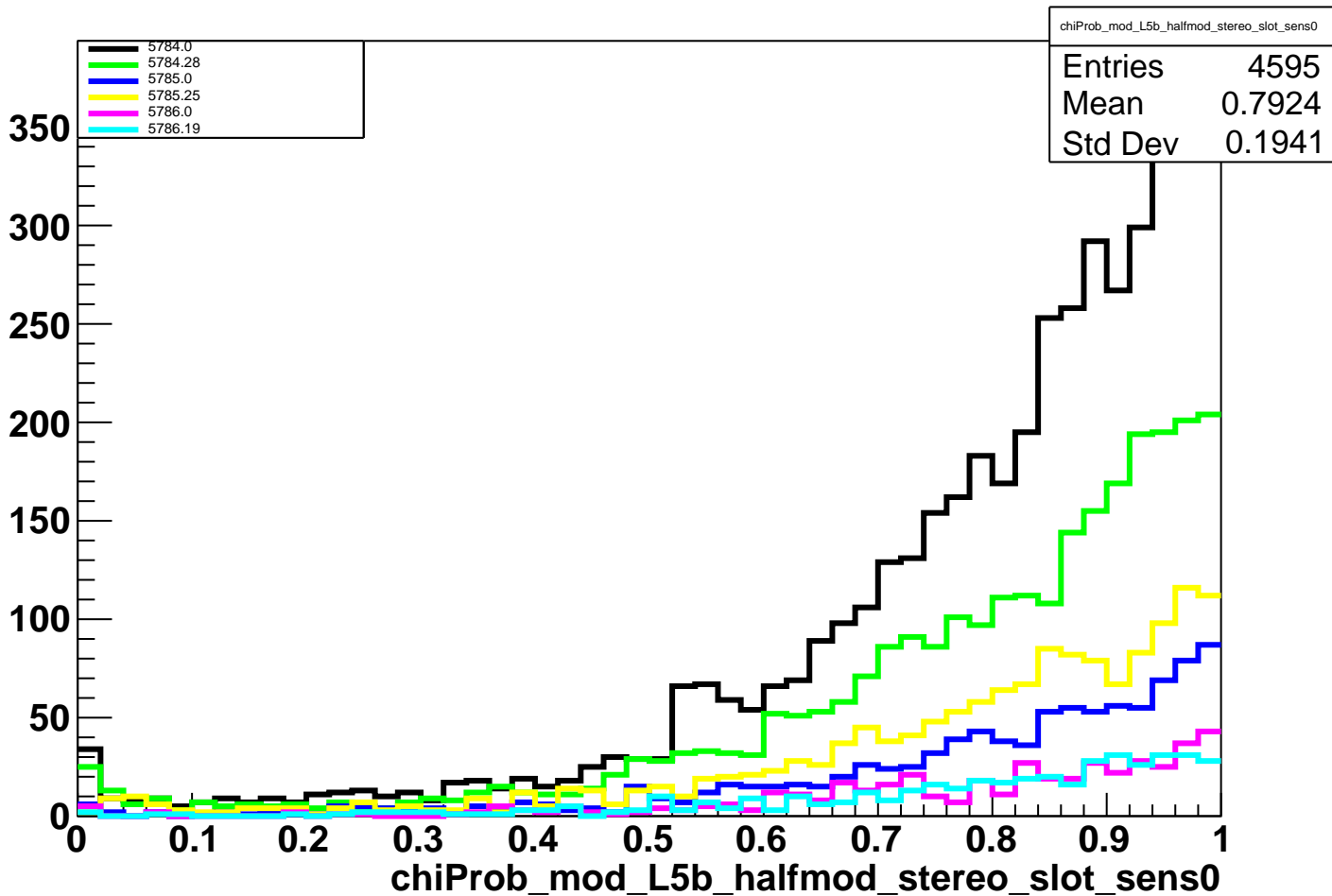
mod_L5b_halfmod_axial_slot_sens0



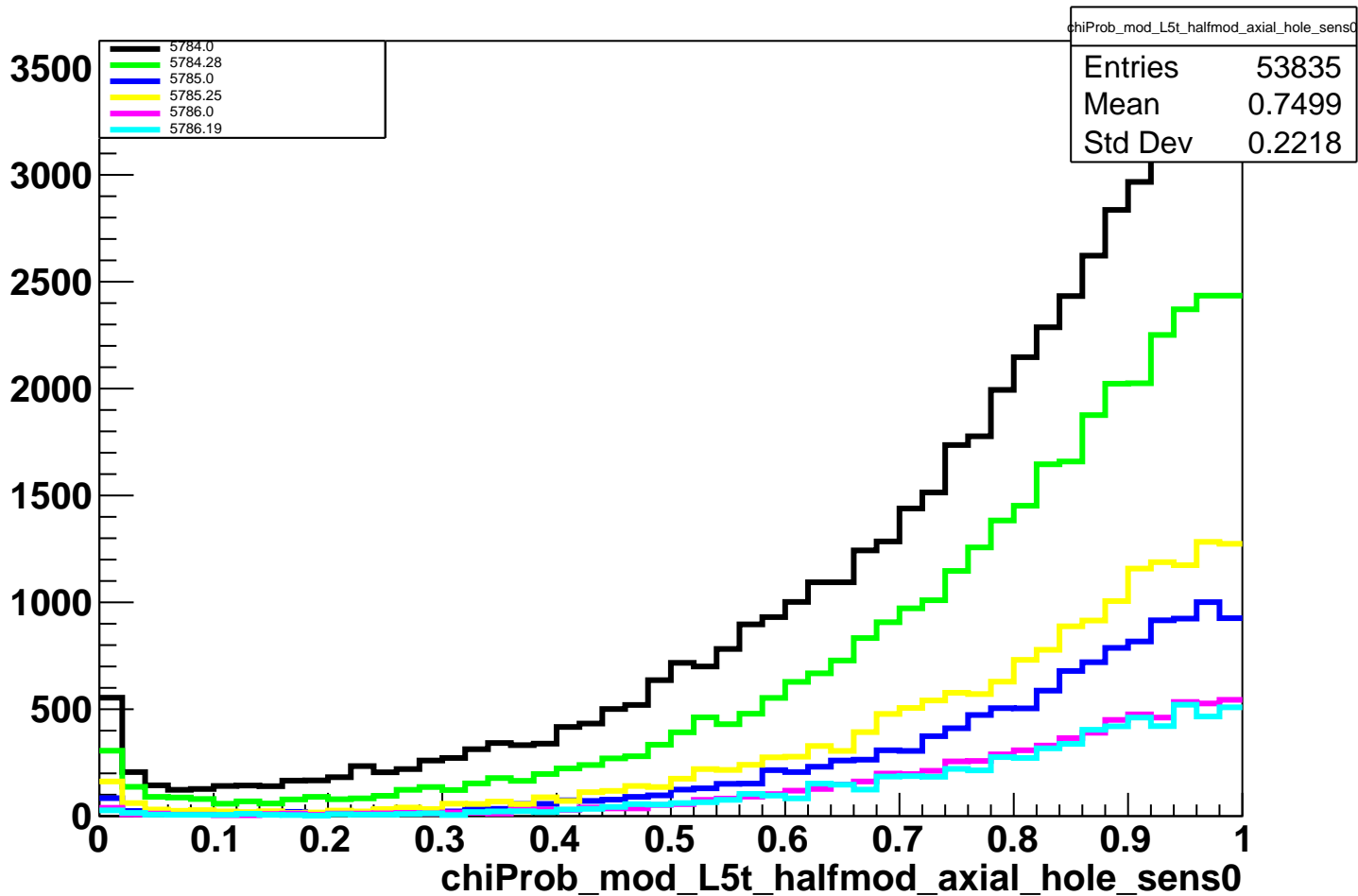
mod_L5b_halfmod_stereo_hole_sens0



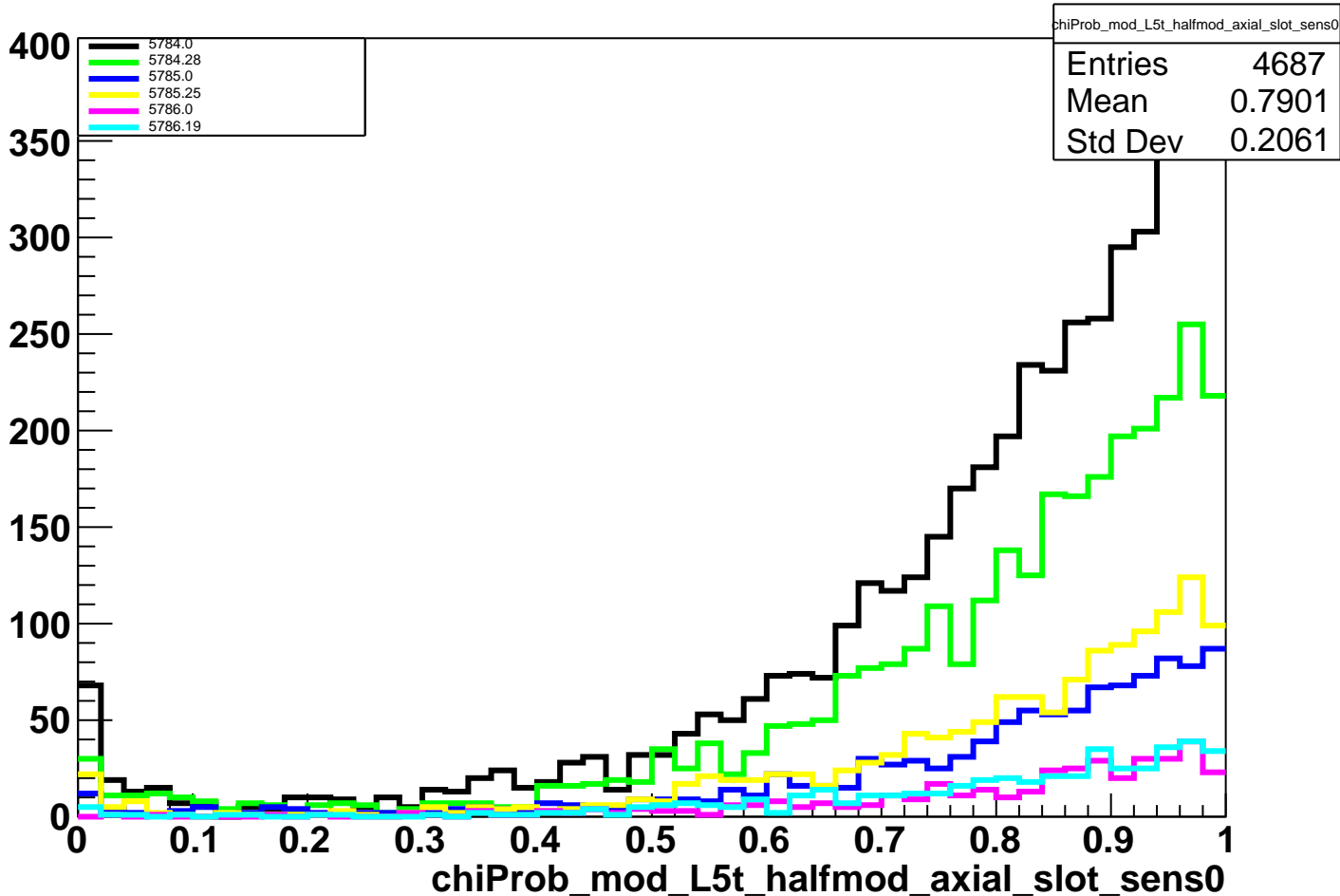
mod_L5b_halfmod_stereo_slot_sens0



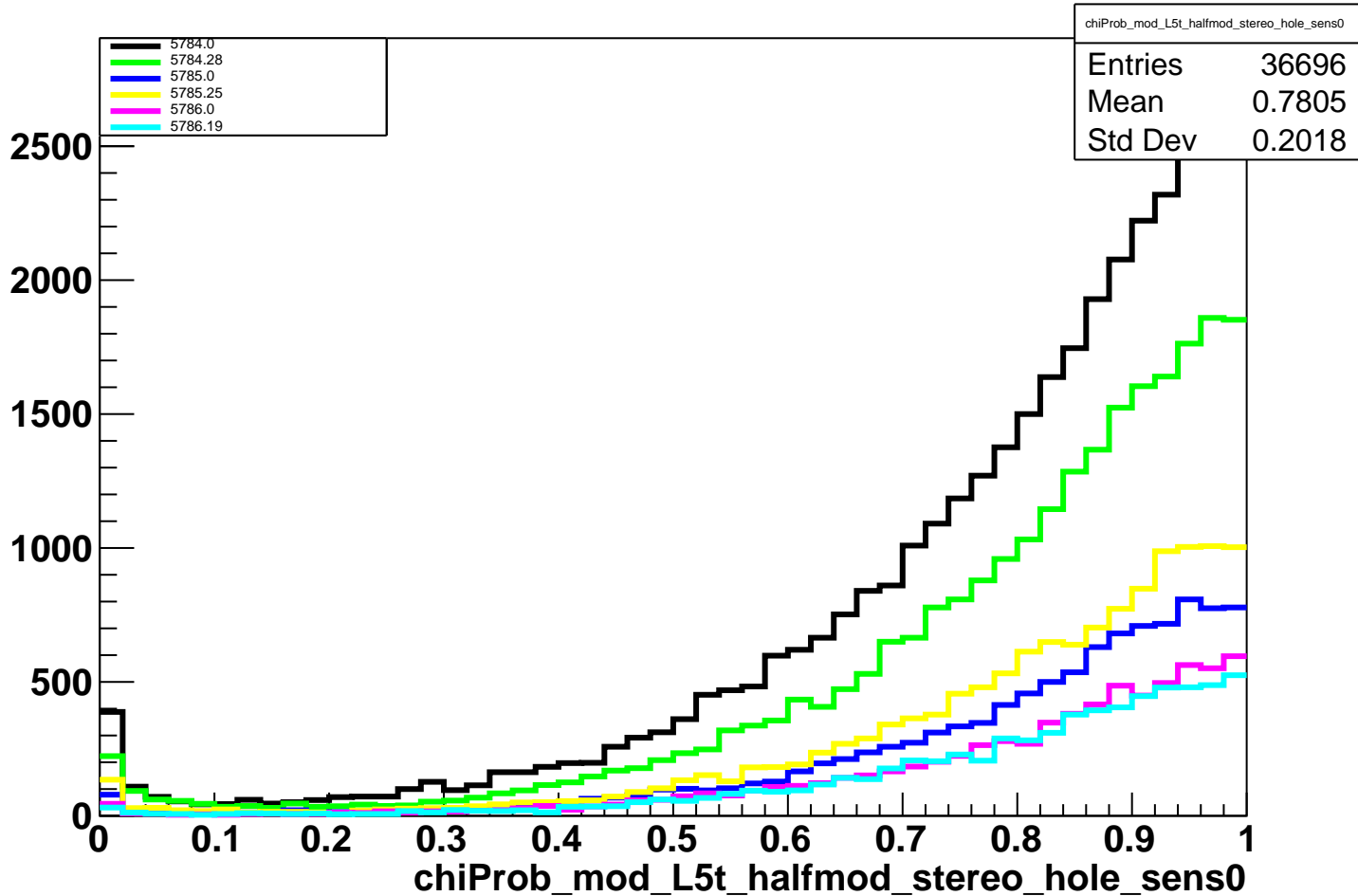
mod_L5t_halfmod_axial_hole_sens0



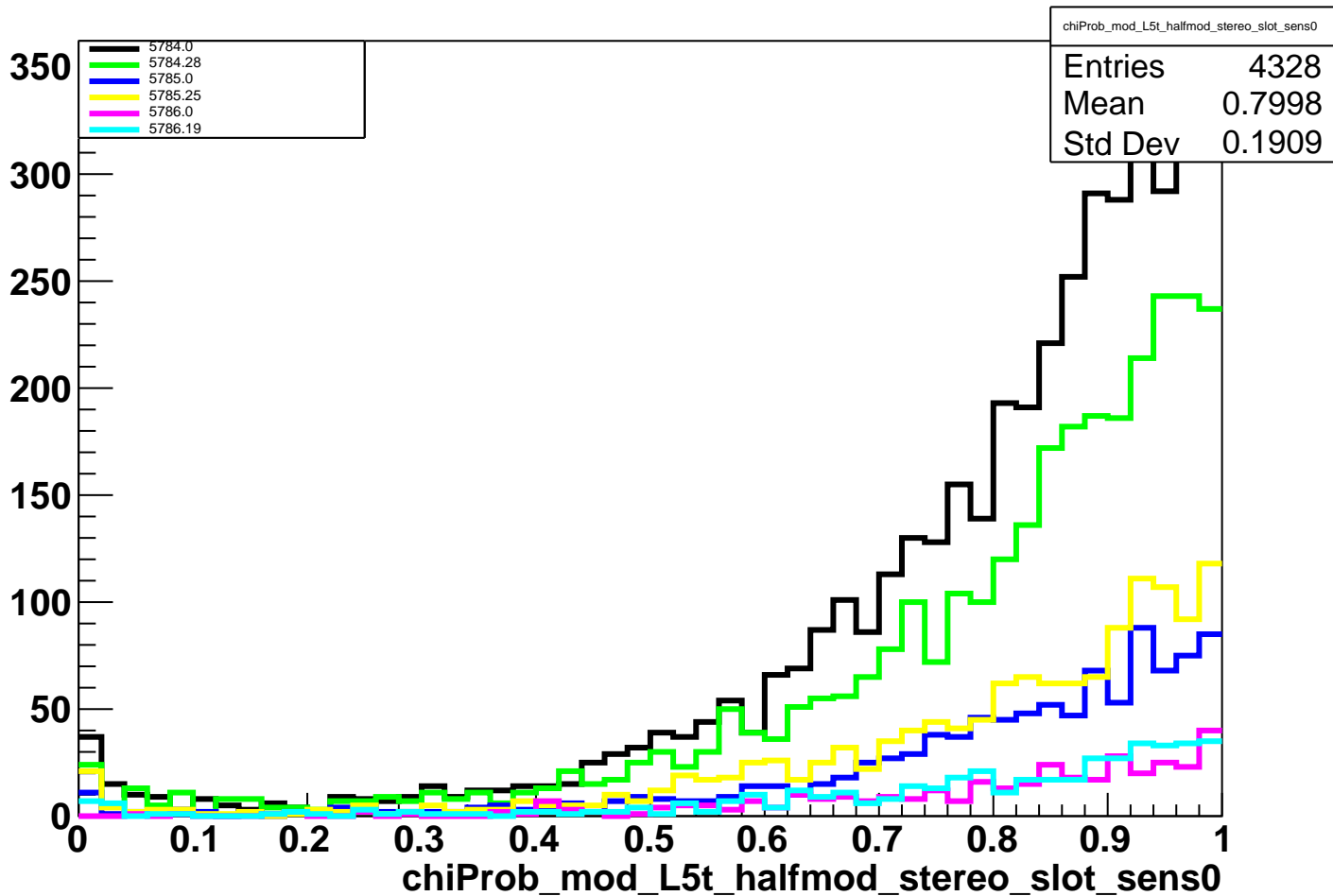
mod_L5t_halfmod_axial_slot_sens0



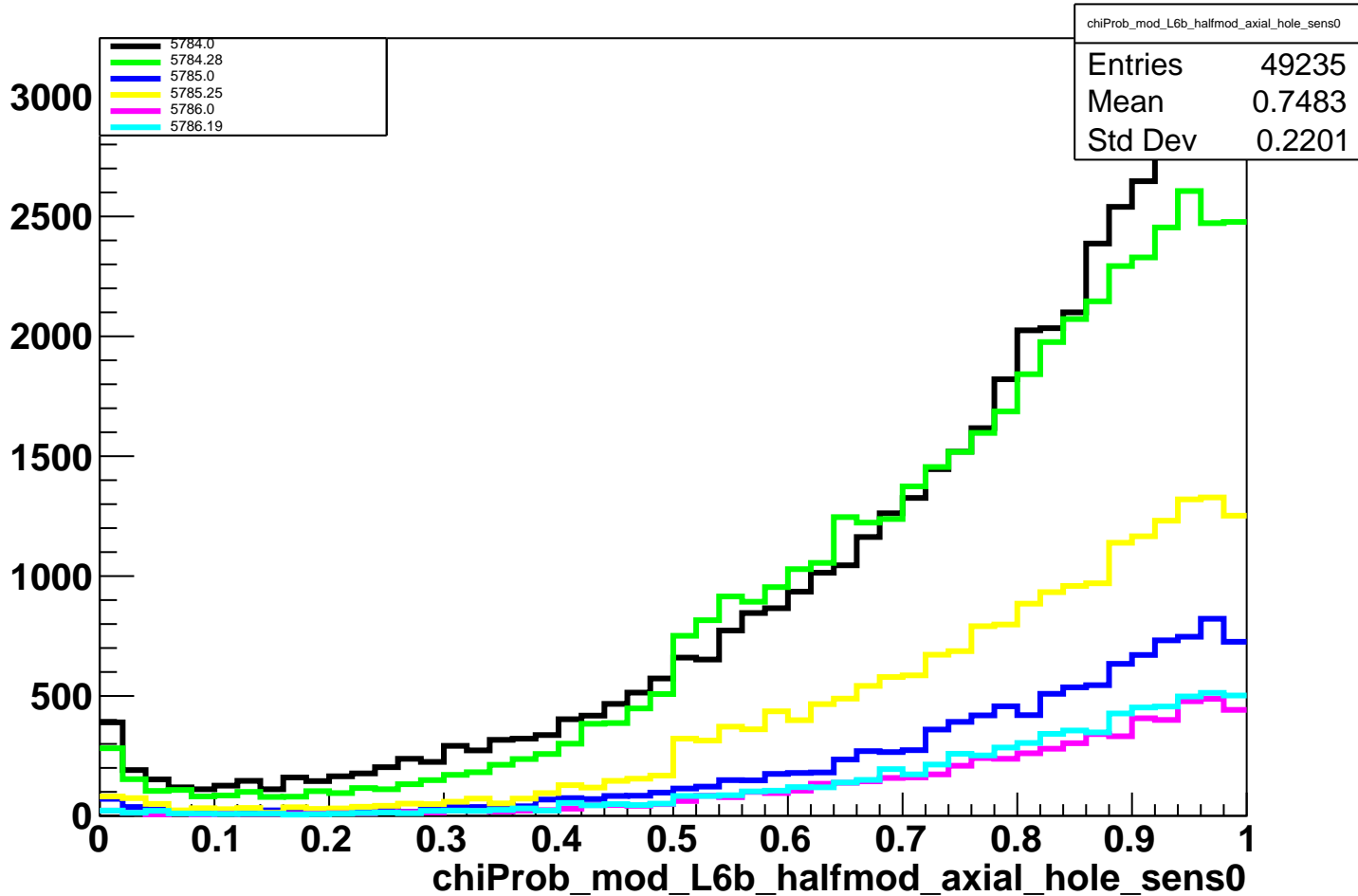
mod_L5t_halfmod_stereo_hole_sens0



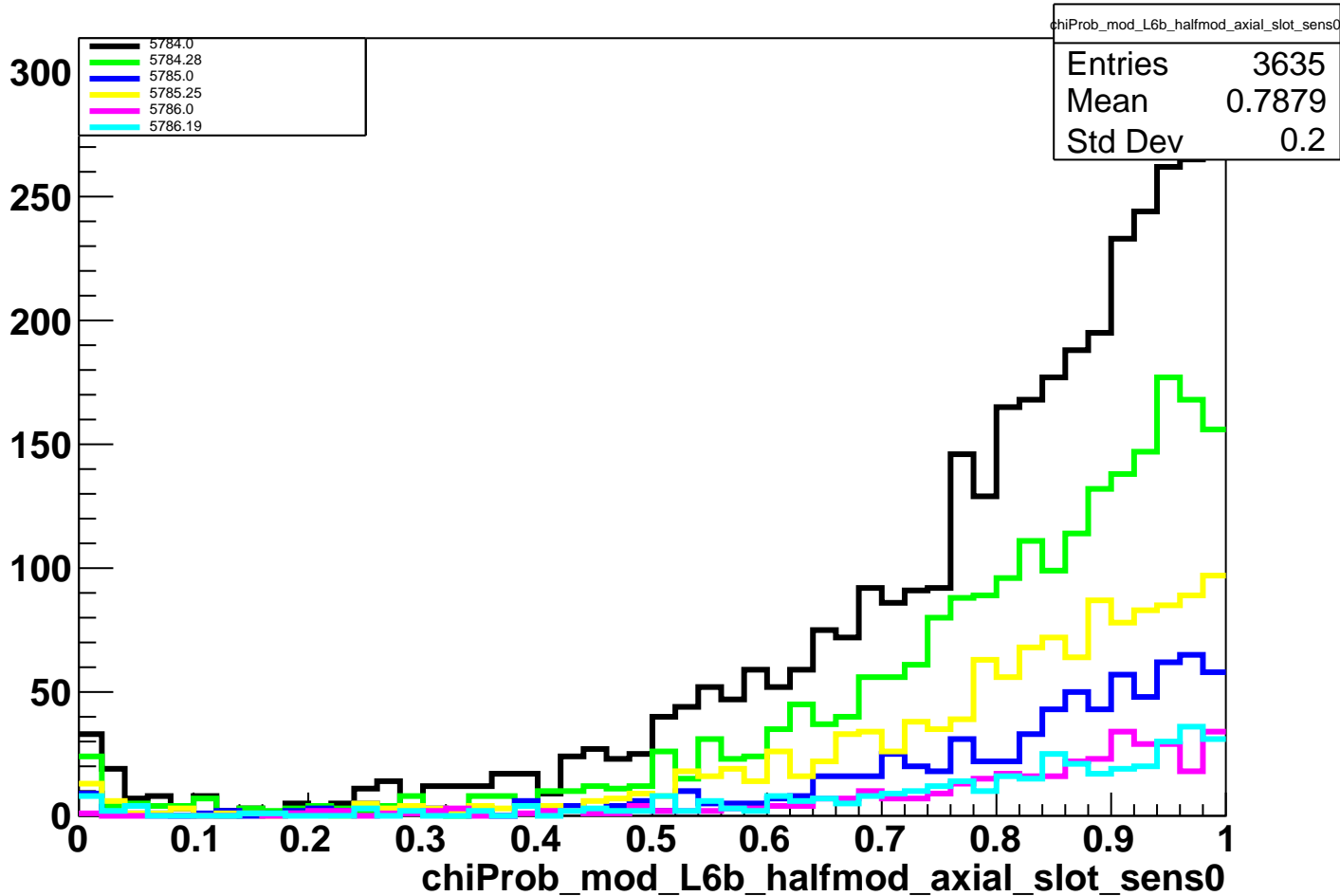
mod_L5t_halfmod_stereo_slot_sens0



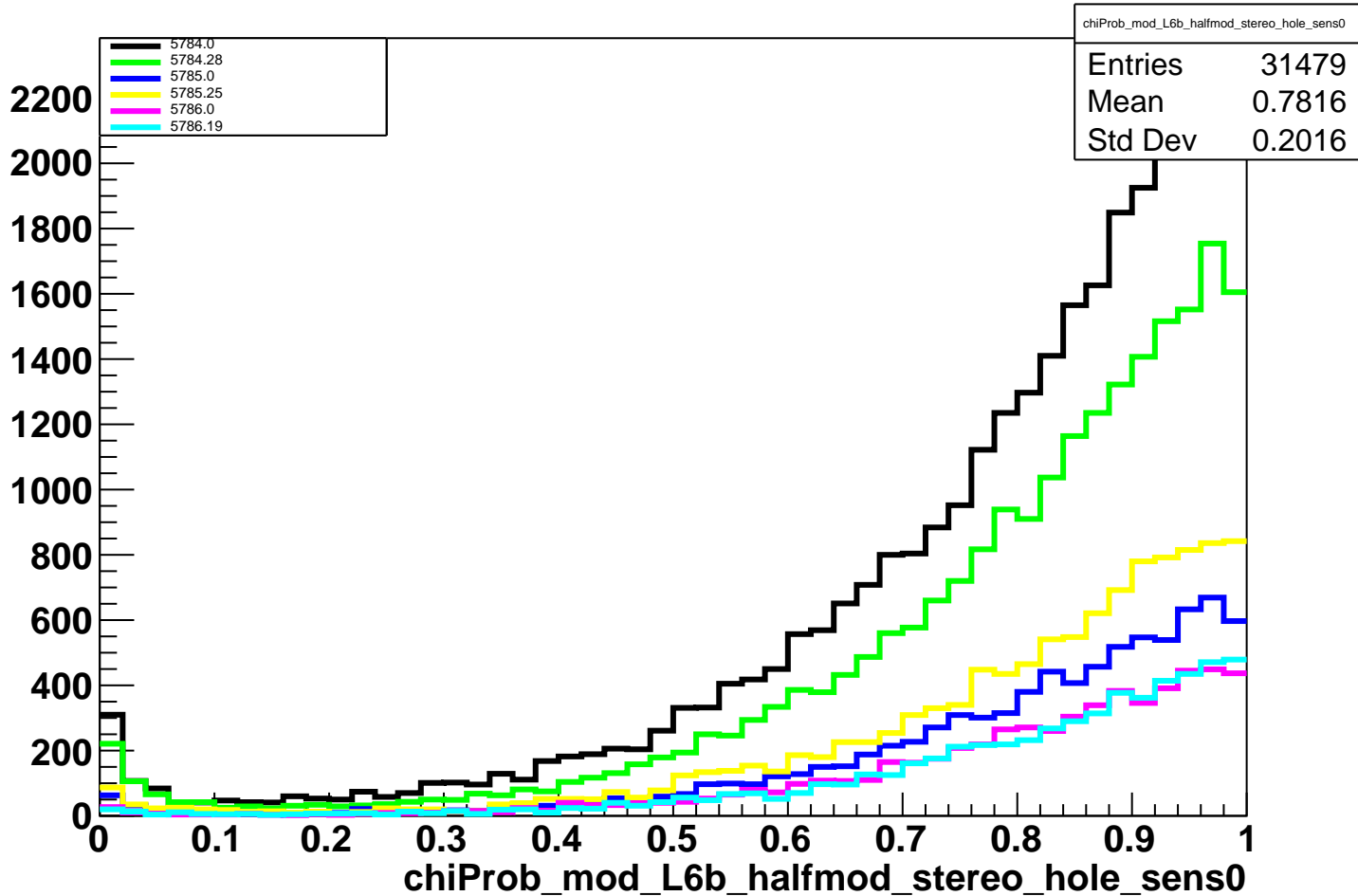
mod_L6b_halfmod_axial_hole_sens0



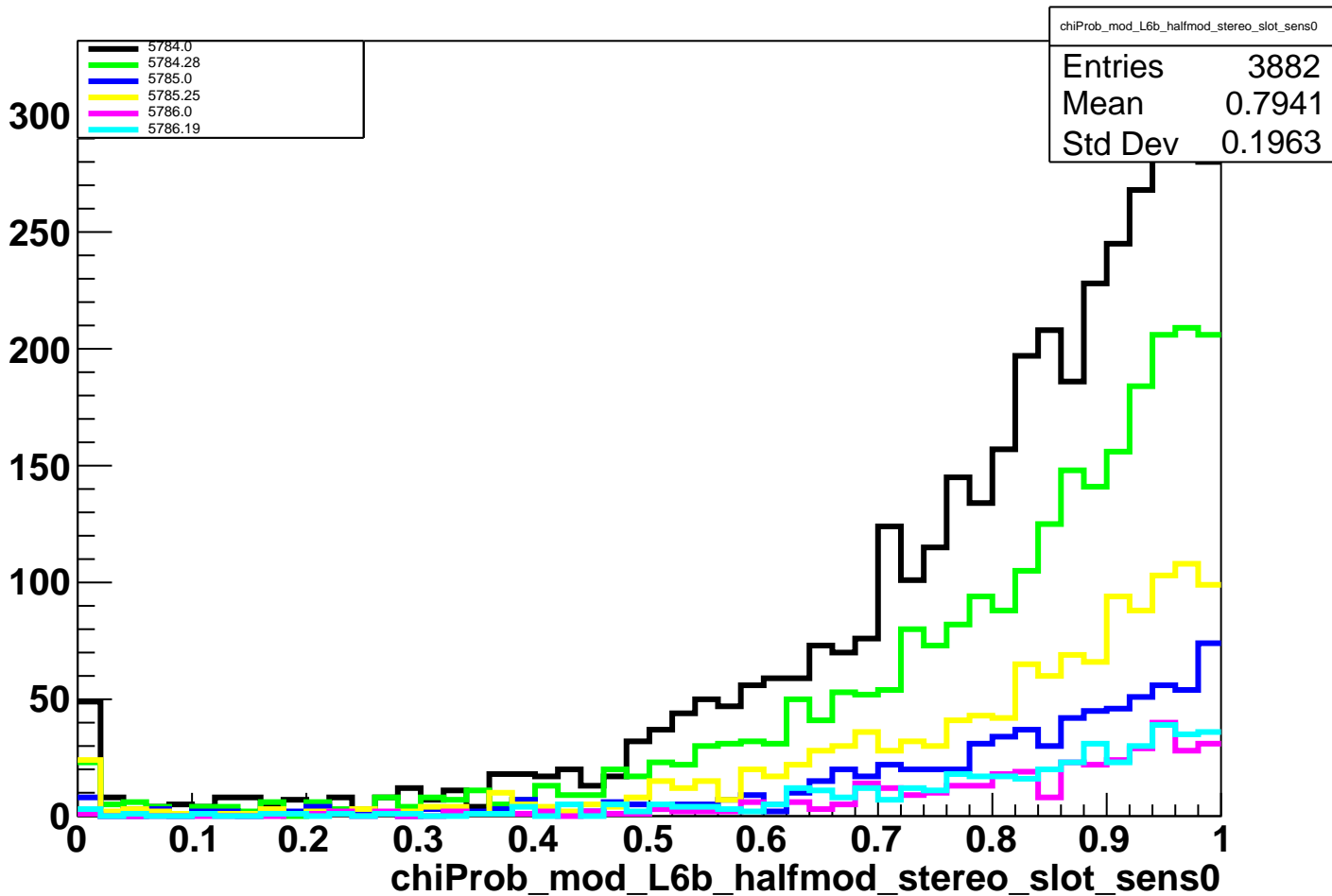
mod_L6b_halfmod_axial_slot_sens0



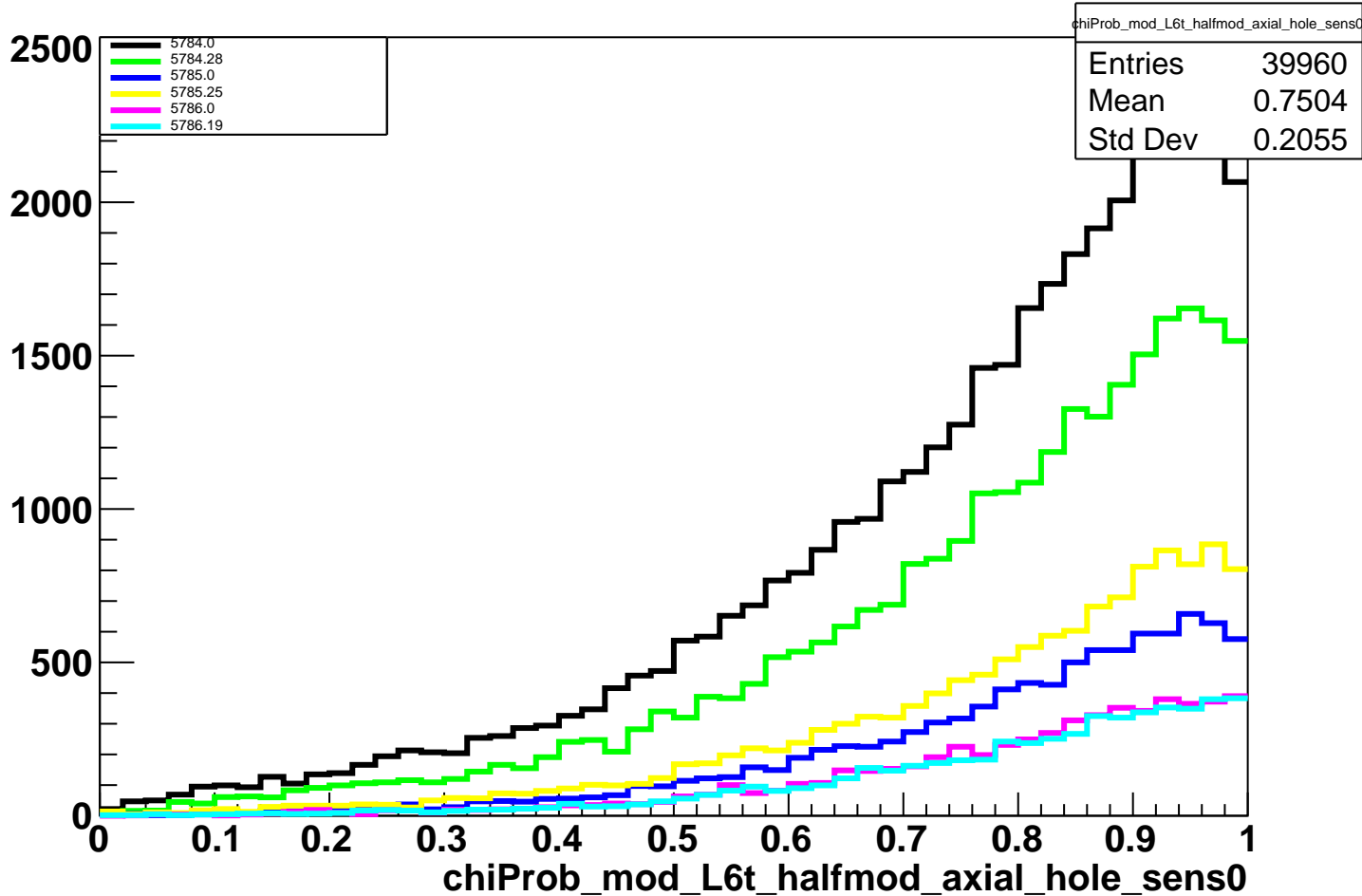
mod_L6b_halfmod_stereo_hole_sens0



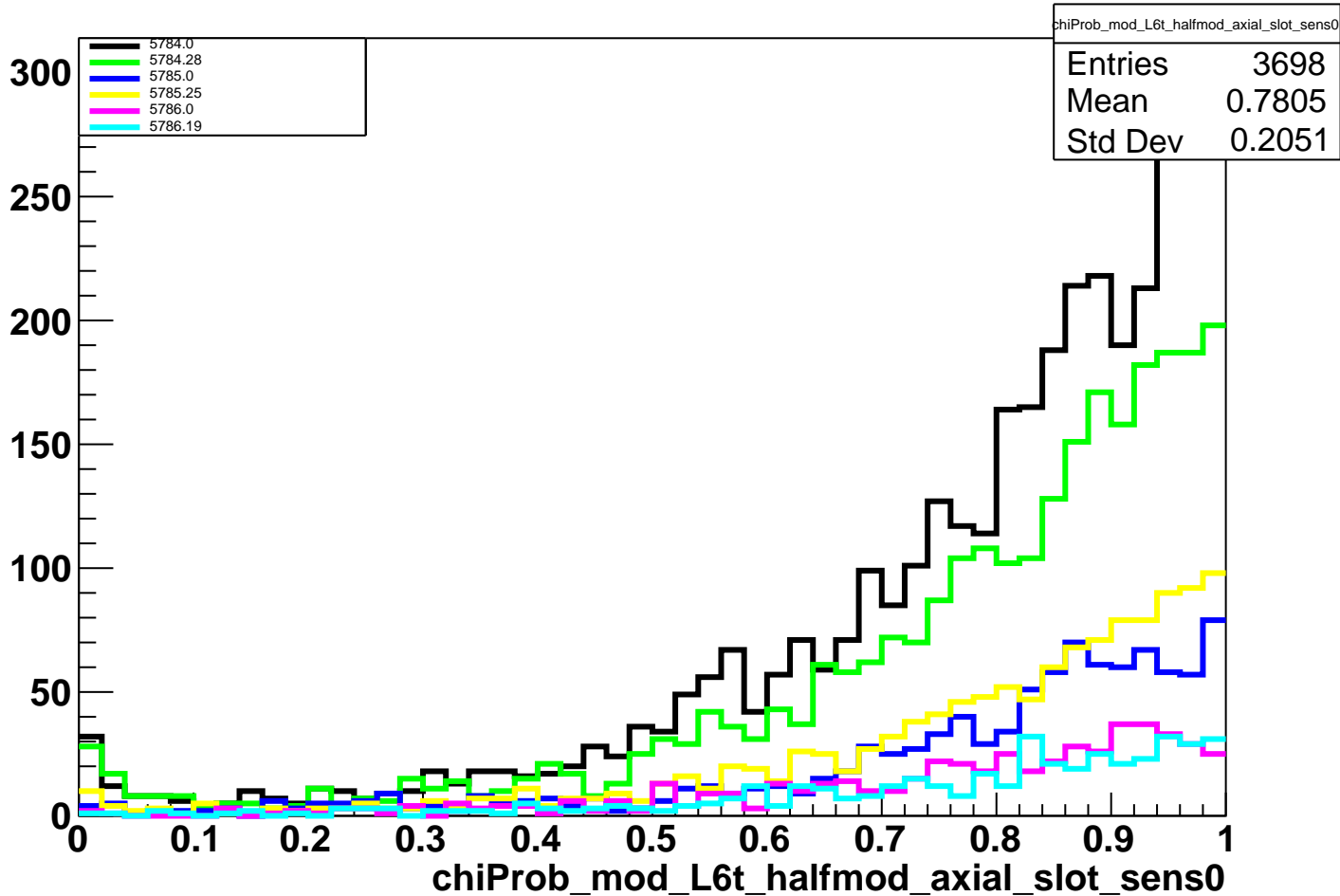
mod_L6b_halfmod_stereo_slot_sens0



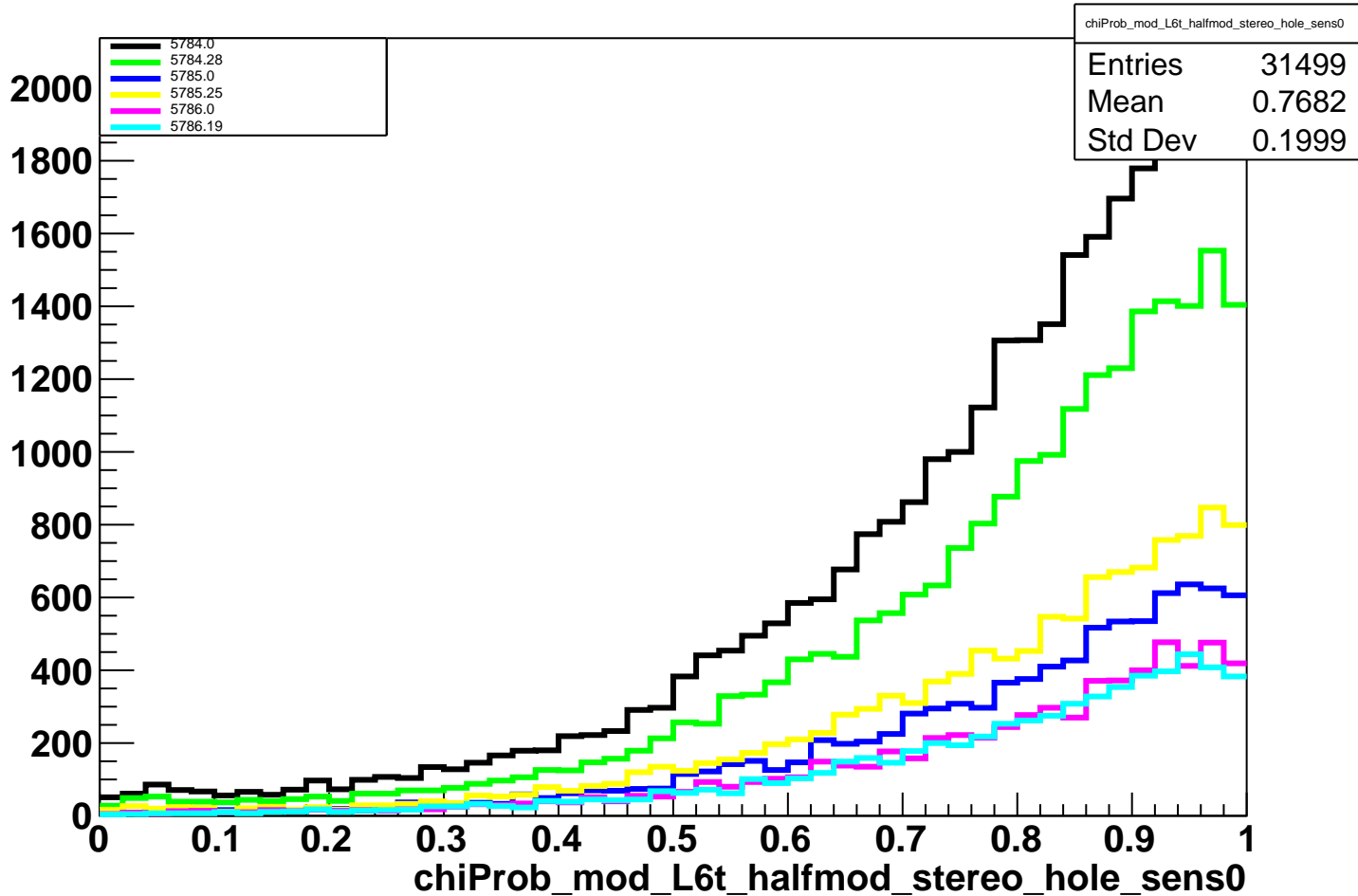
mod_L6t_halfmod_axial_hole_sens0



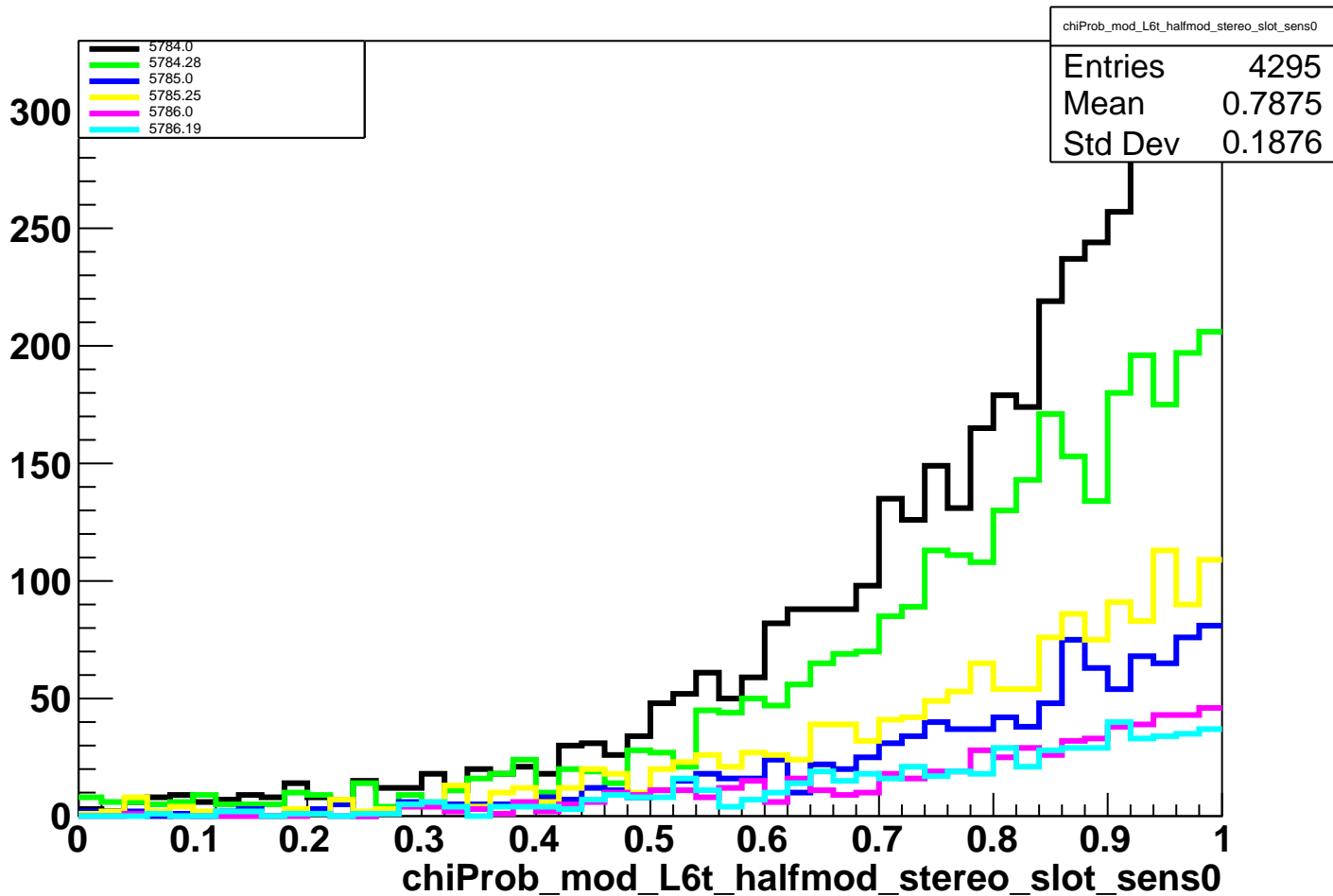
mod_L6t_halfmod_axial_slot_sens0



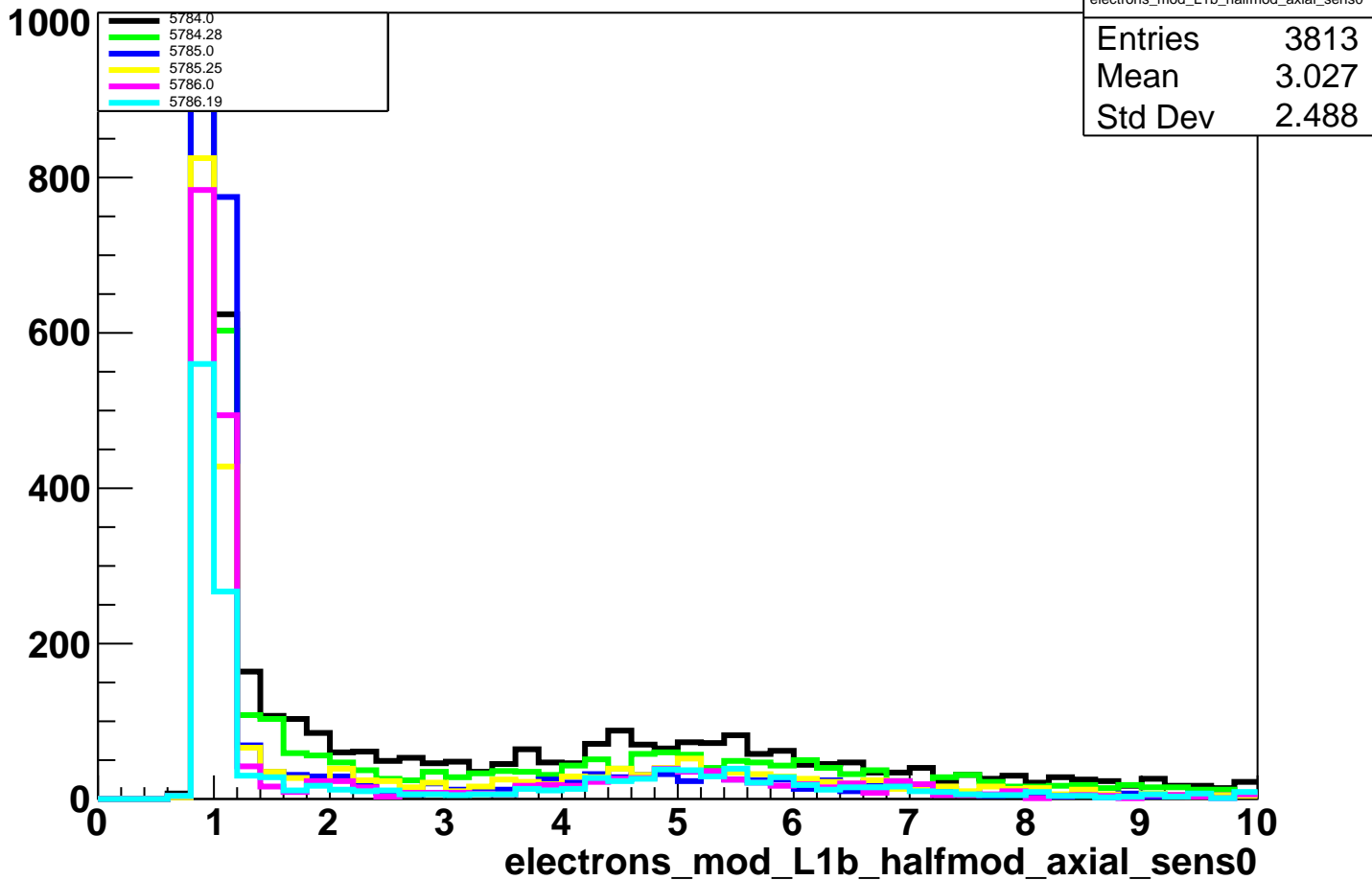
mod_L6t_halfmod_stereo_hole_sens0



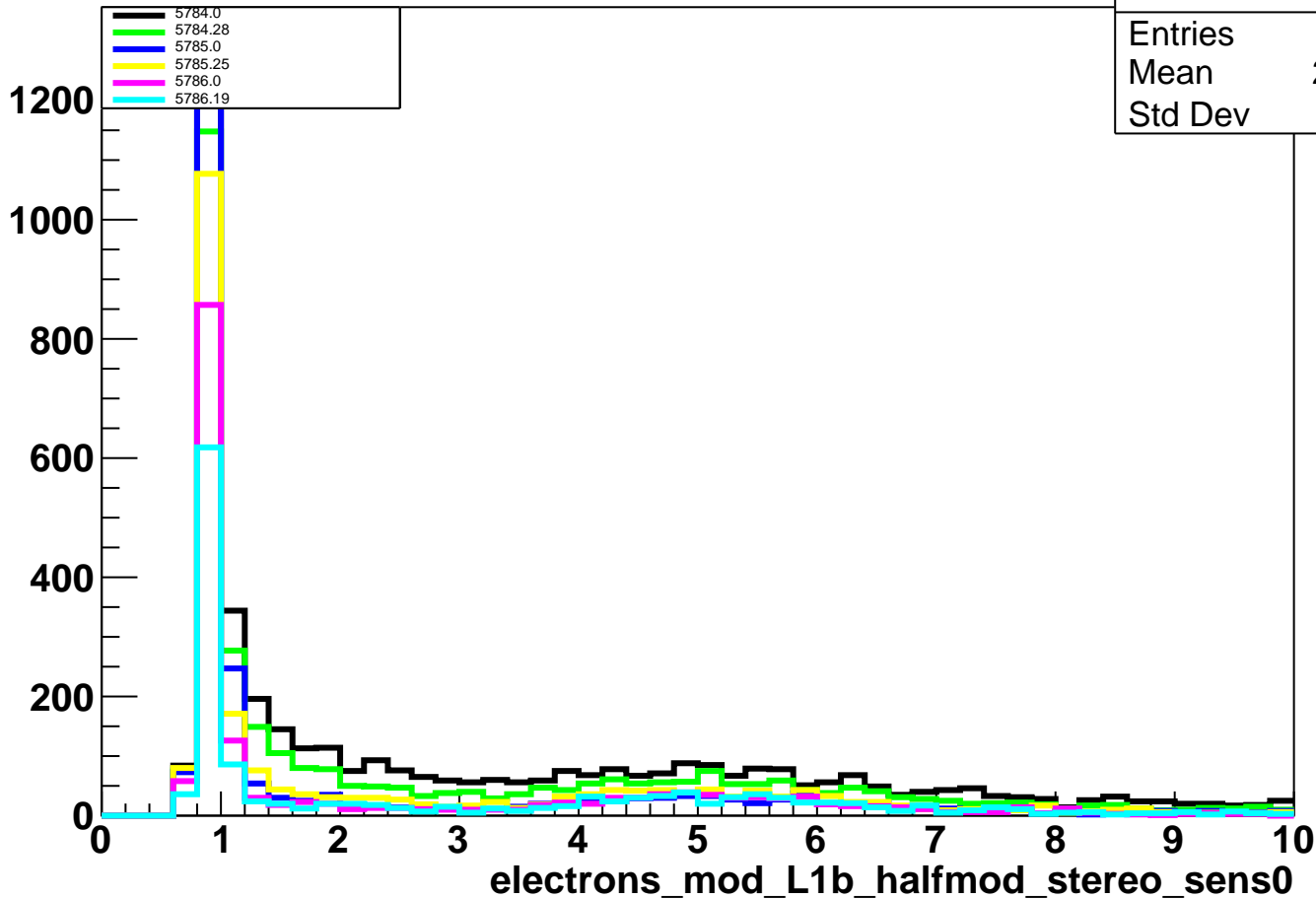
mod_L6t_halfmod_stereo_slot_sens0



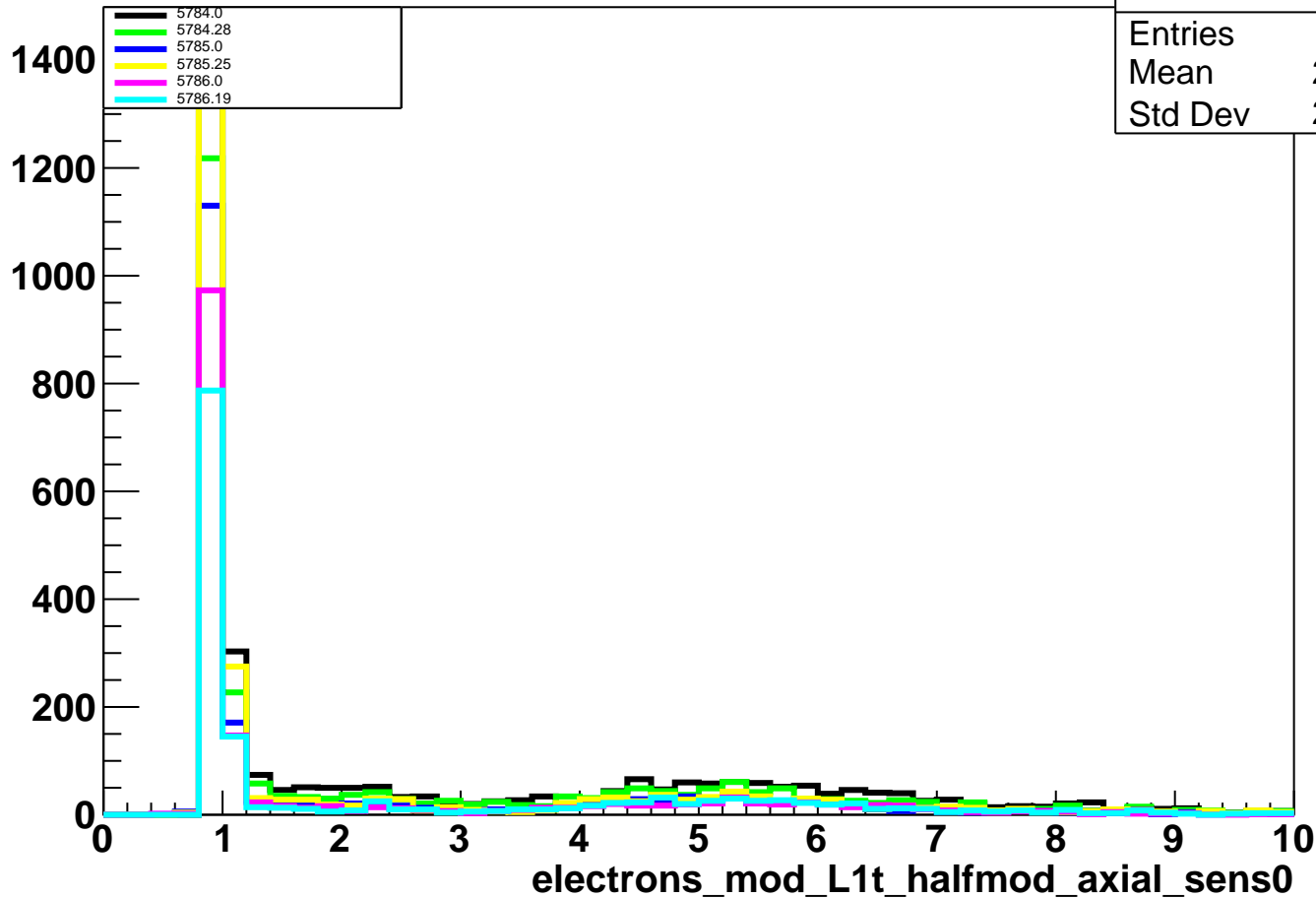
mod_L1b_halfmod_axial_sens0



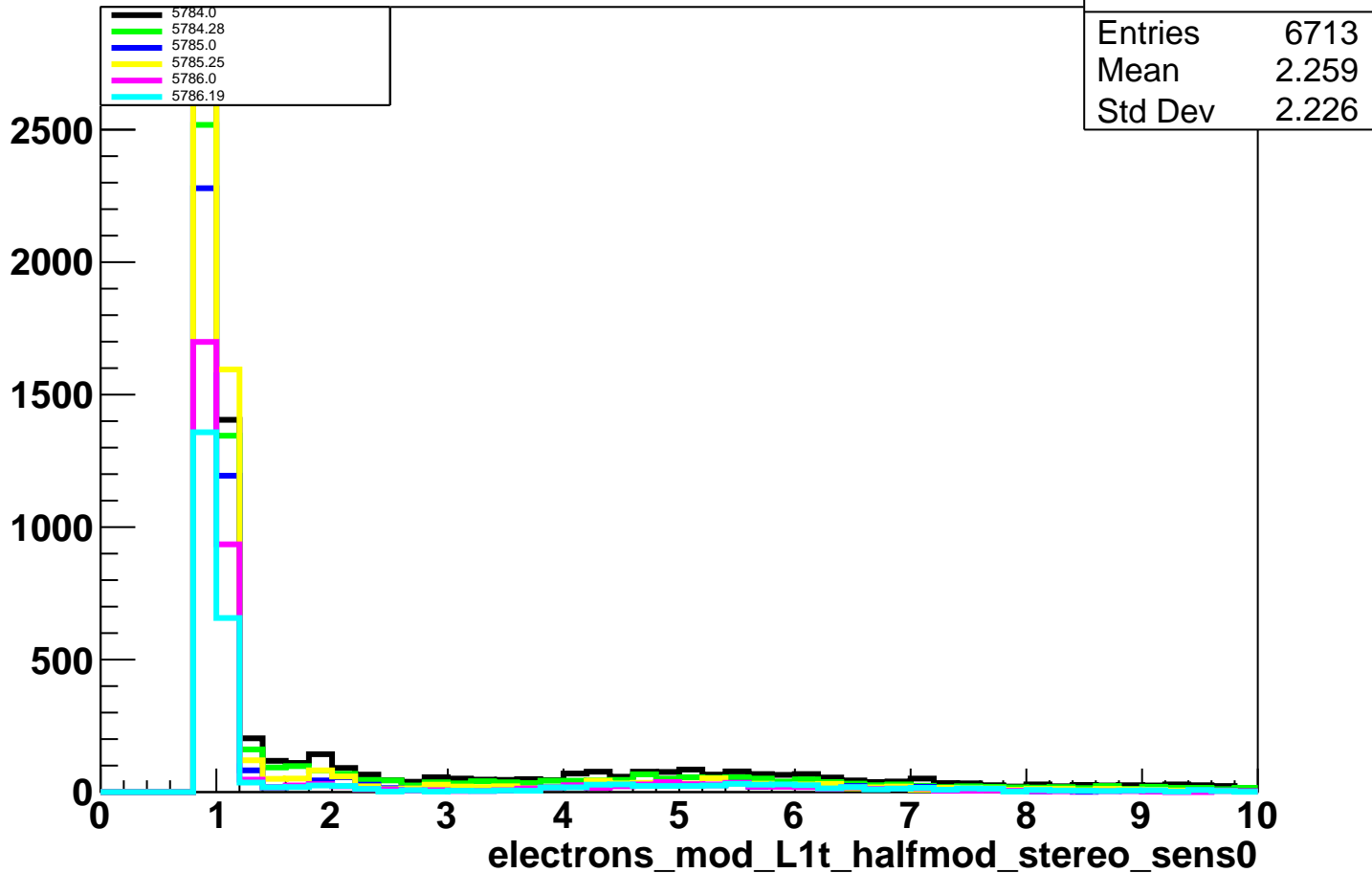
mod_L1b_halfmod_stereo_sens0



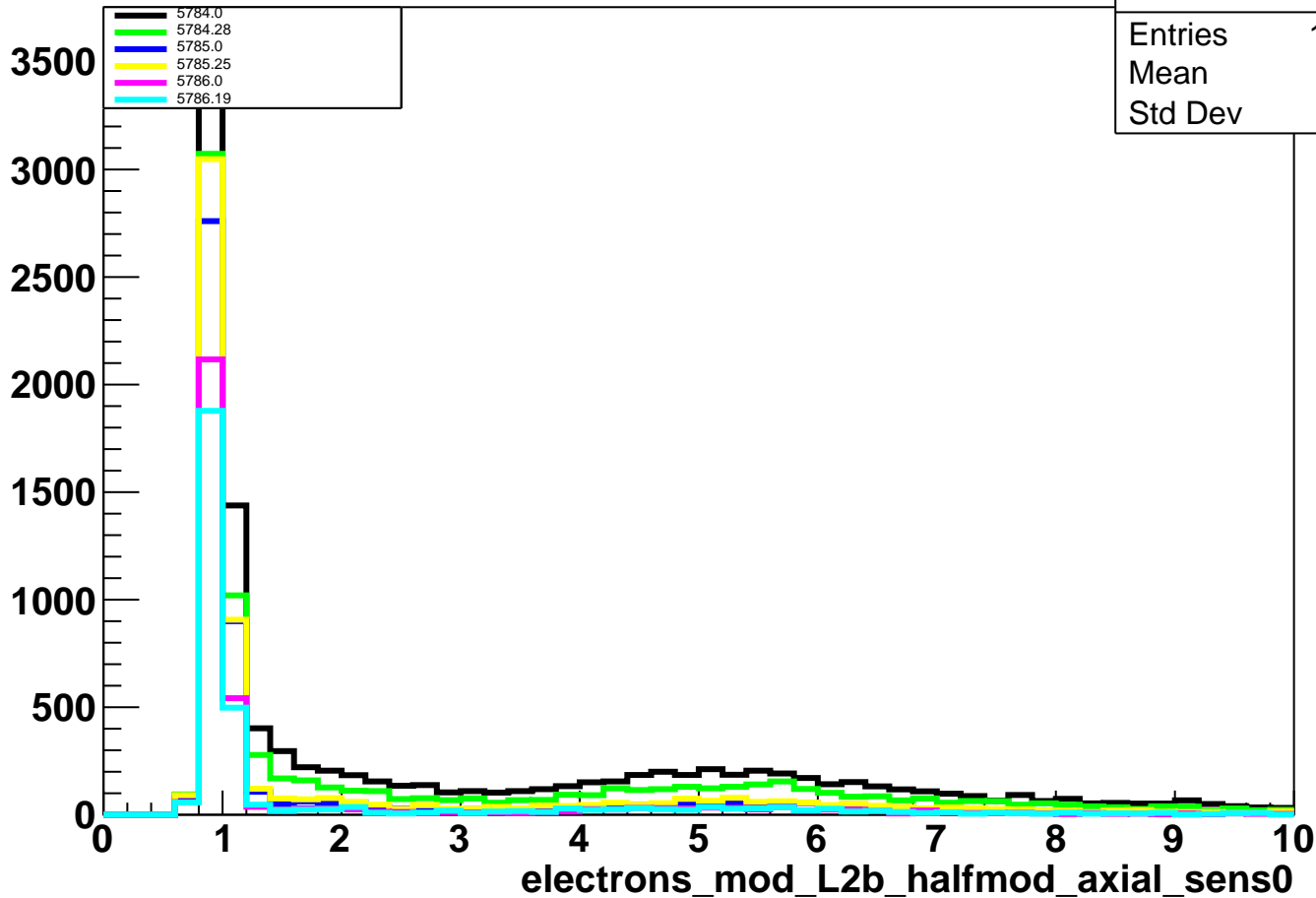
mod_L1t_halfmod_axial_sens0



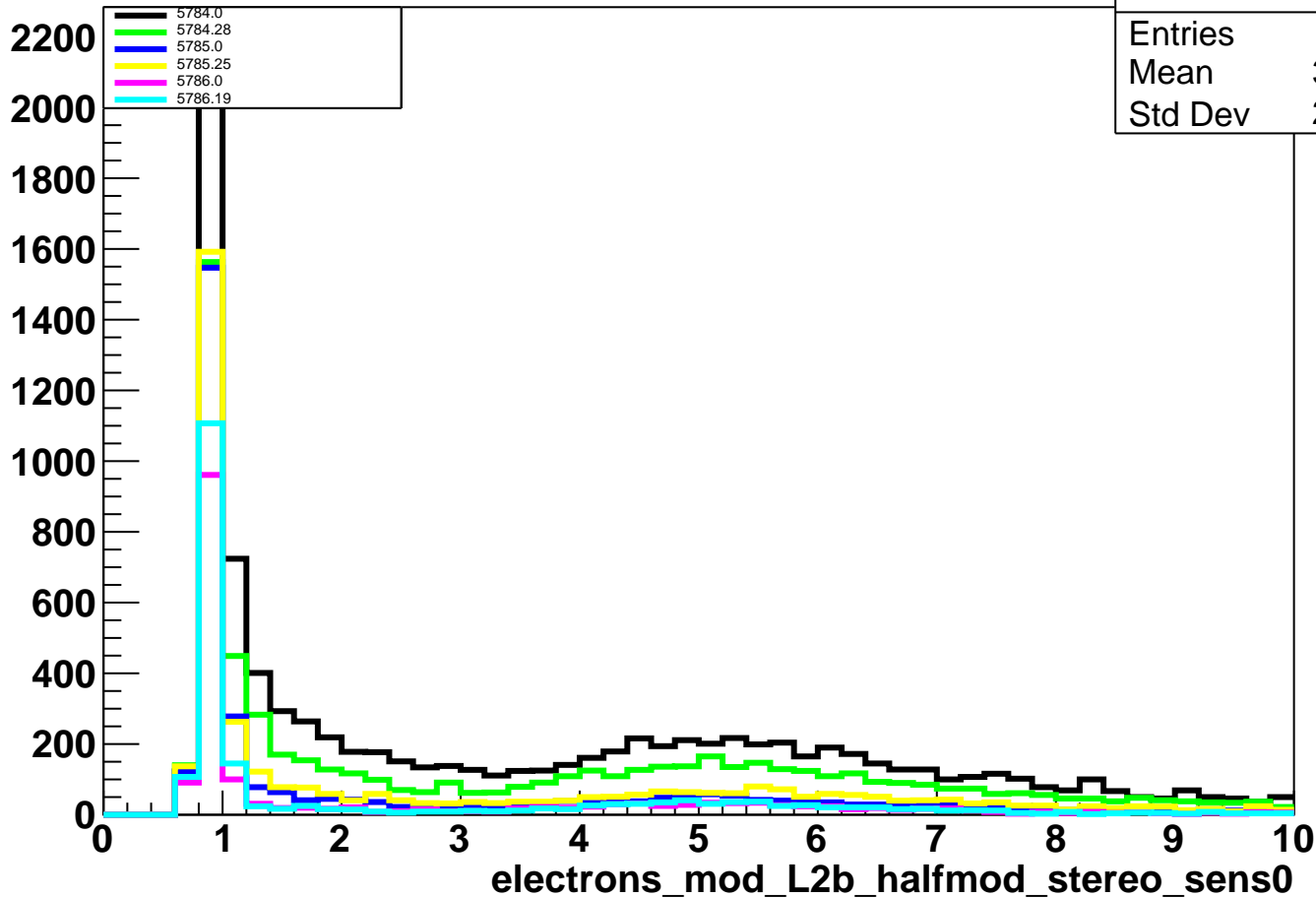
mod_L1t_halfmod_stereo_sens0



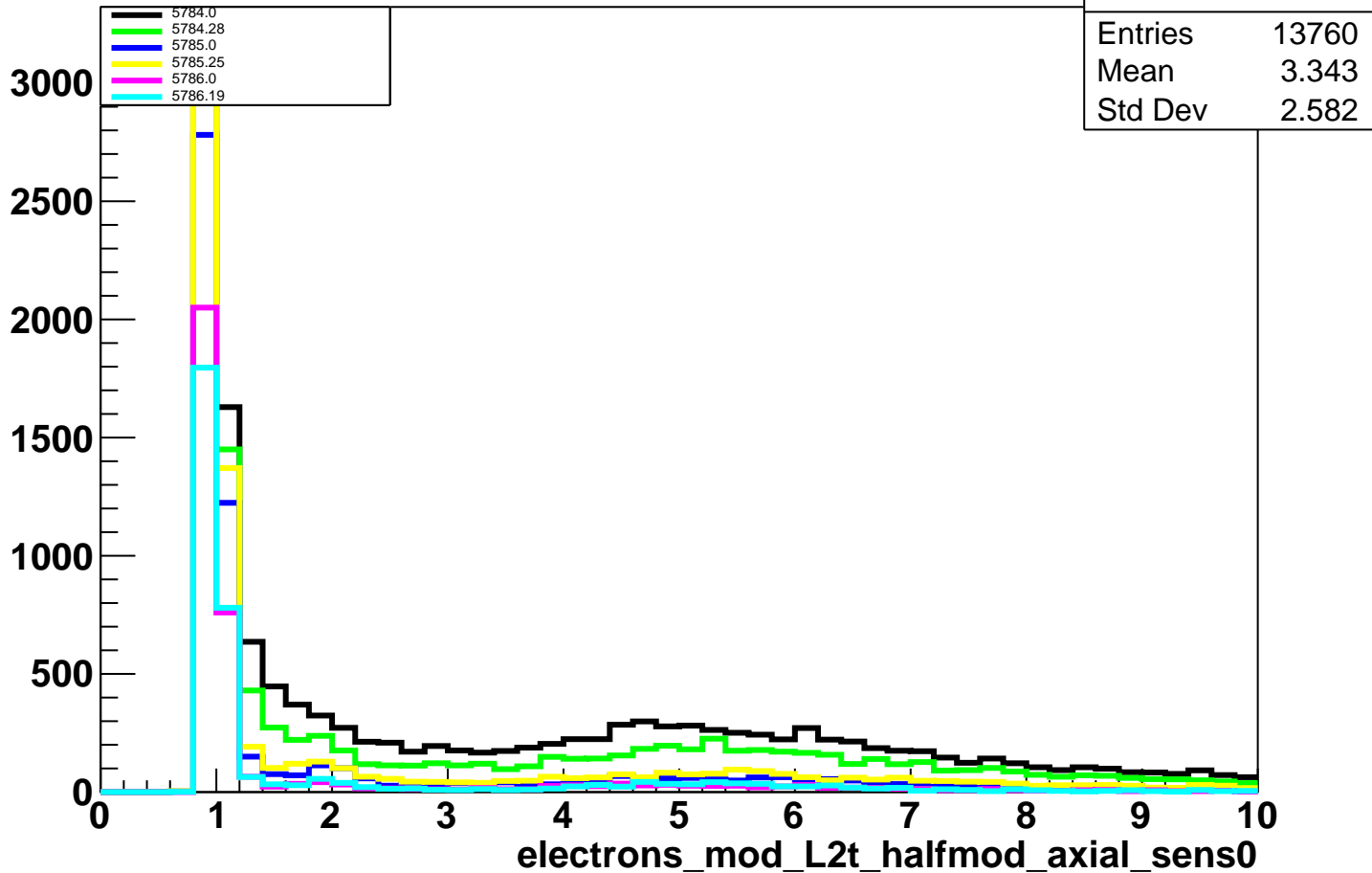
mod_L2b_halfmod_axial_sens0



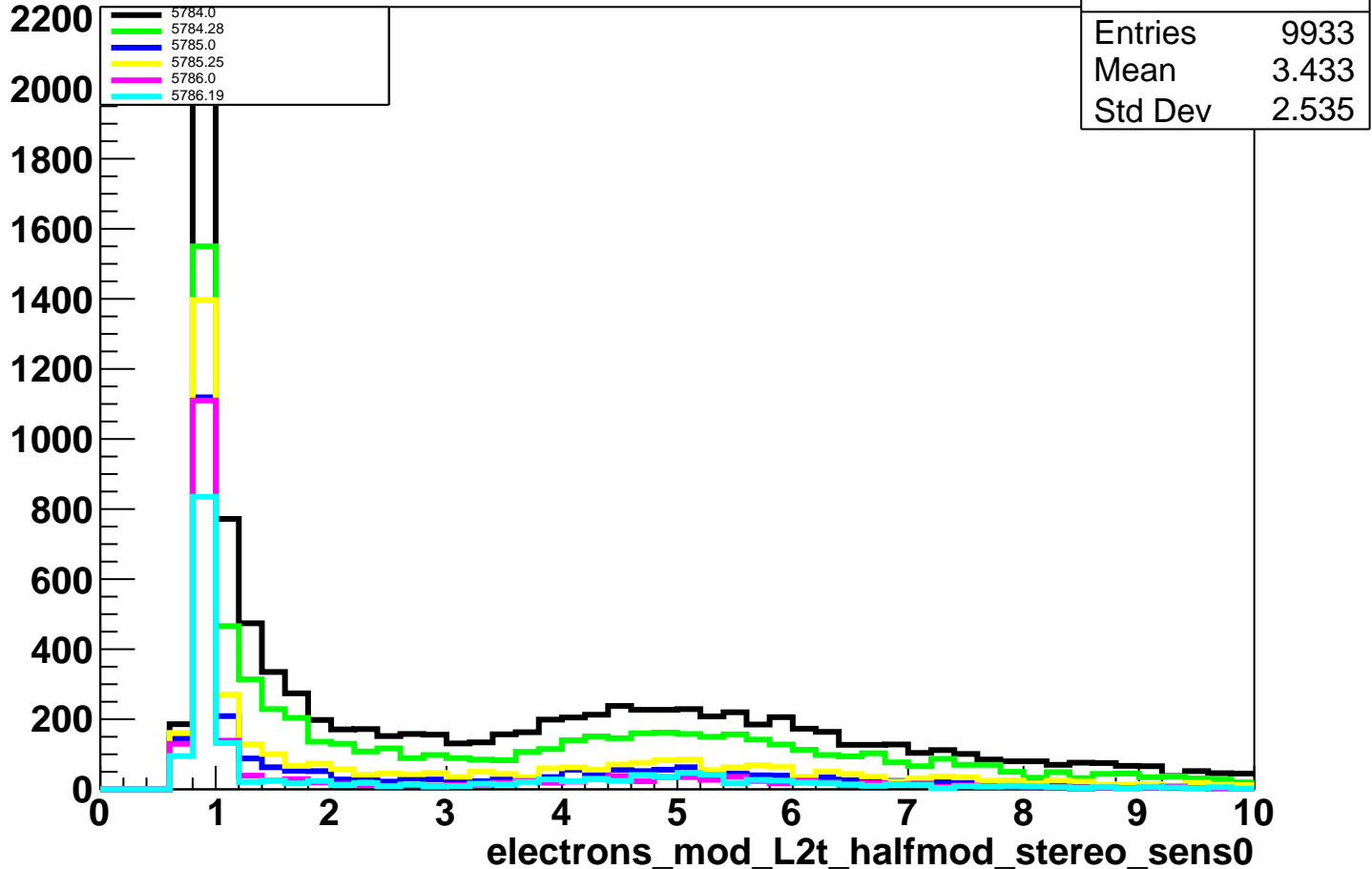
mod_L2b_halfmod_stereo_sens0



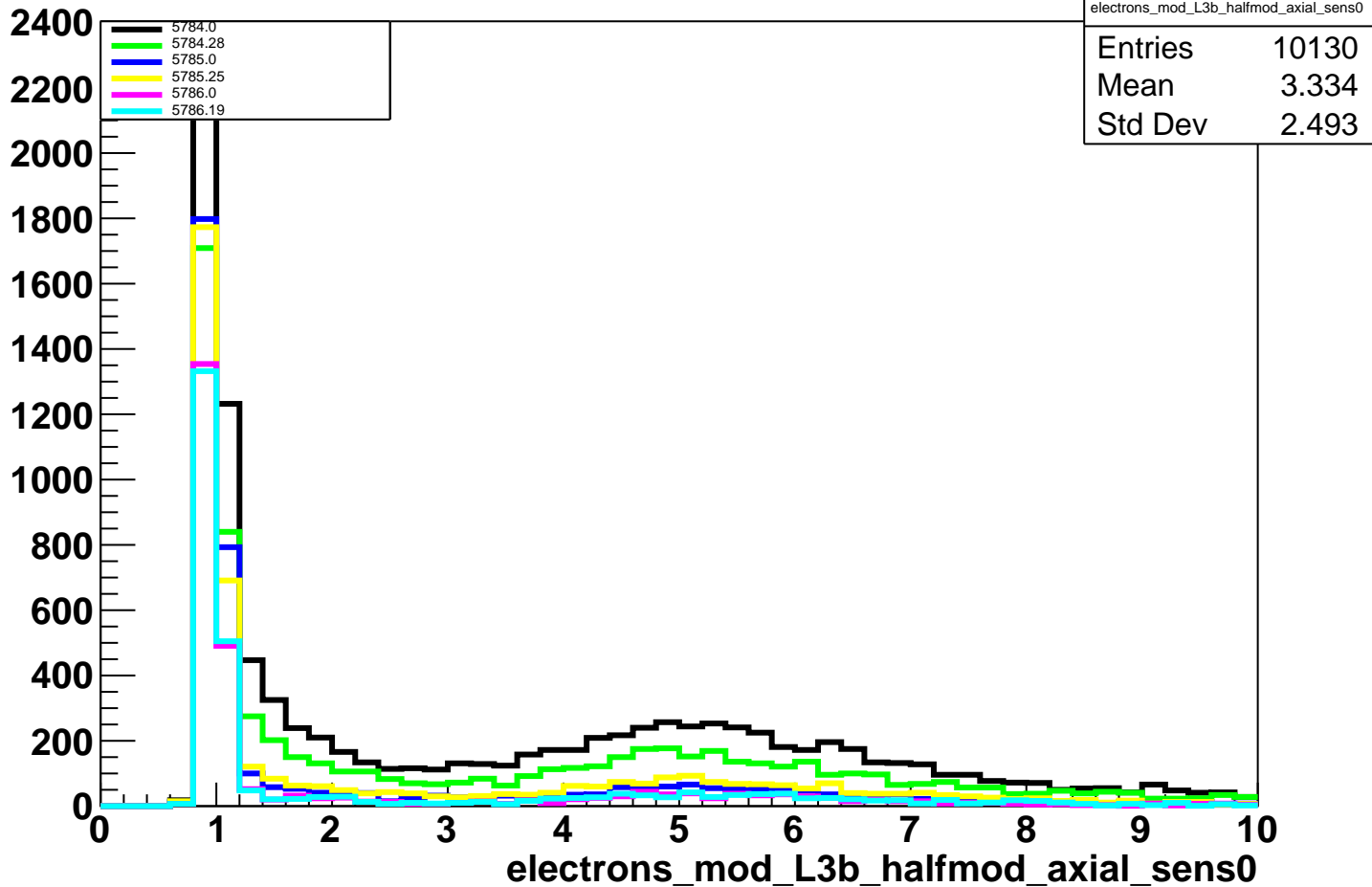
mod_L2t_halfmod_axial_sens0



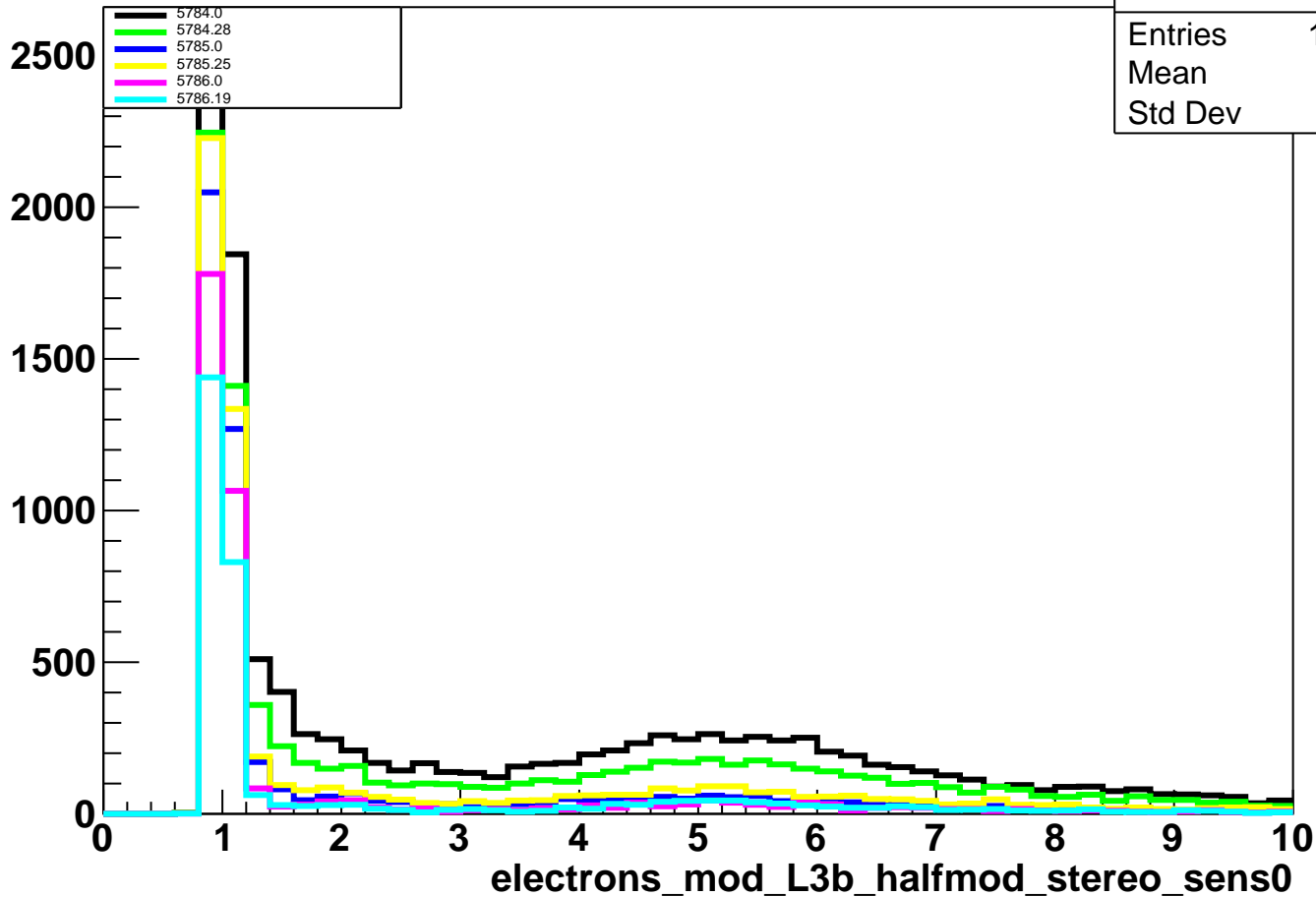
mod_L2t_halfmod_stereo_sens0



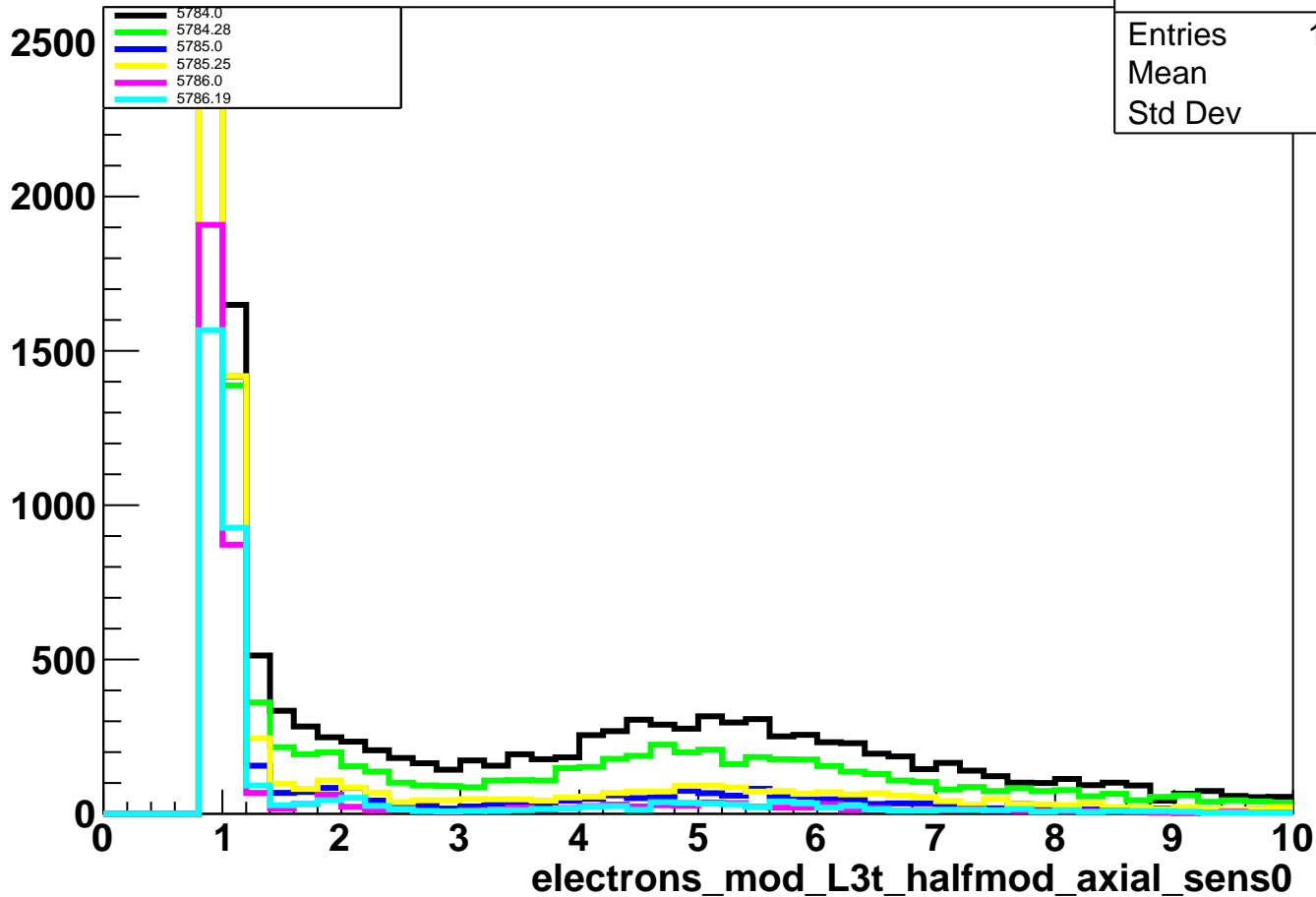
mod_L3b_halfmod_axial_sens0



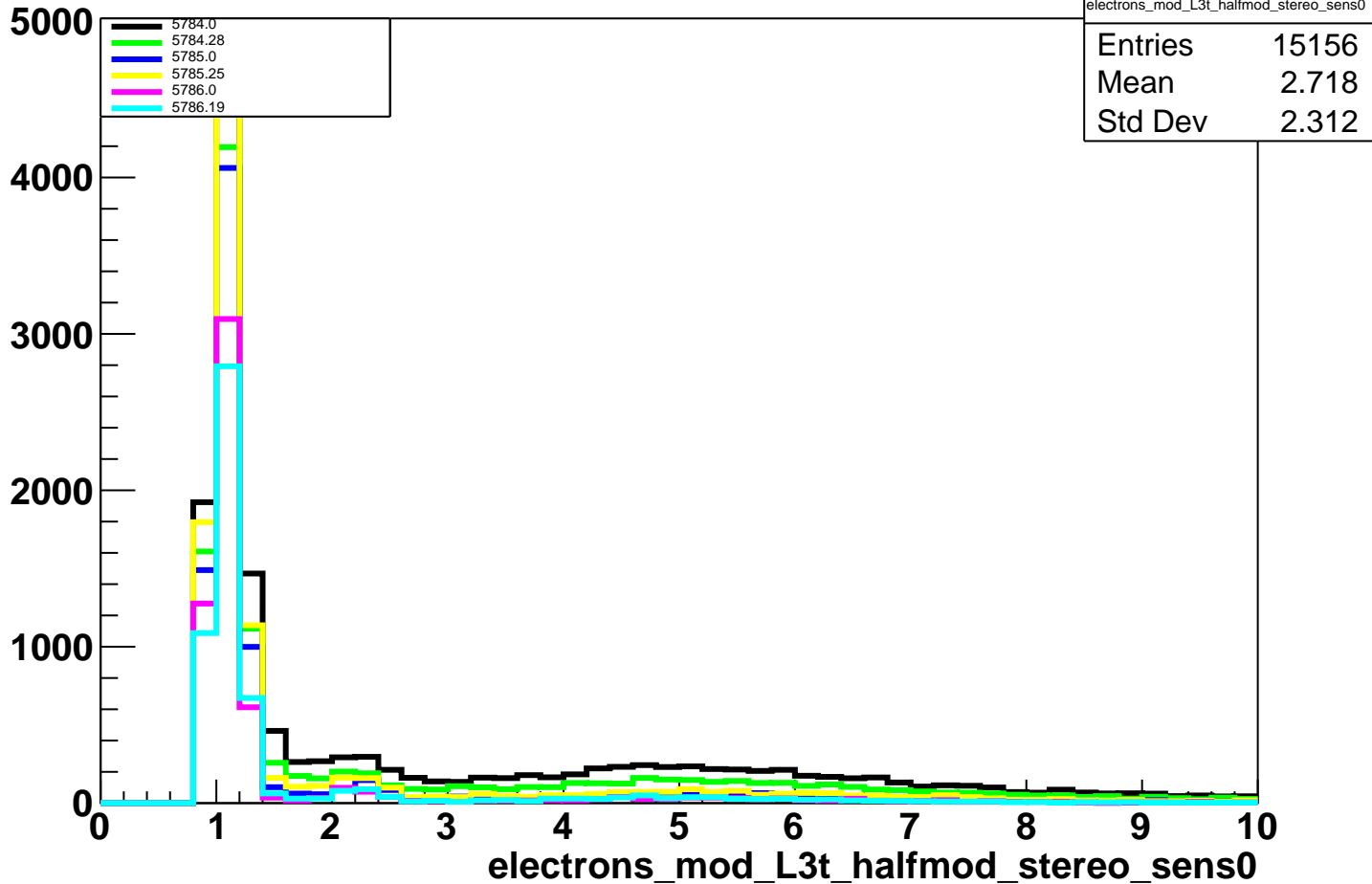
mod_L3b_halfmod_stereo_sens0



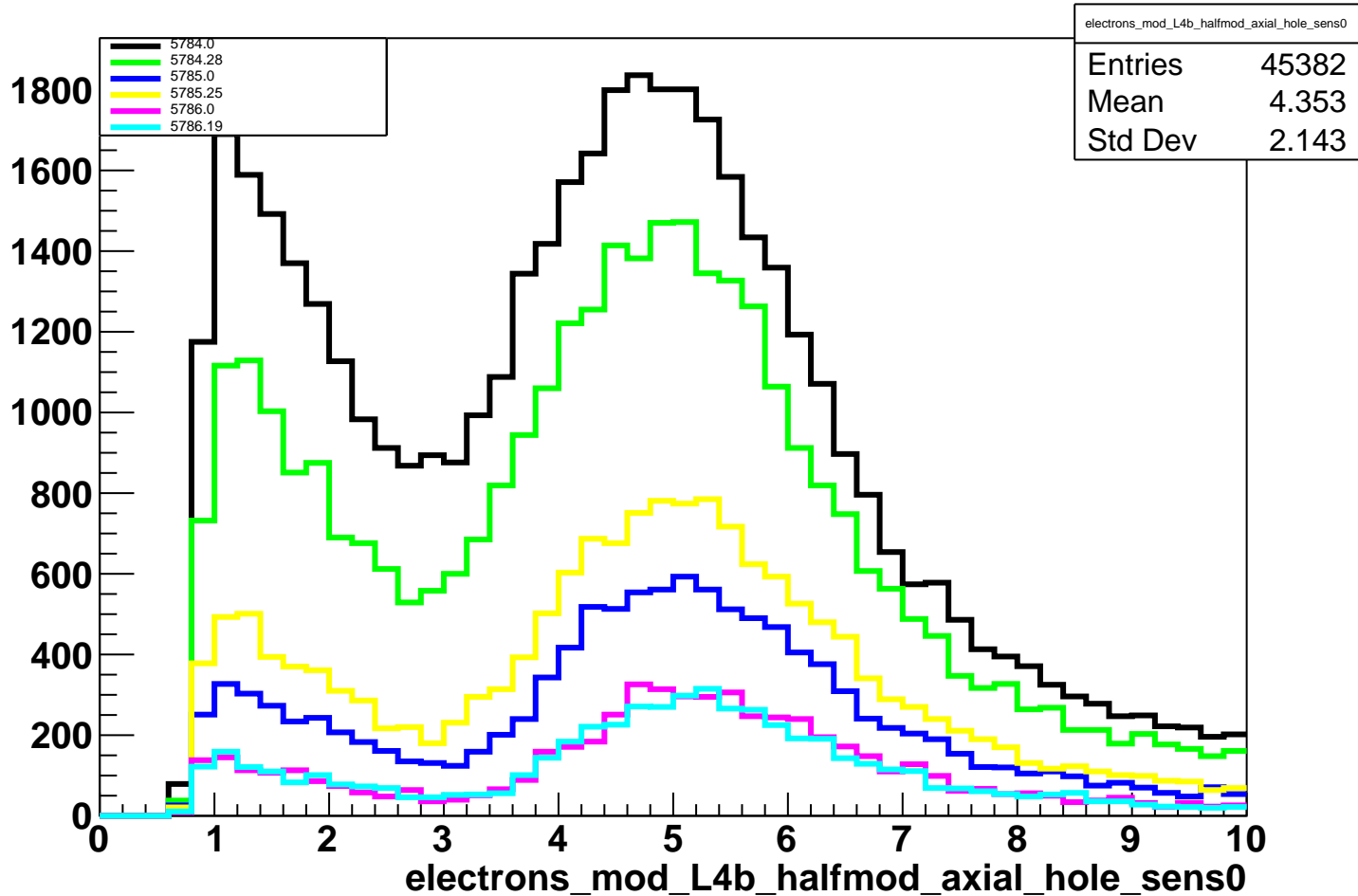
mod_L3t_halfmod_axial_sens0



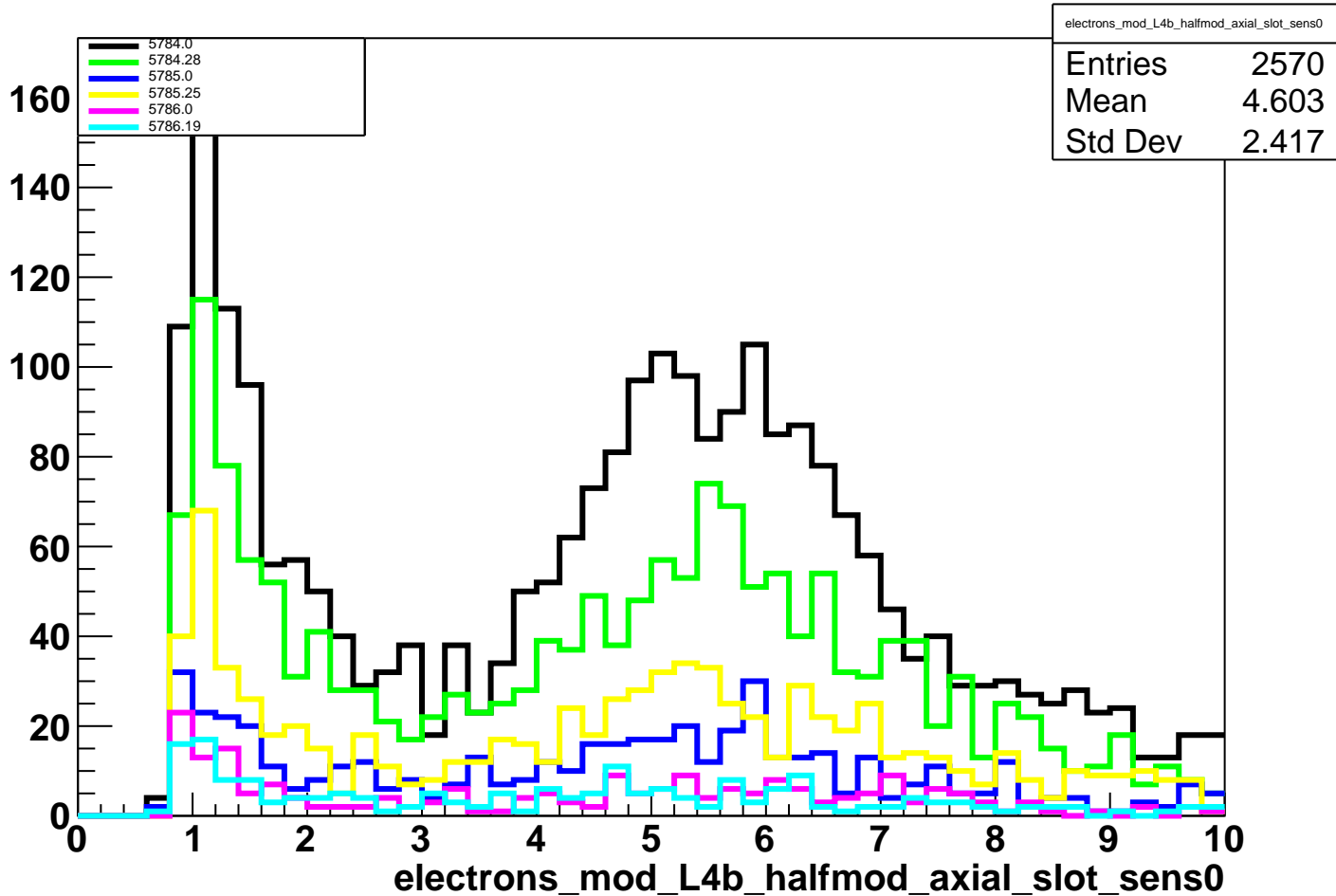
mod_L3t_halfmod_stereo_sens0



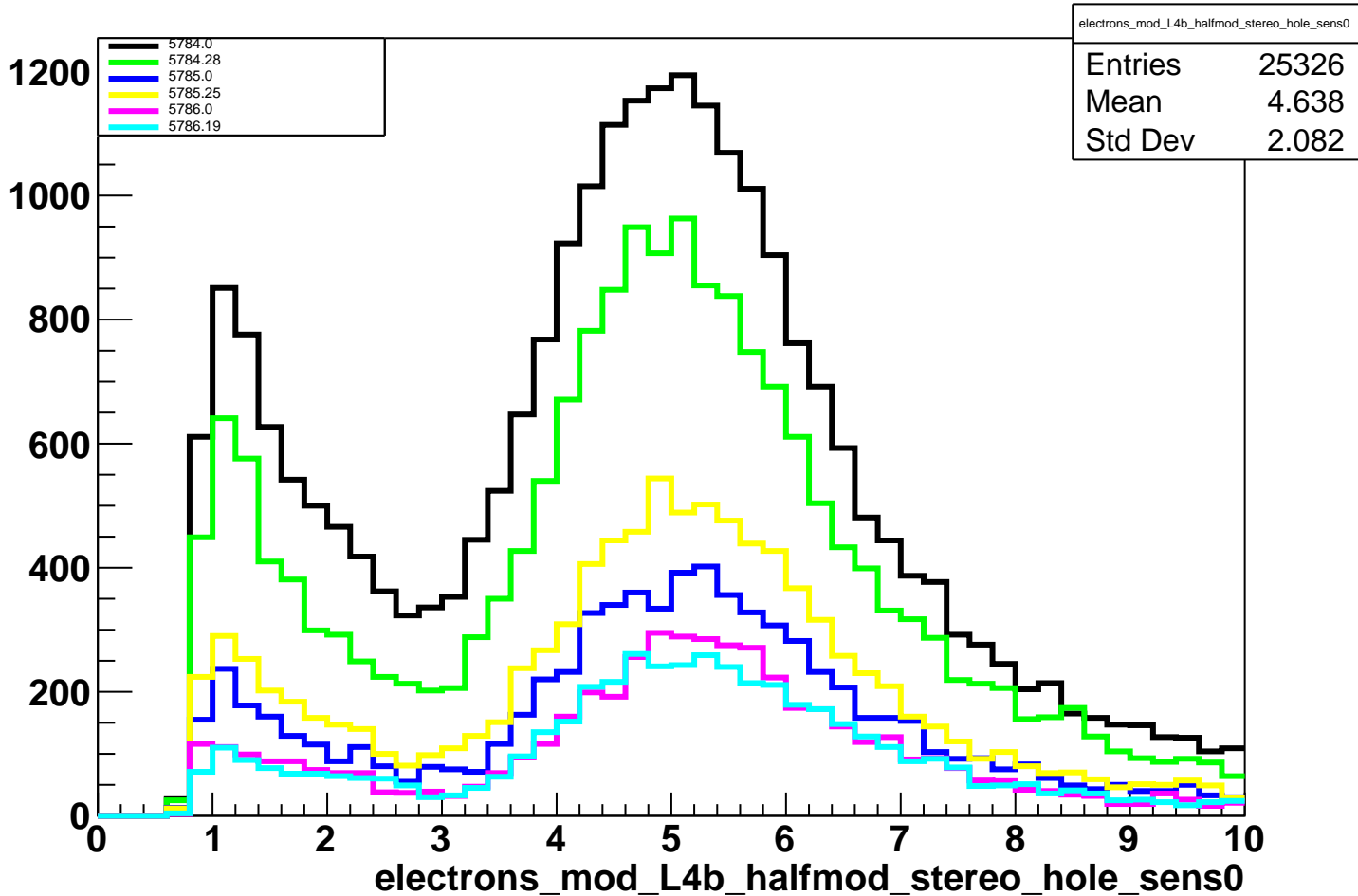
mod_L4b_halfmod_axial_hole_sens0



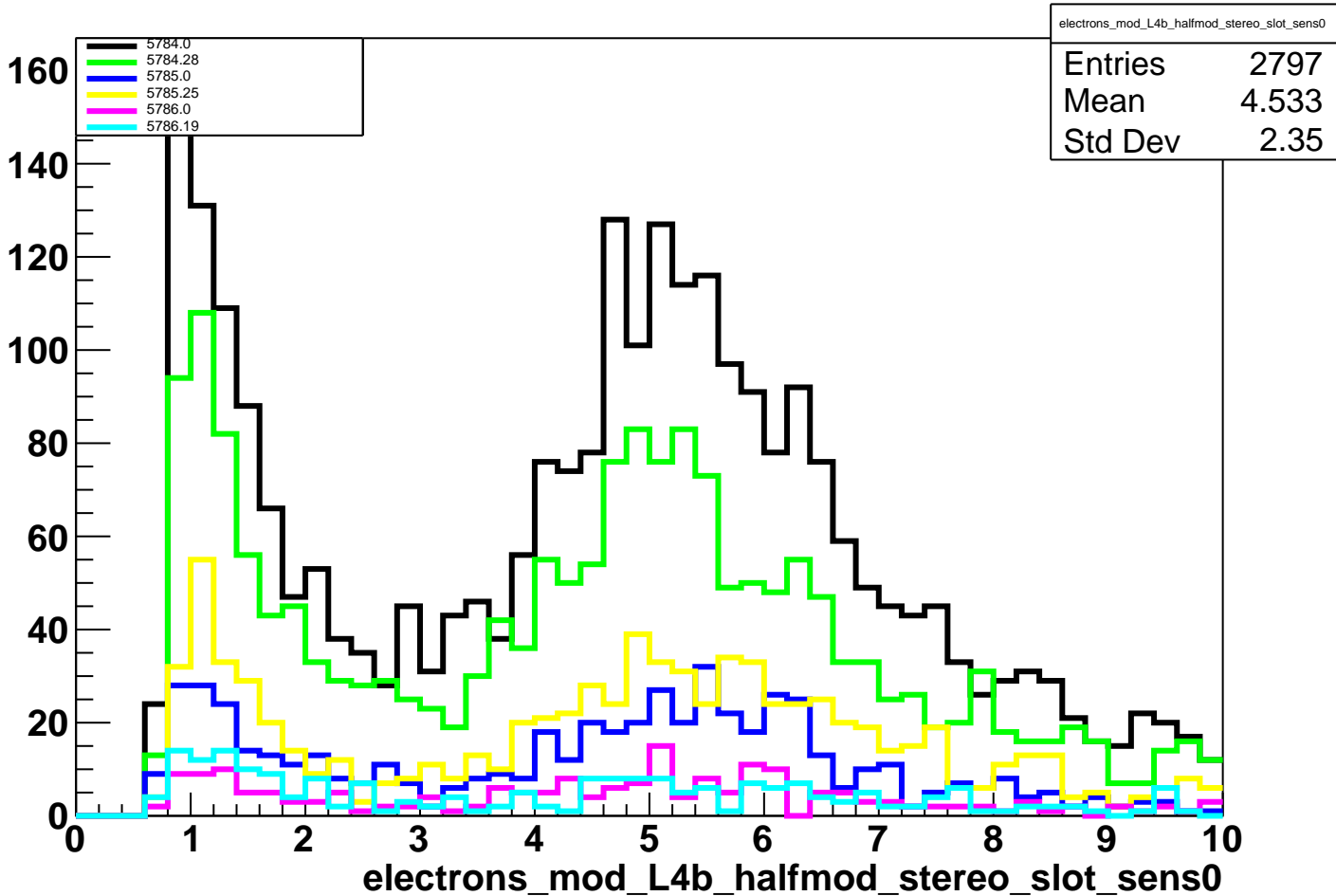
mod_L4b_halfmod_axial_slot_sens0



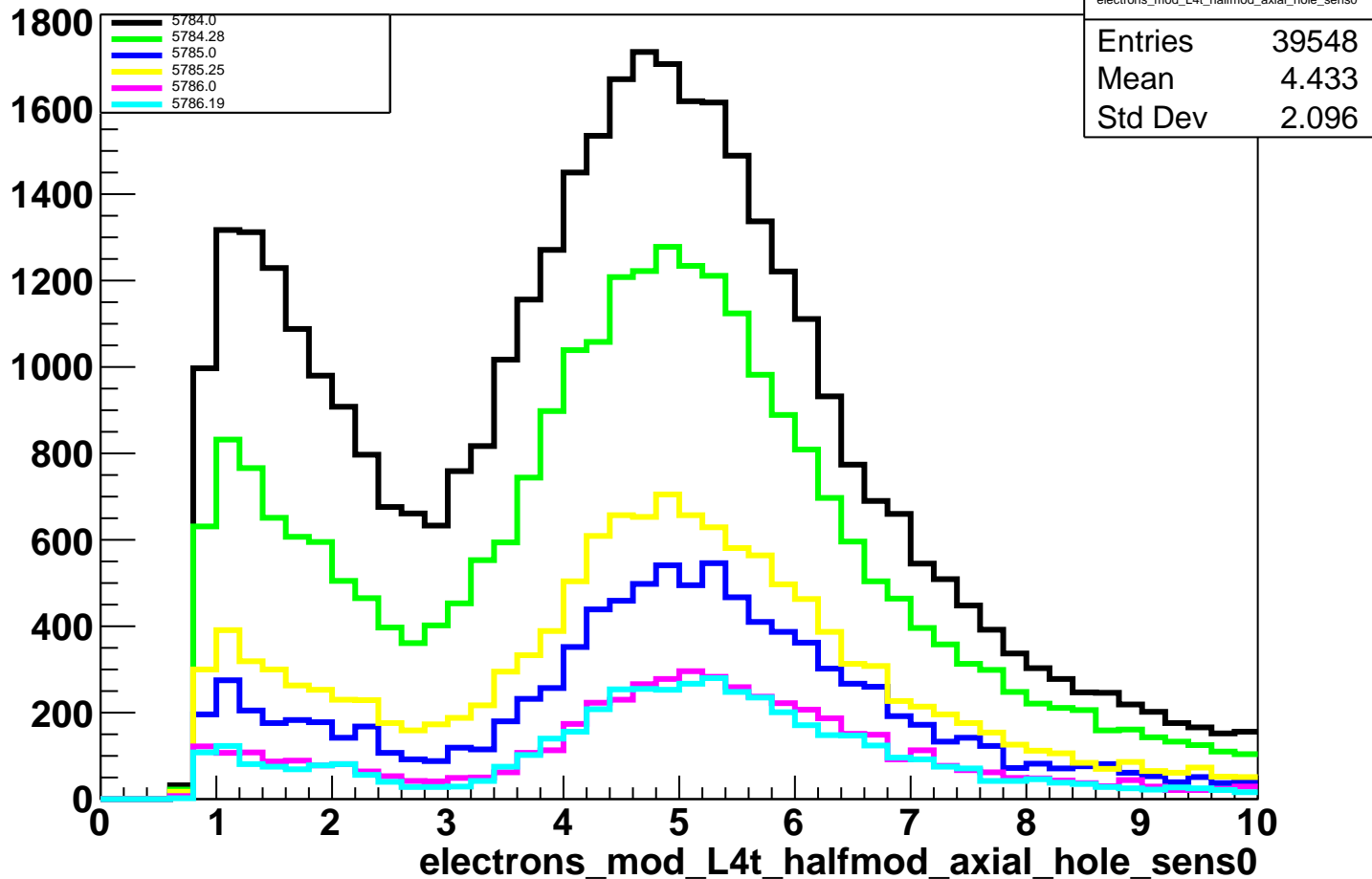
mod_L4b_halfmod_stereo_hole_sens0



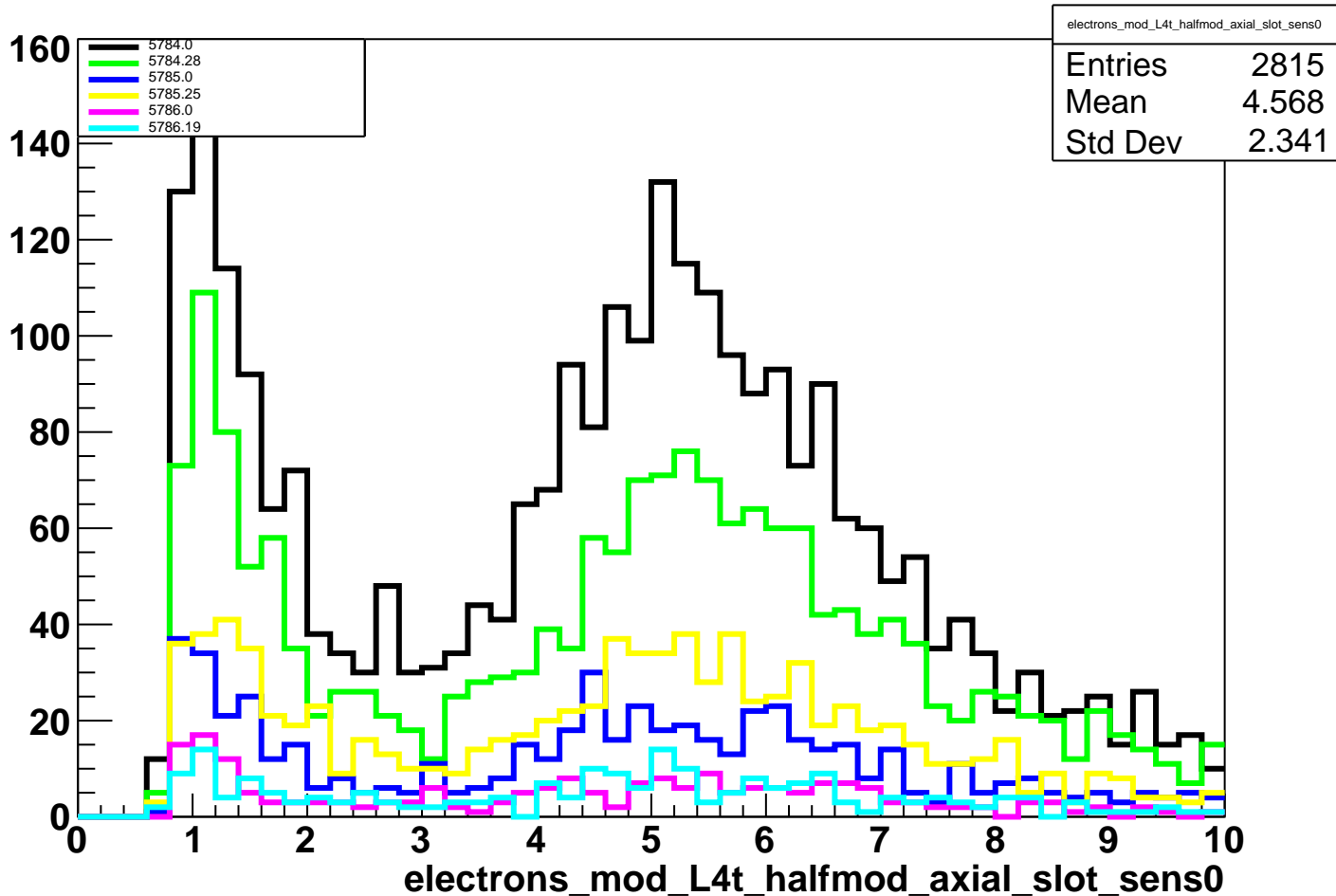
mod_L4b_halfmod_stereo_slot_sens0



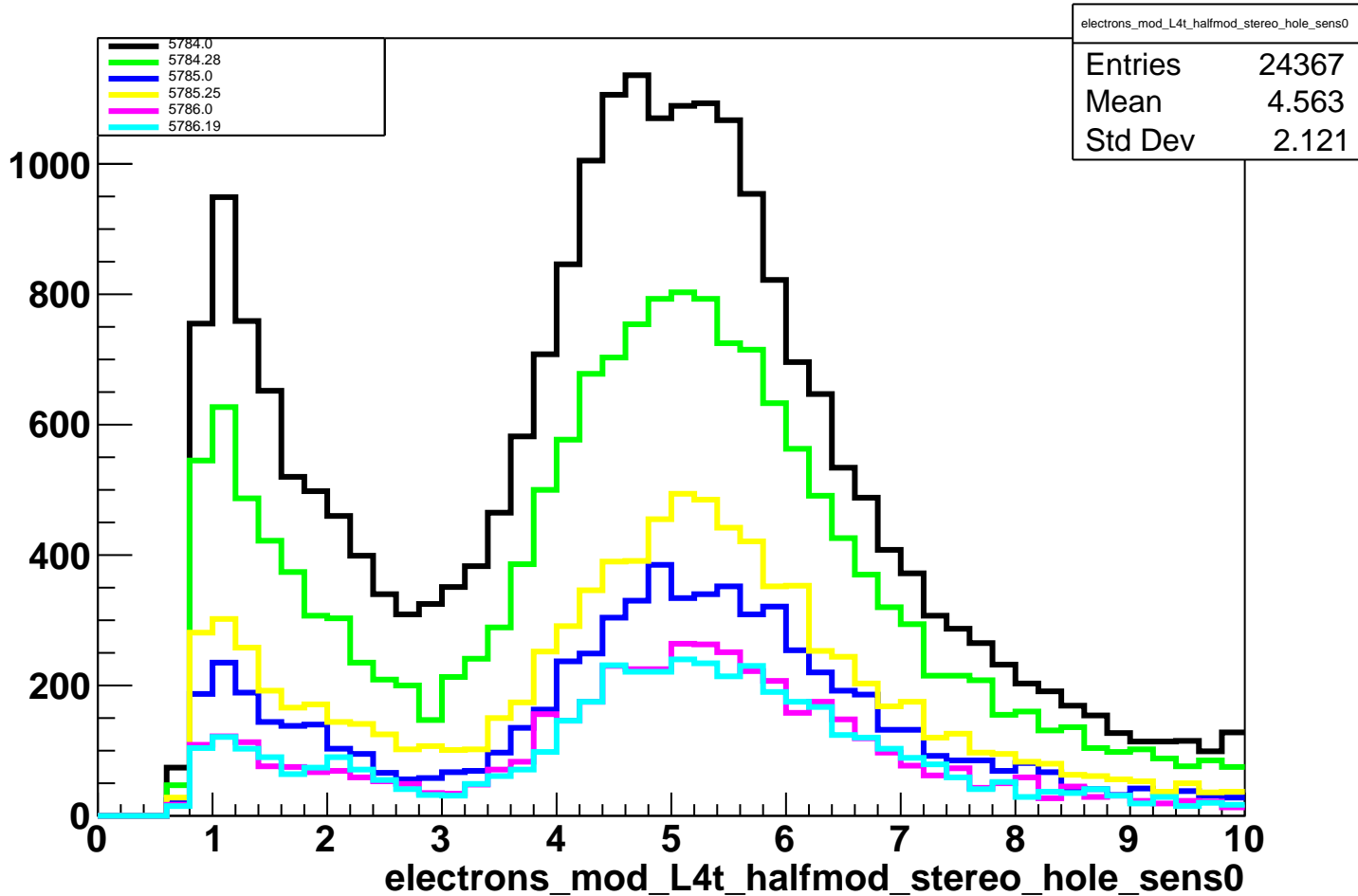
mod_L4t_halfmod_axial_hole_sens0



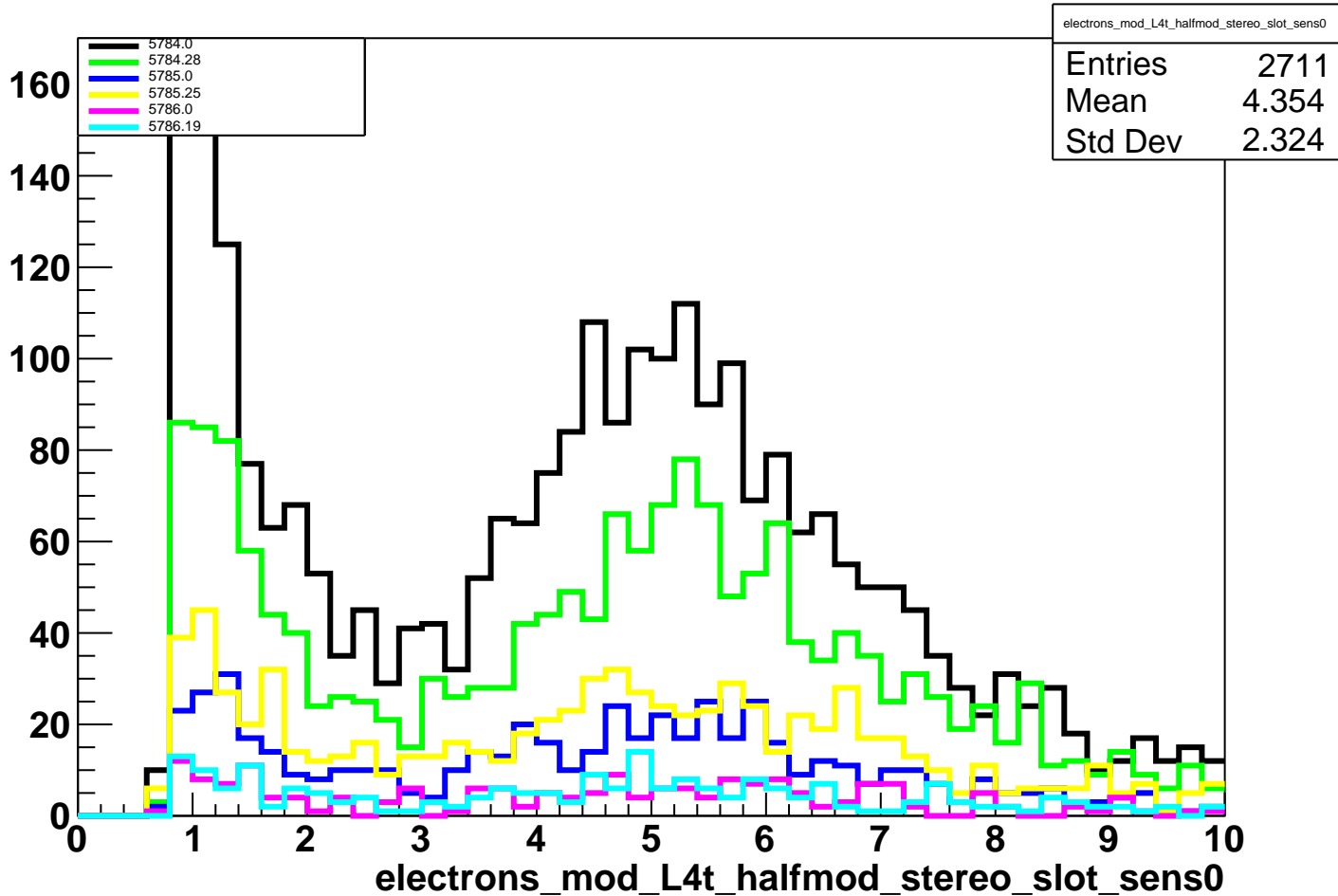
mod_L4t_halfmod_axial_slot_sens0



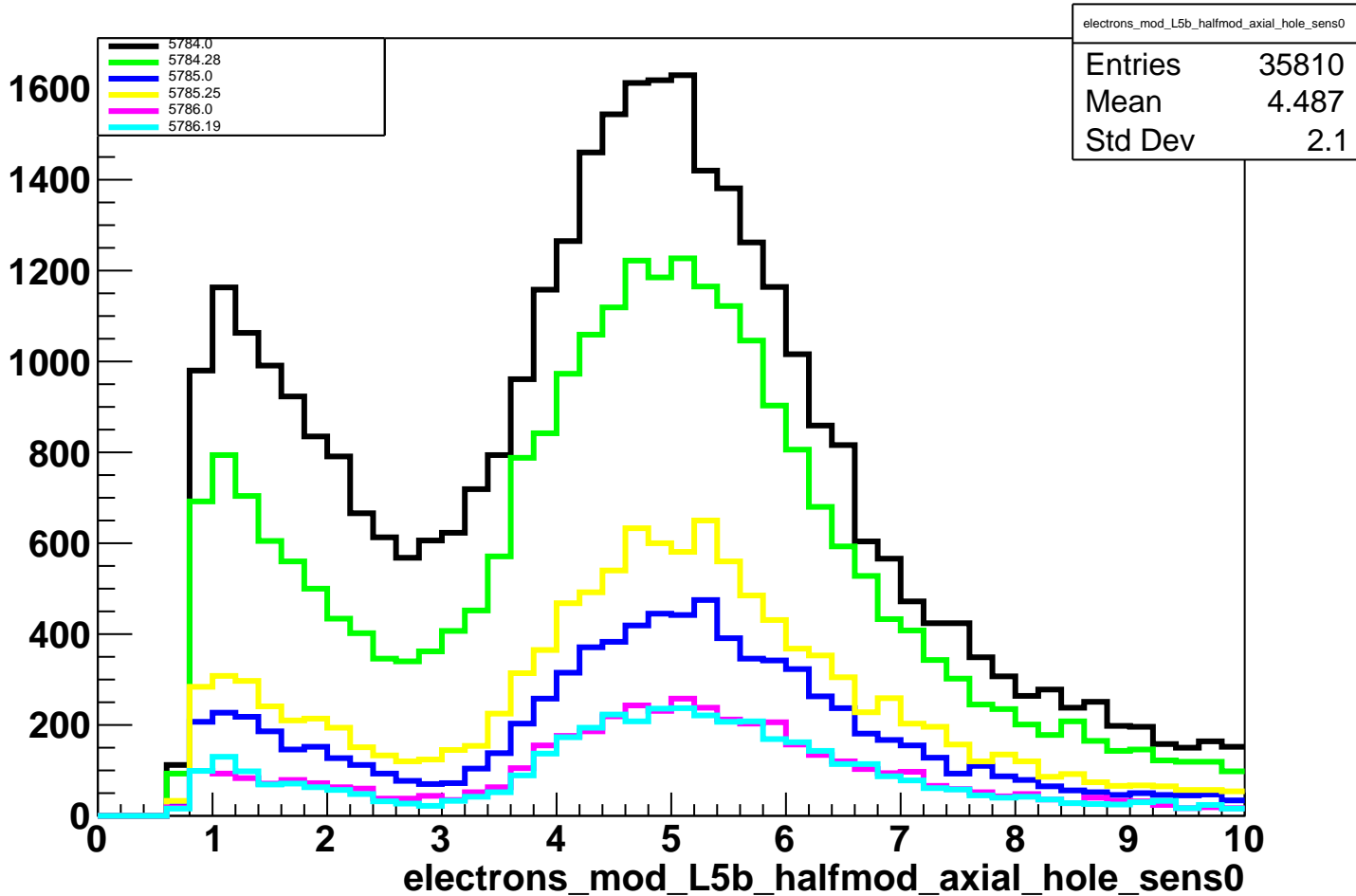
mod_L4t_halfmod_stereo_hole_sens0



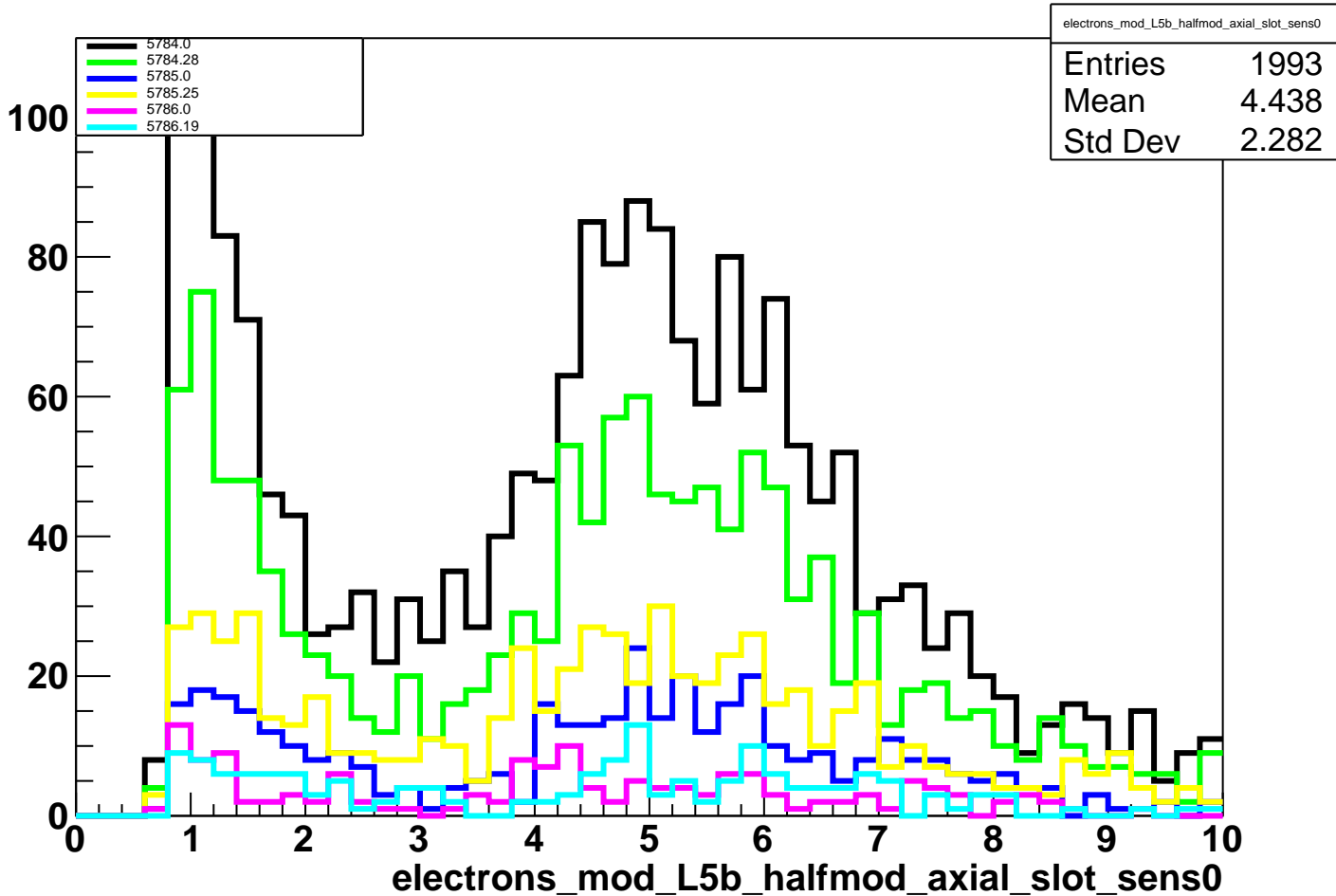
mod_L4t_halfmod_stereo_slot_sens0



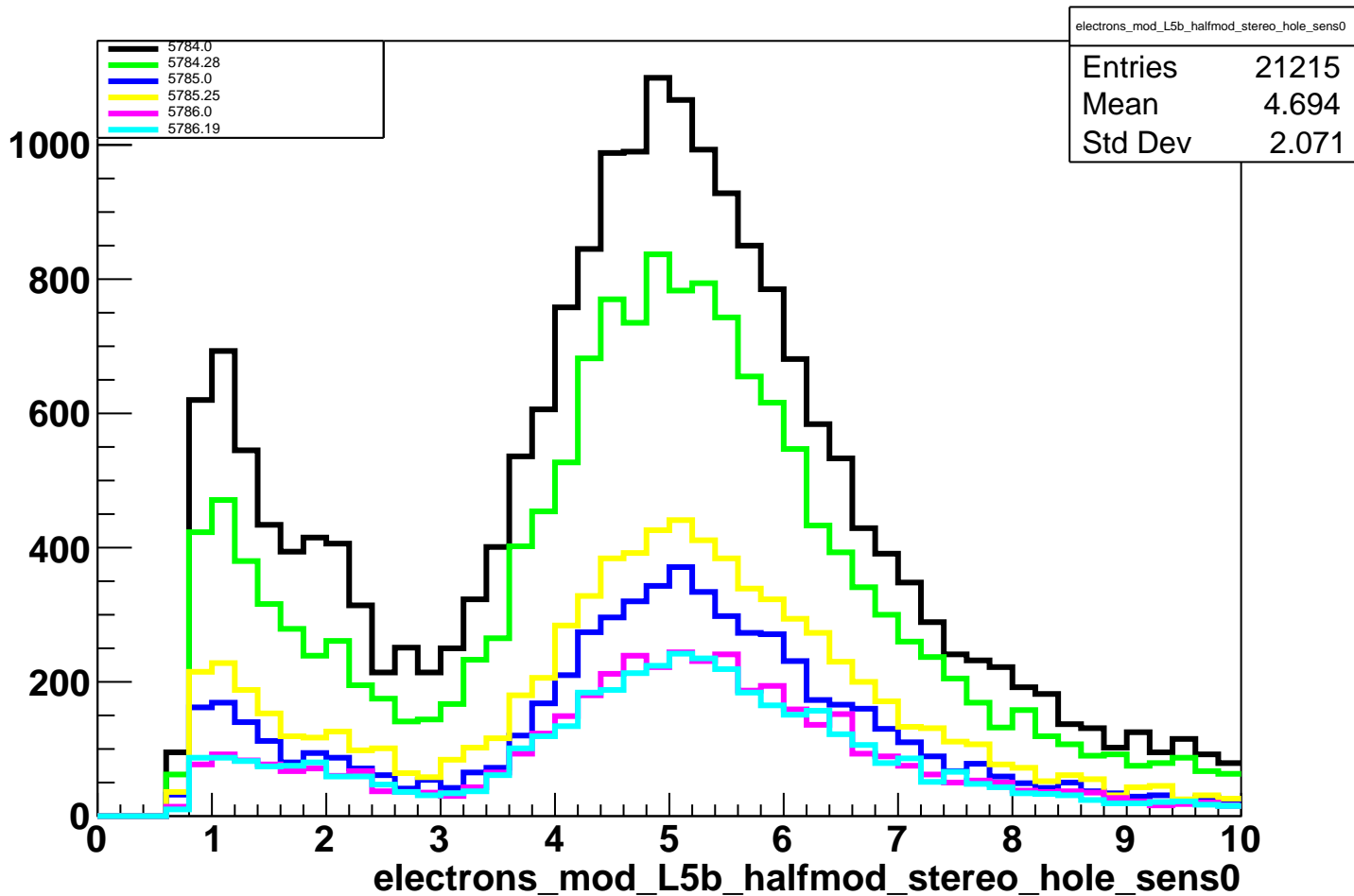
mod_L5b_halfmod_axial_hole_sens0



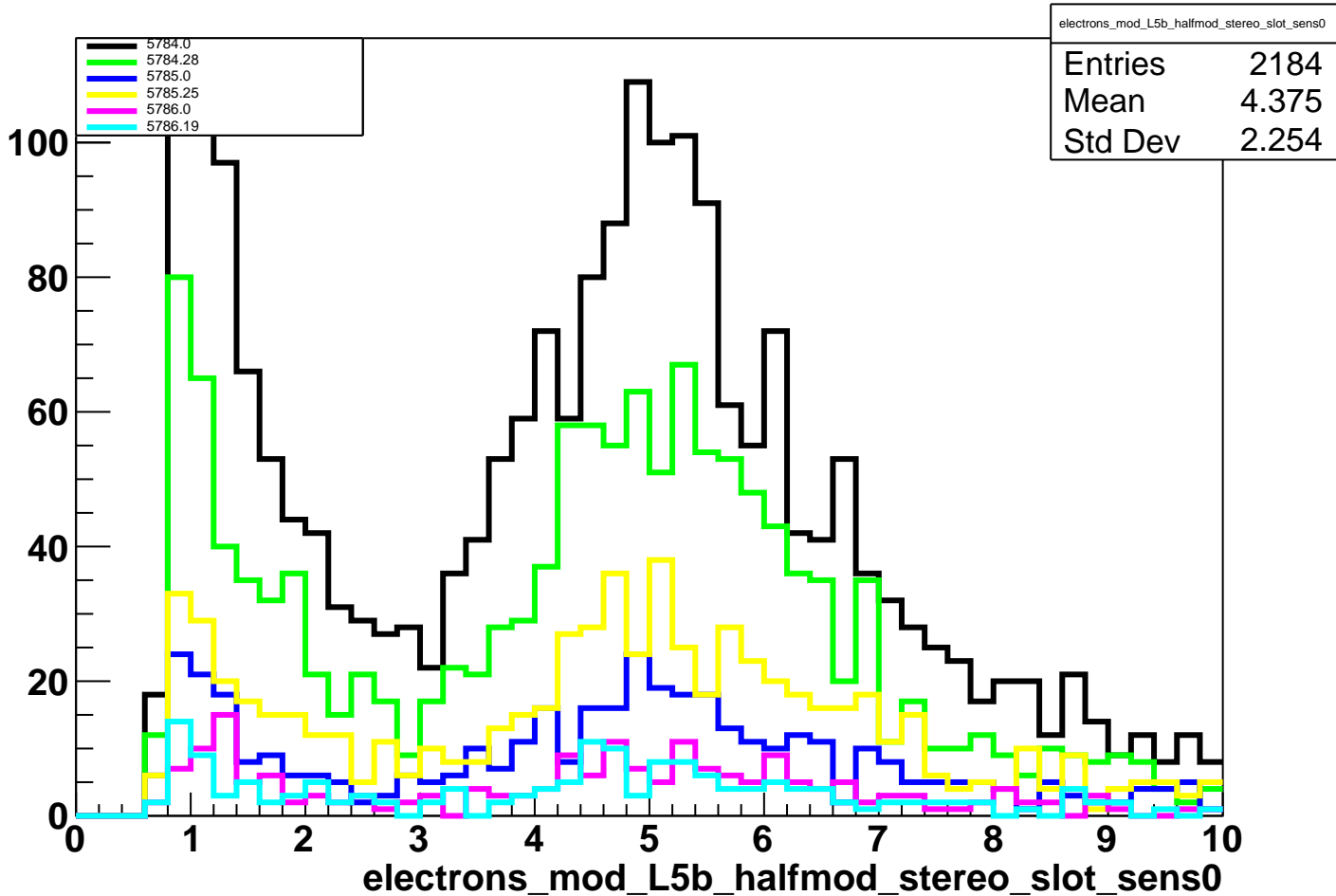
mod_L5b_halfmod_axial_slot_sens0



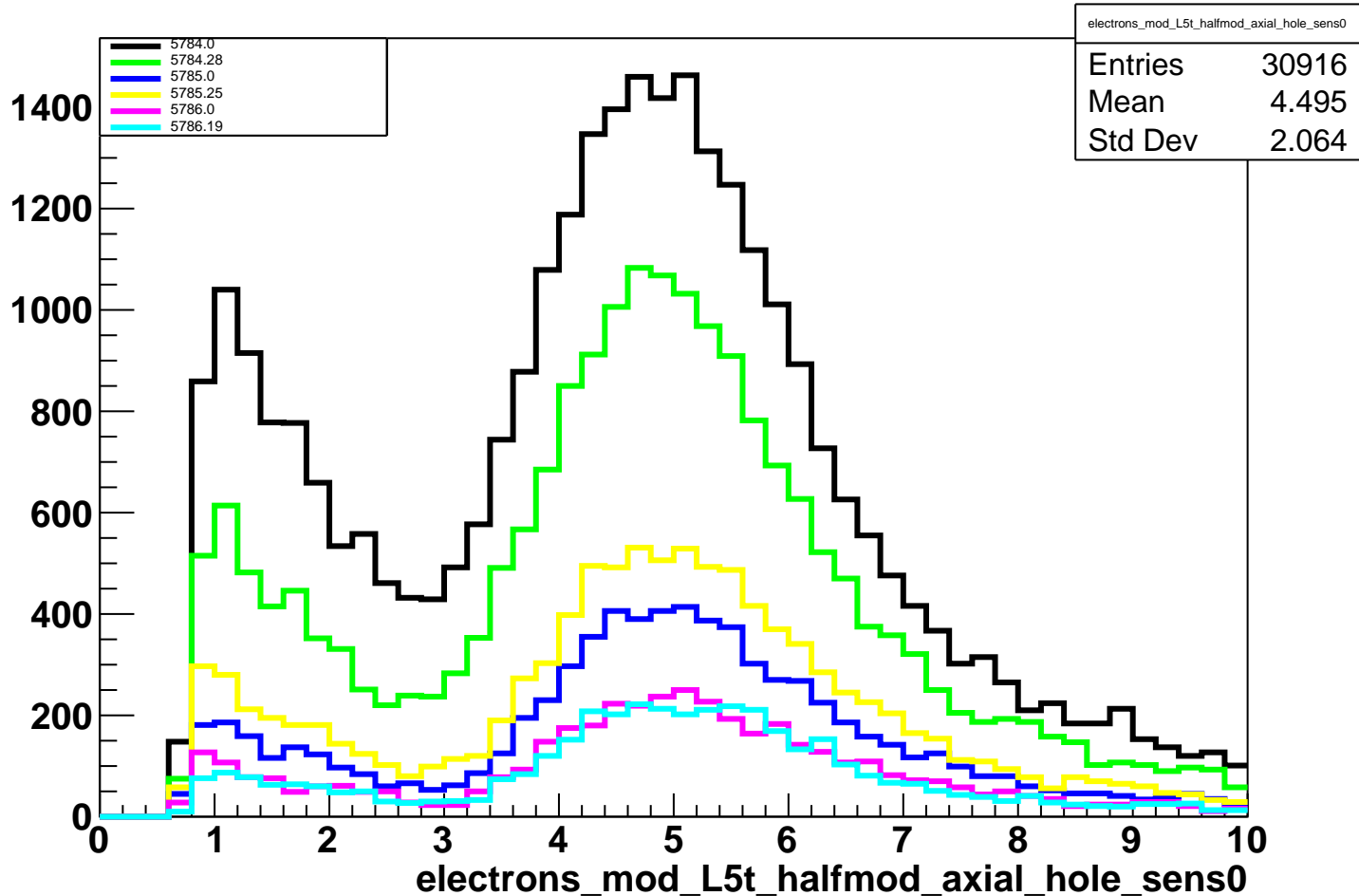
mod_L5b_halfmod_stereo_hole_sens0



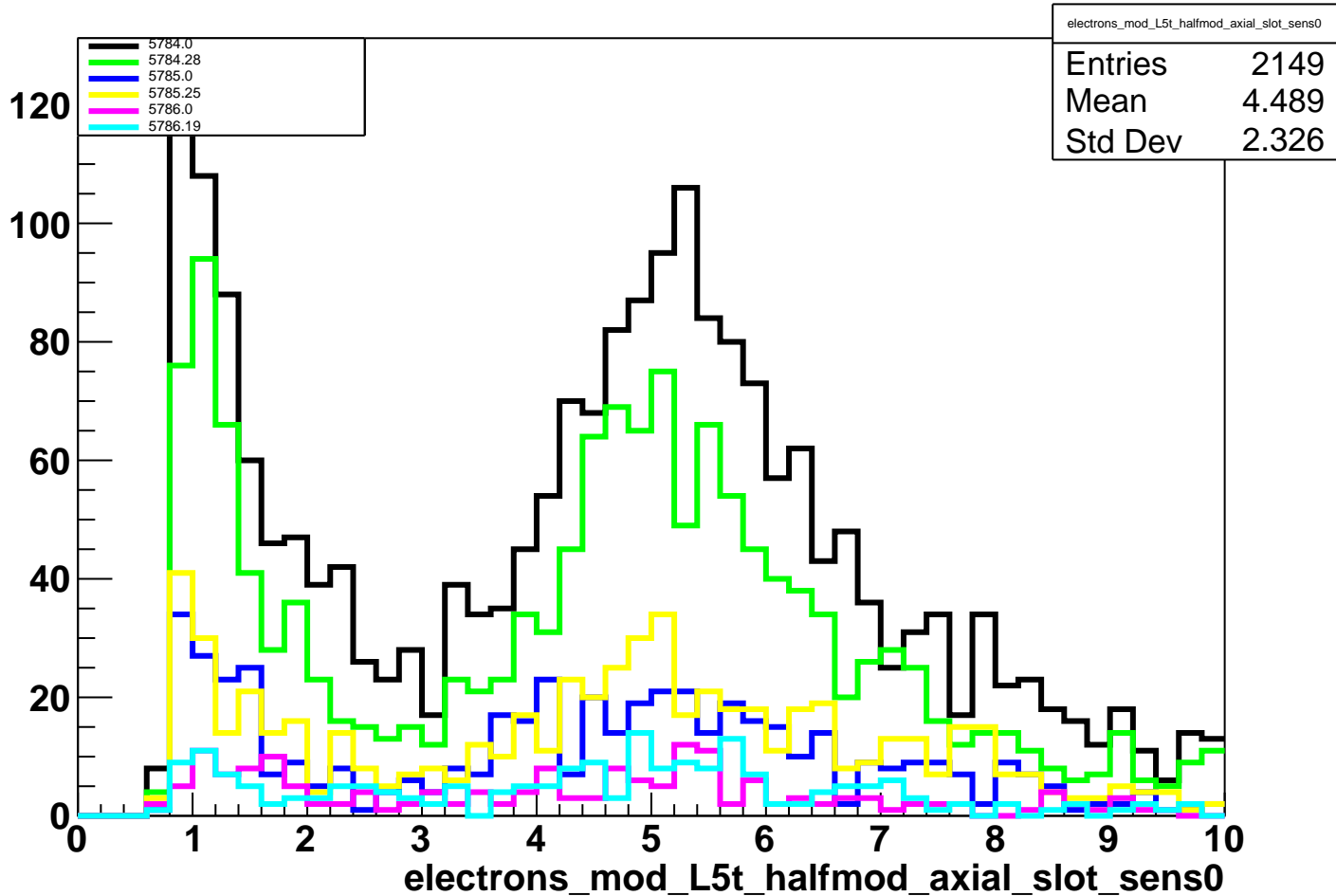
mod_L5b_halfmod_stereo_slot_sens0



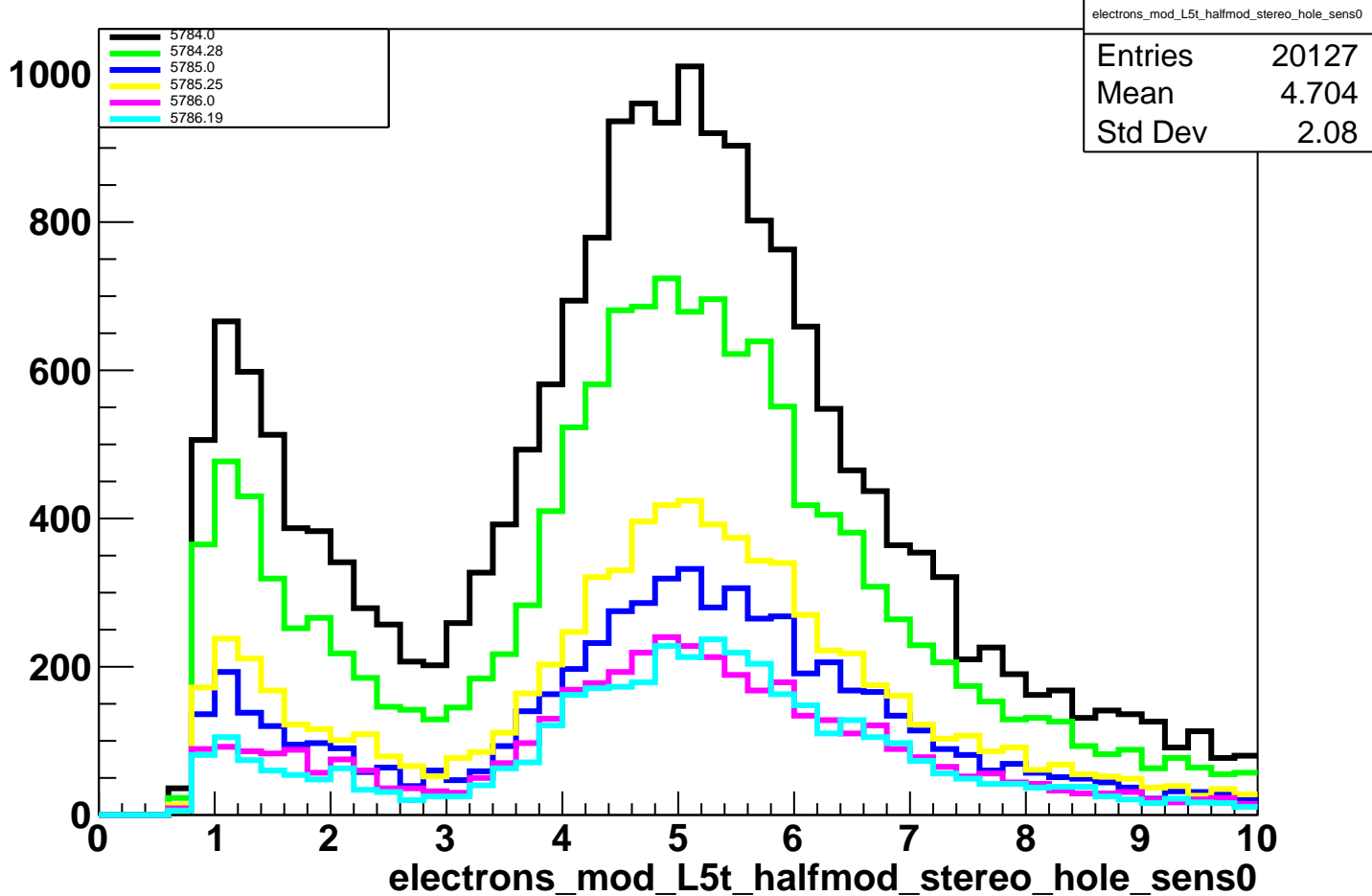
mod_L5t_halfmod_axial_hole_sens0



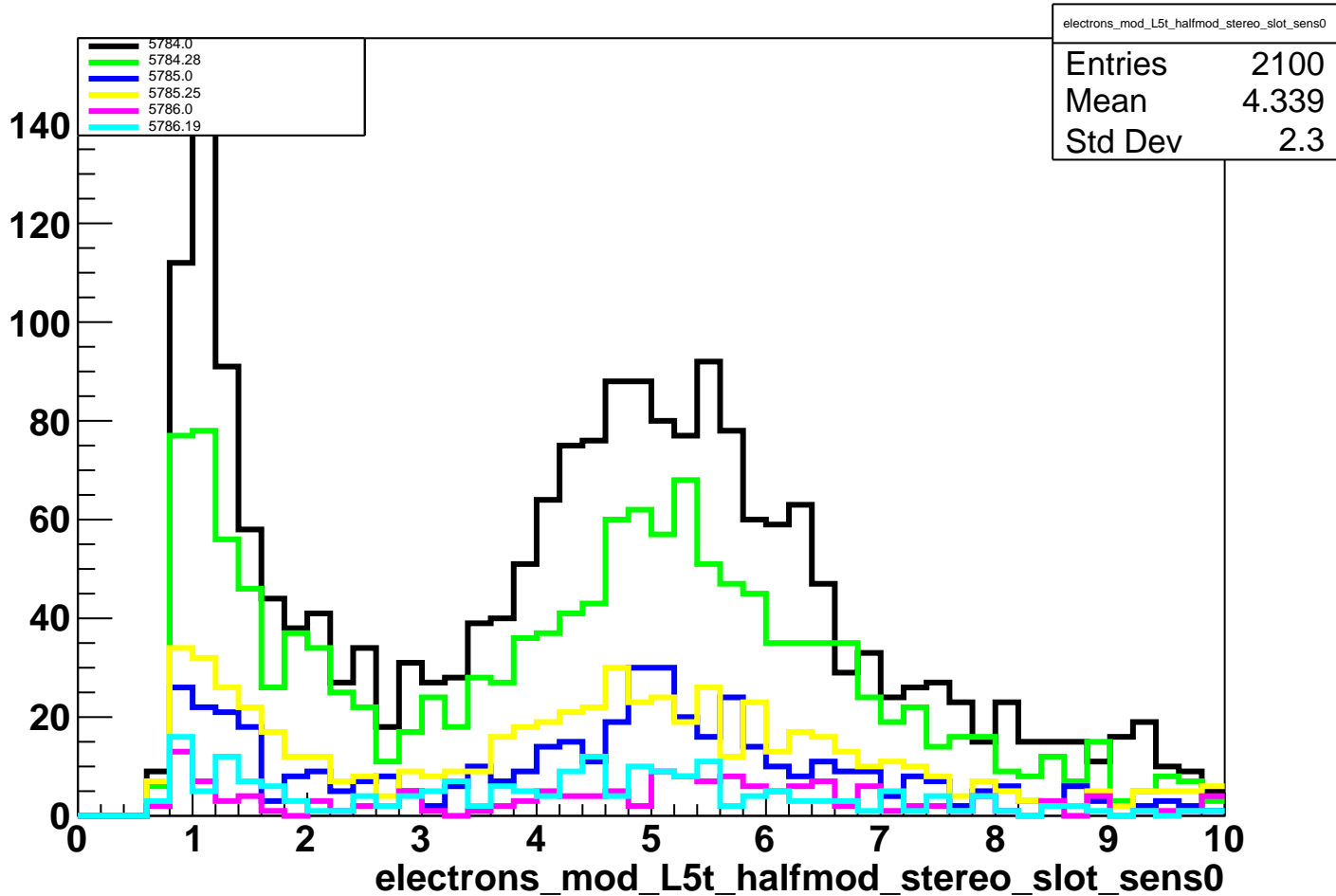
mod_L5t_halfmod_axial_slot_sens0



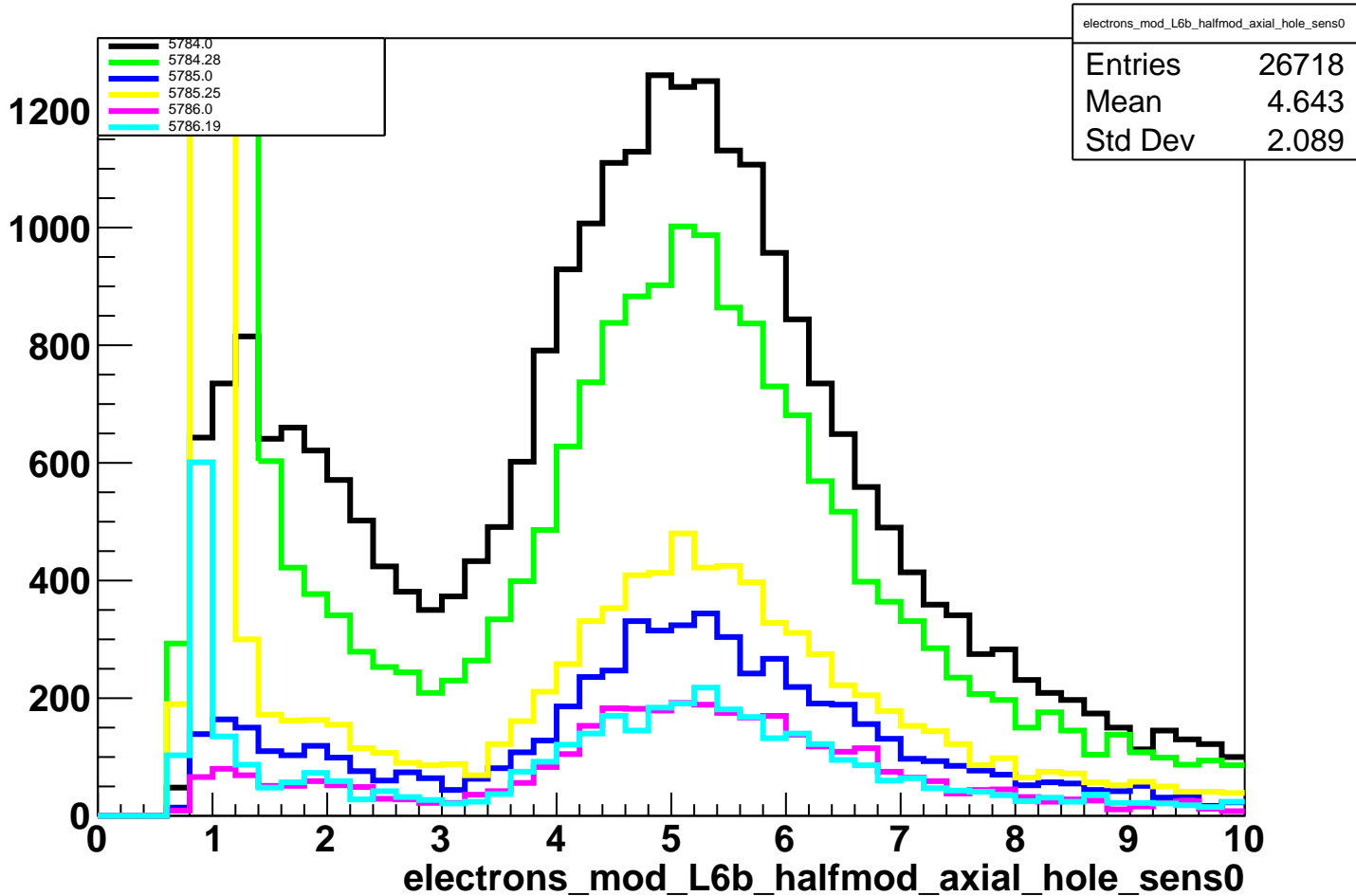
mod_L5t_halfmod_stereo_hole_sens0



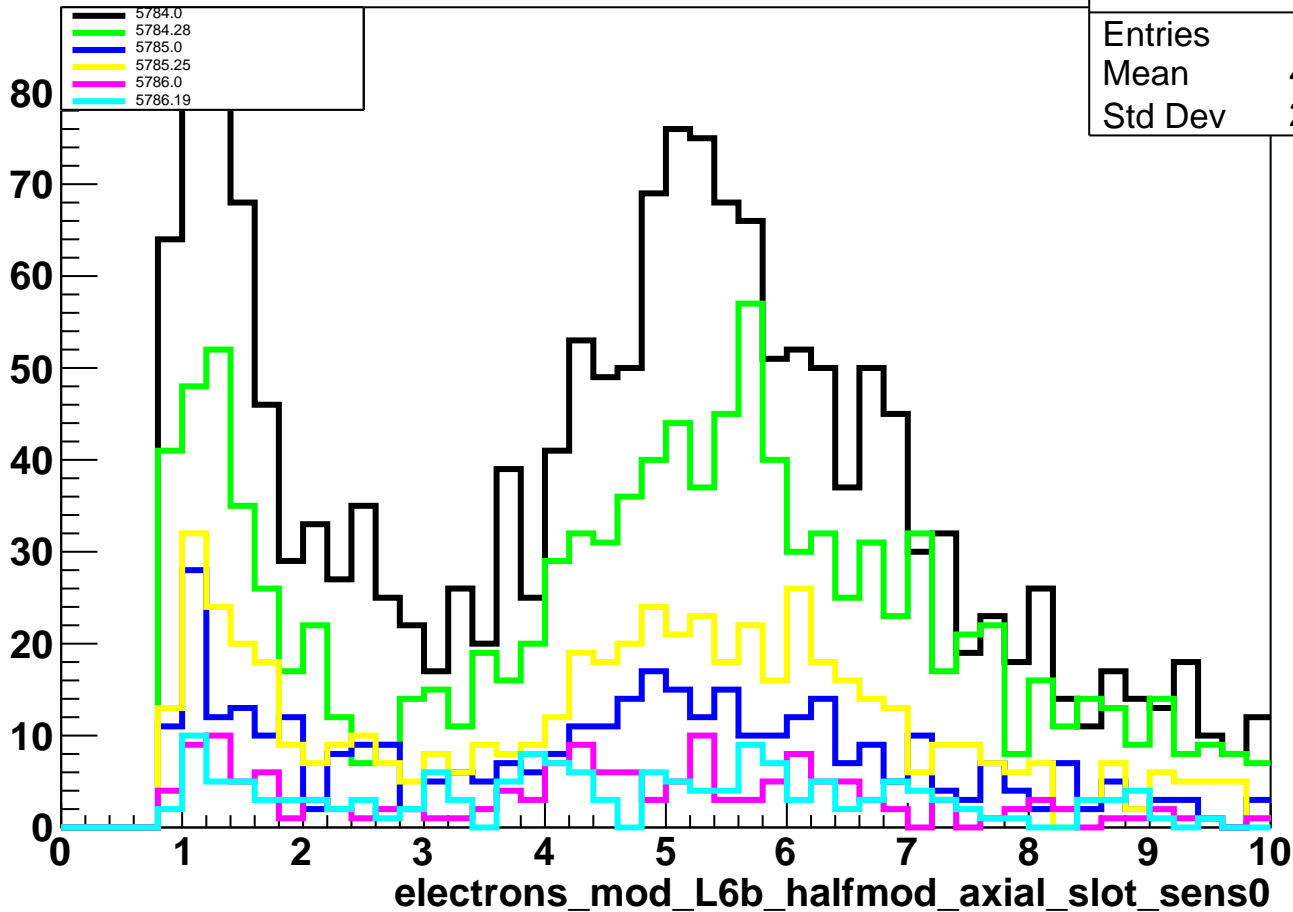
mod_L5t_halfmod_stereo_slot_sens0



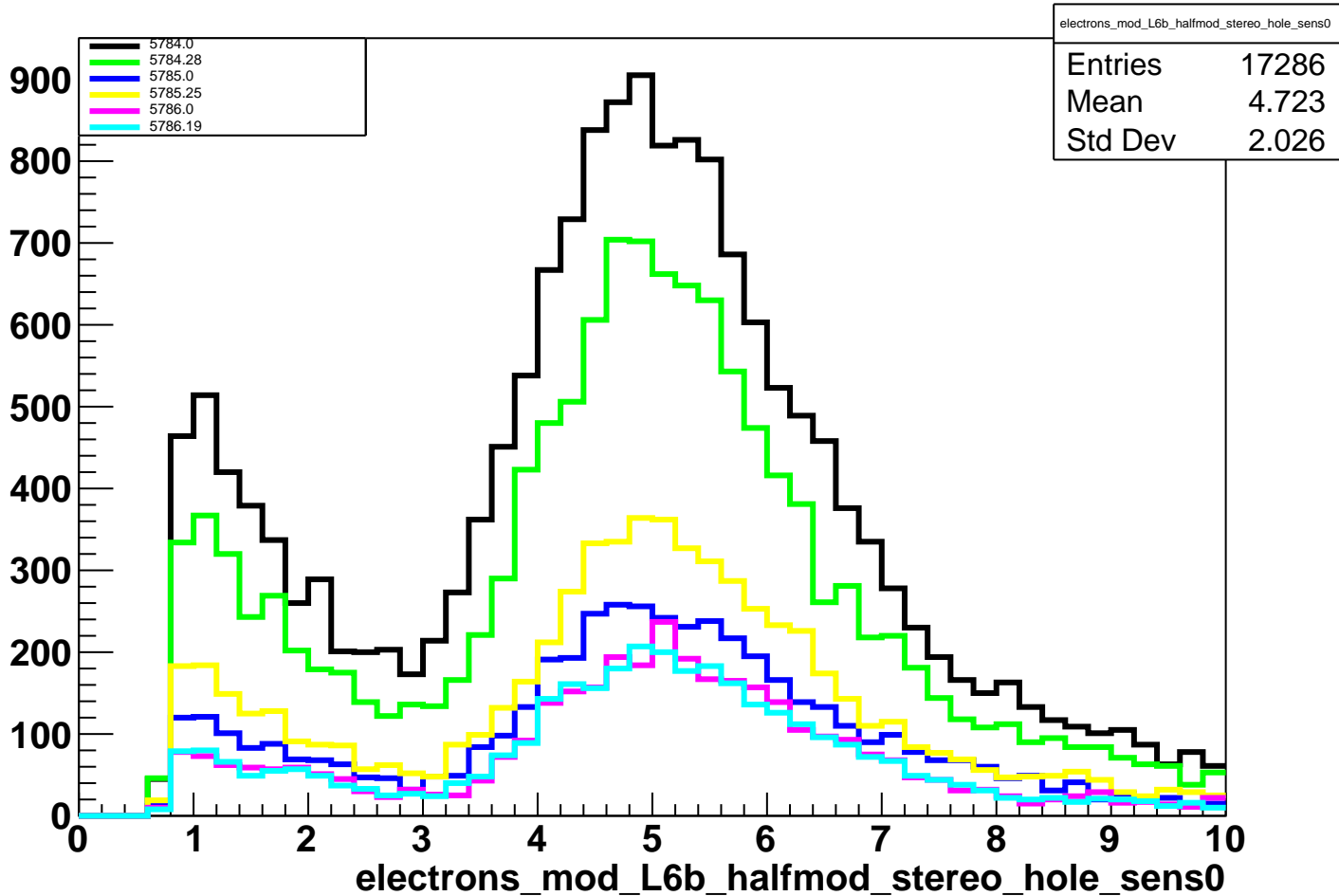
mod_L6b_halfmod_axial_hole_sens0



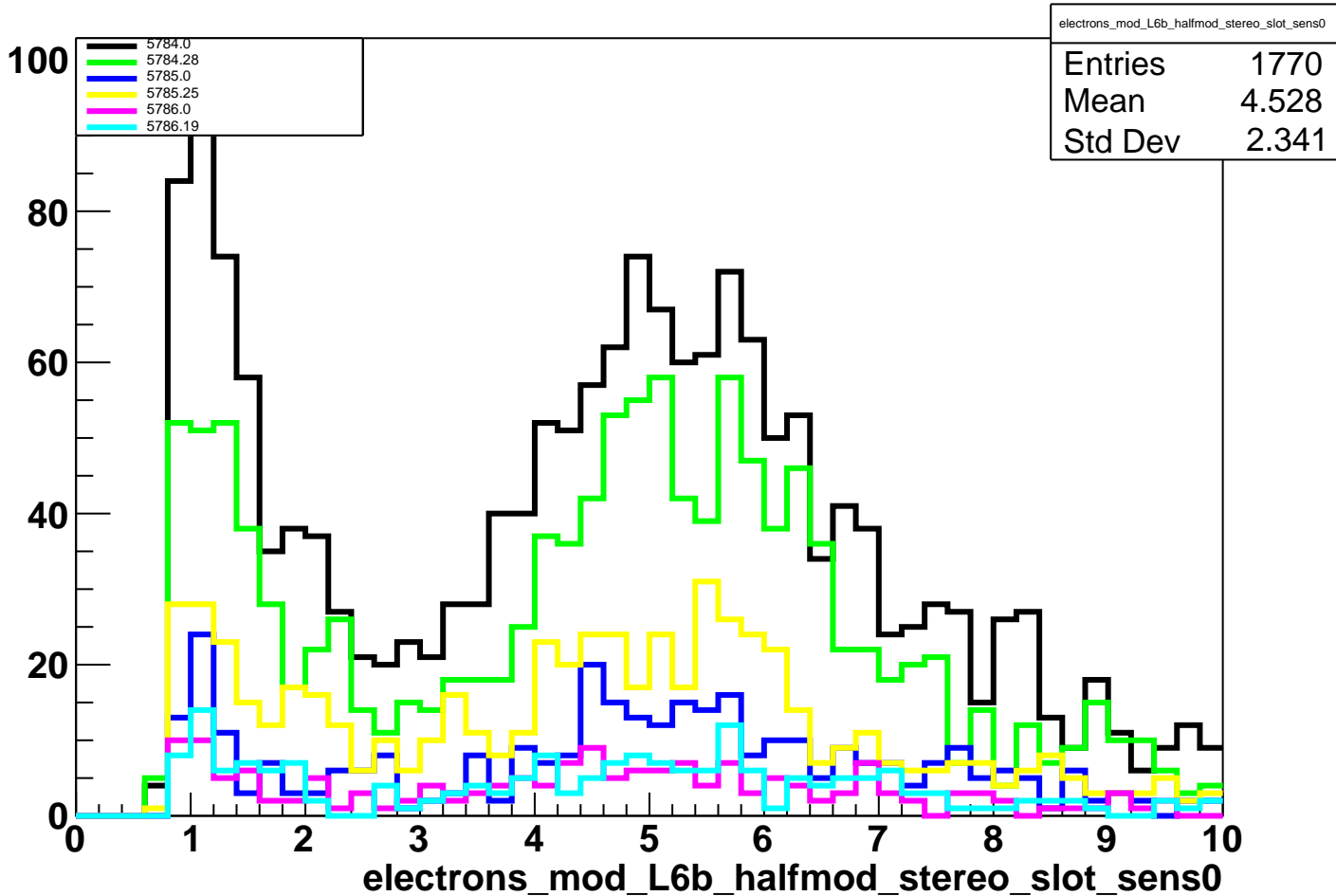
mod_L6b_halfmod_axial_slot_sens0



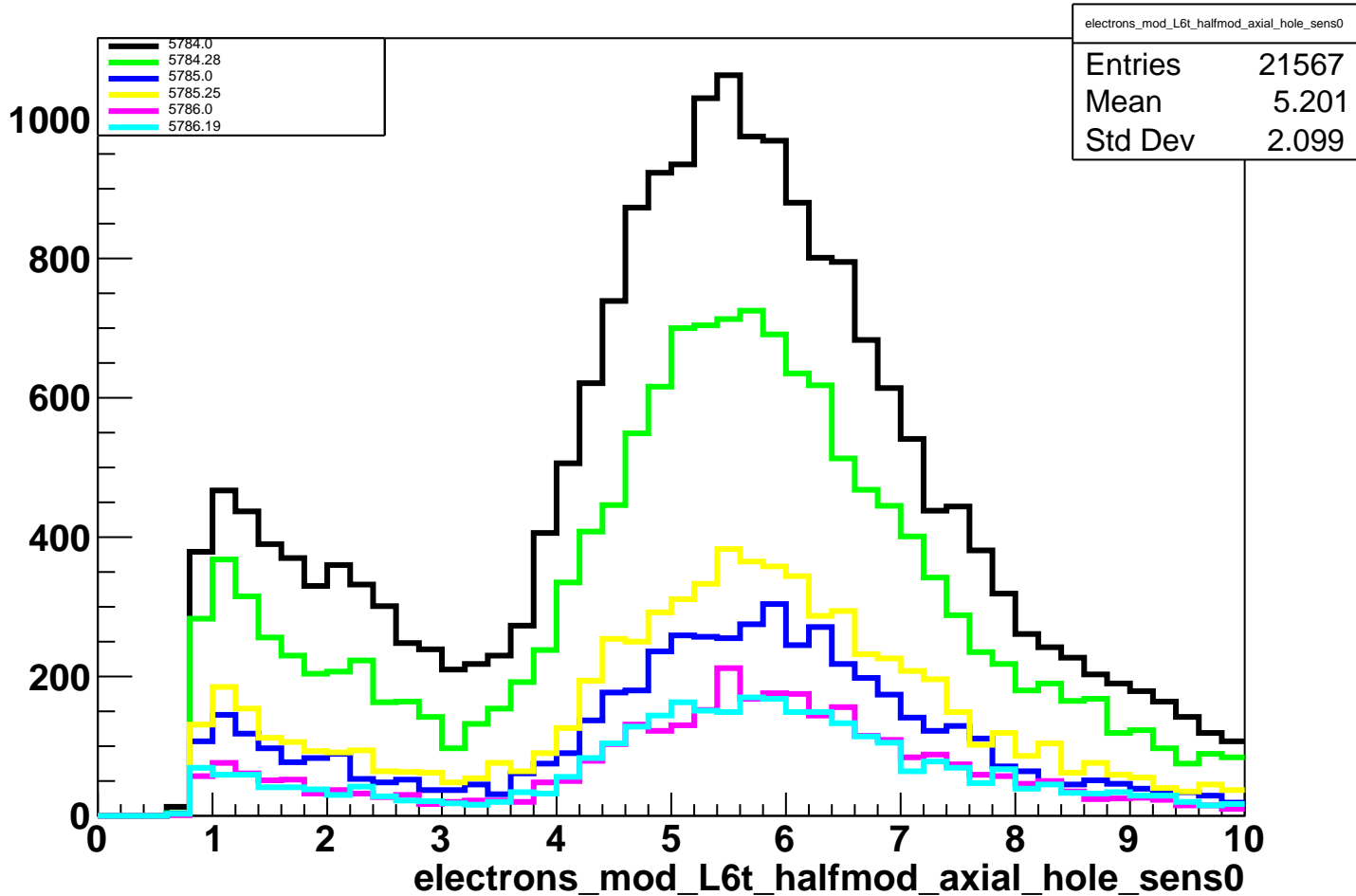
mod_L6b_halfmod_stereo_hole_sens0



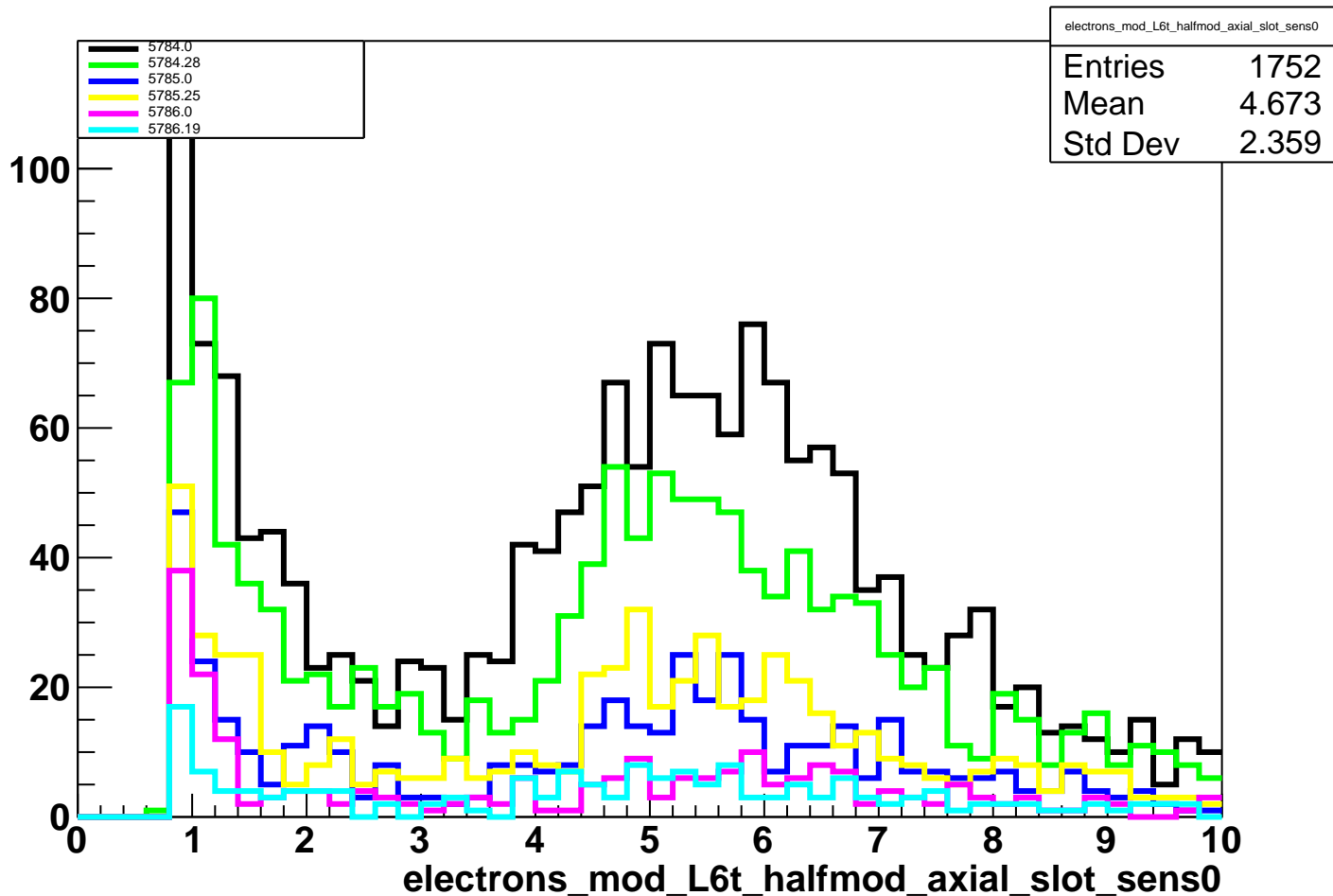
mod_L6b_halfmod_stereo_slot_sens0



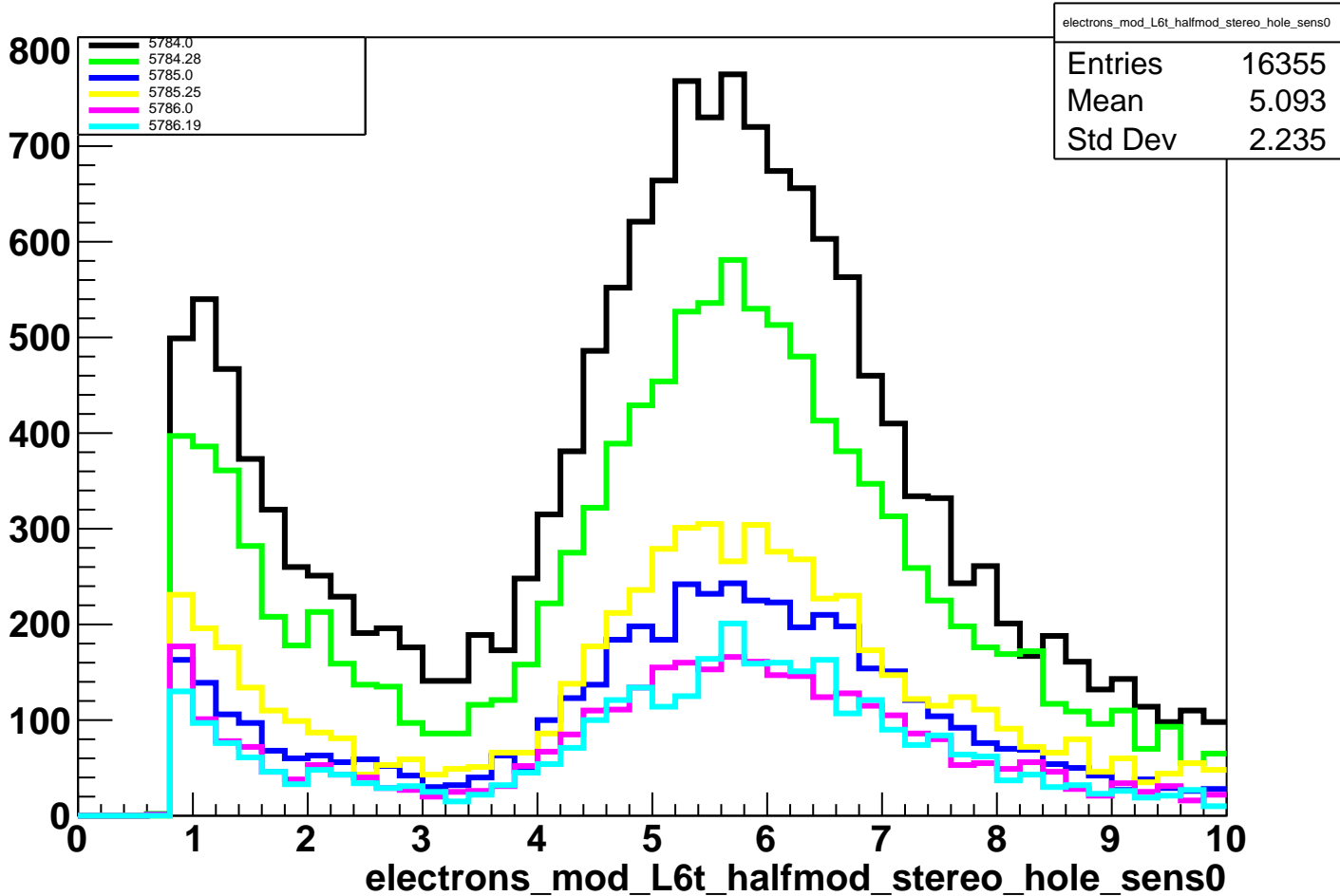
mod_L6t_halfmod_axial_hole_sens0



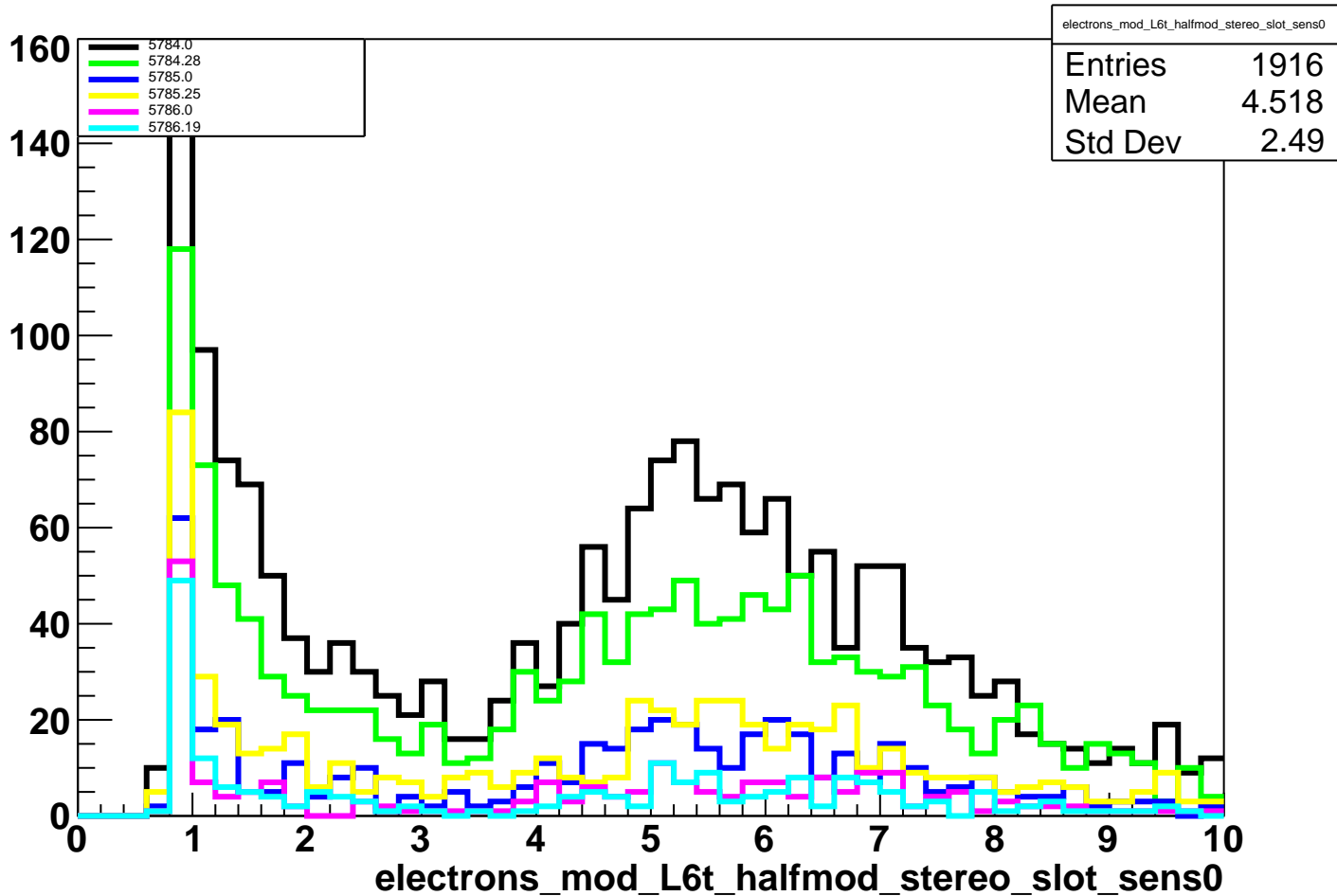
mod_L6t_halfmod_axial_slot_sens0



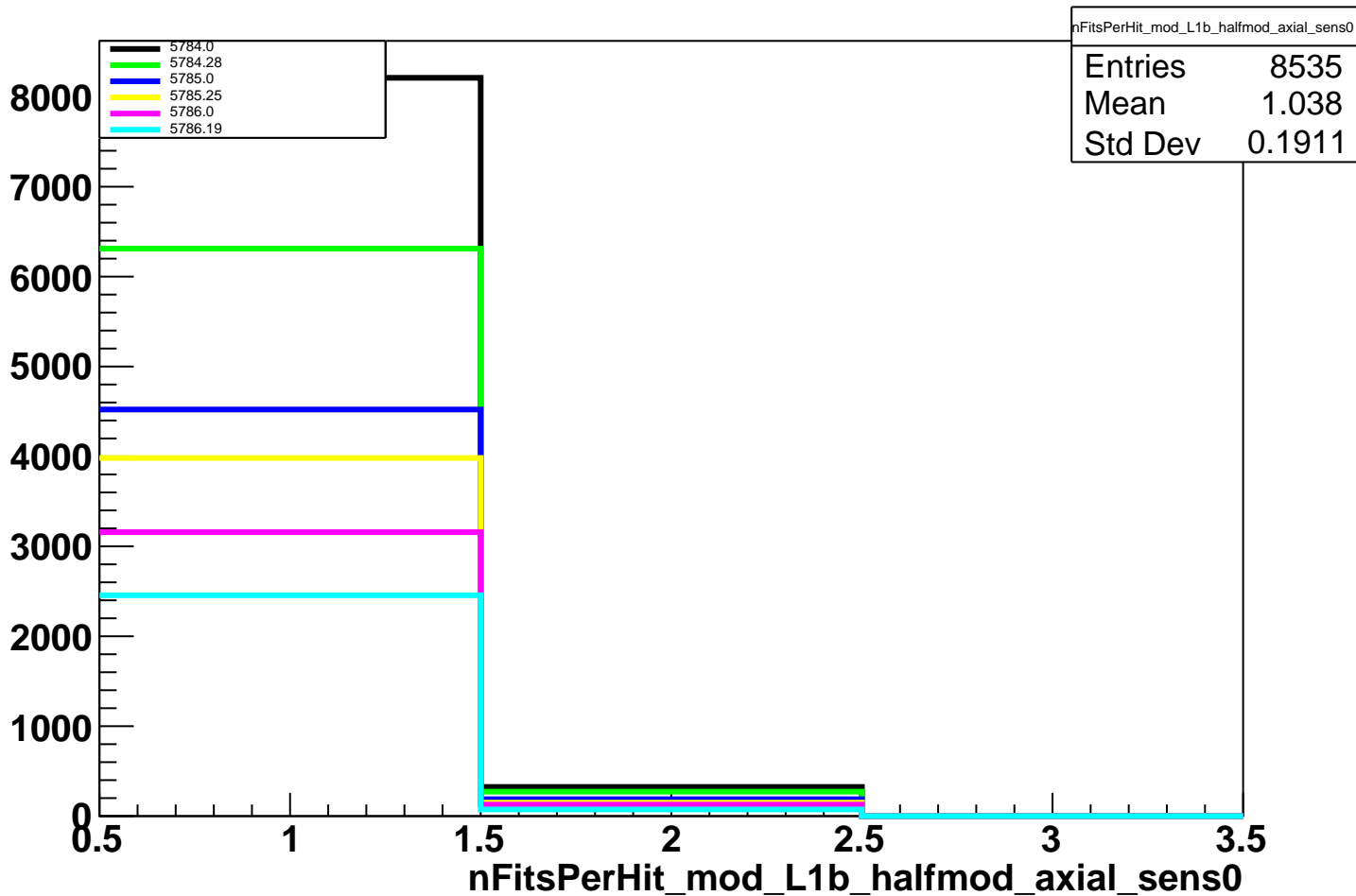
mod_L6t_halfmod_stereo_hole_sens0



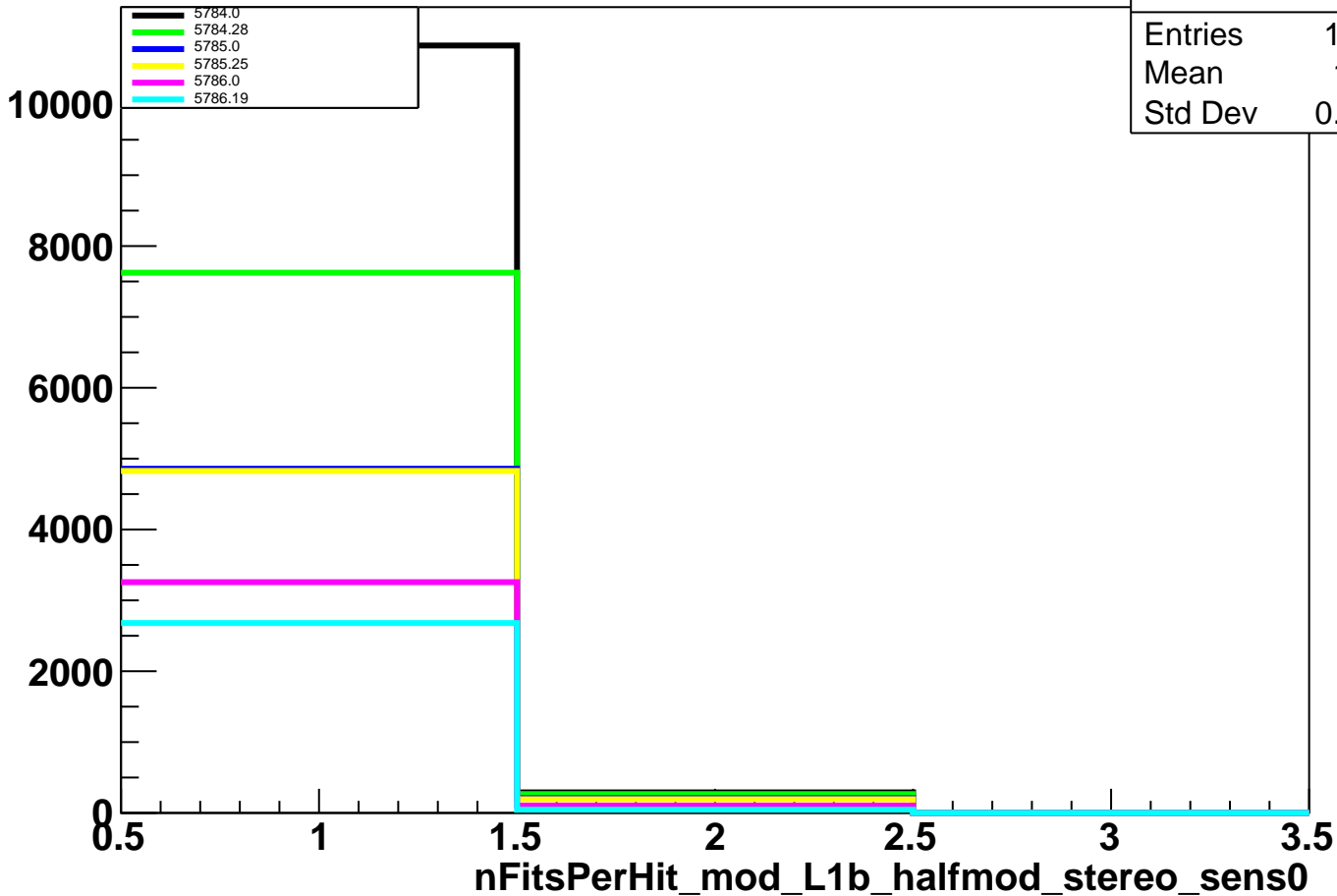
mod_L6t_halfmod_stereo_slot_sens0



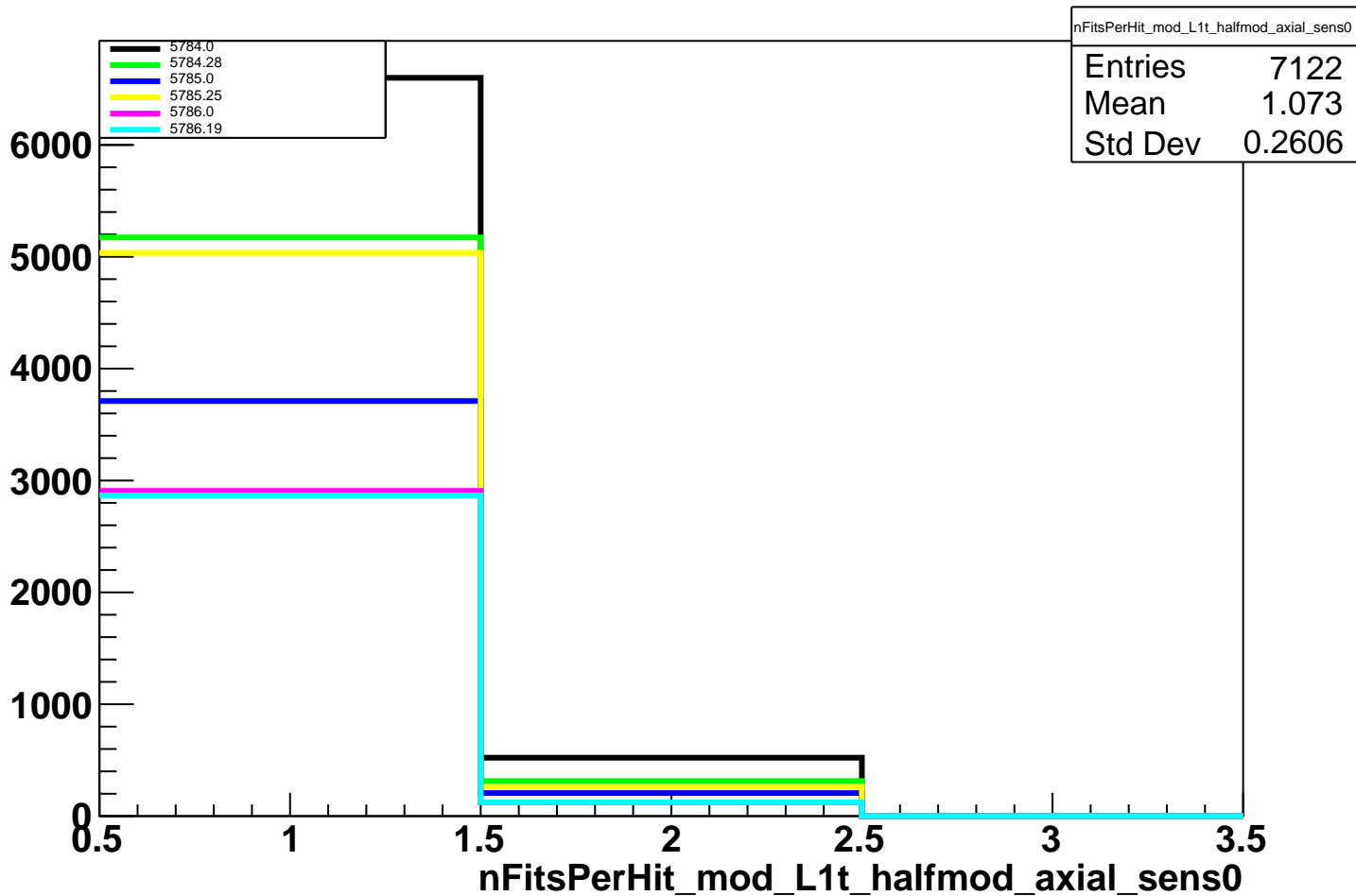
mod_L1b_halfmod_axial_sens0



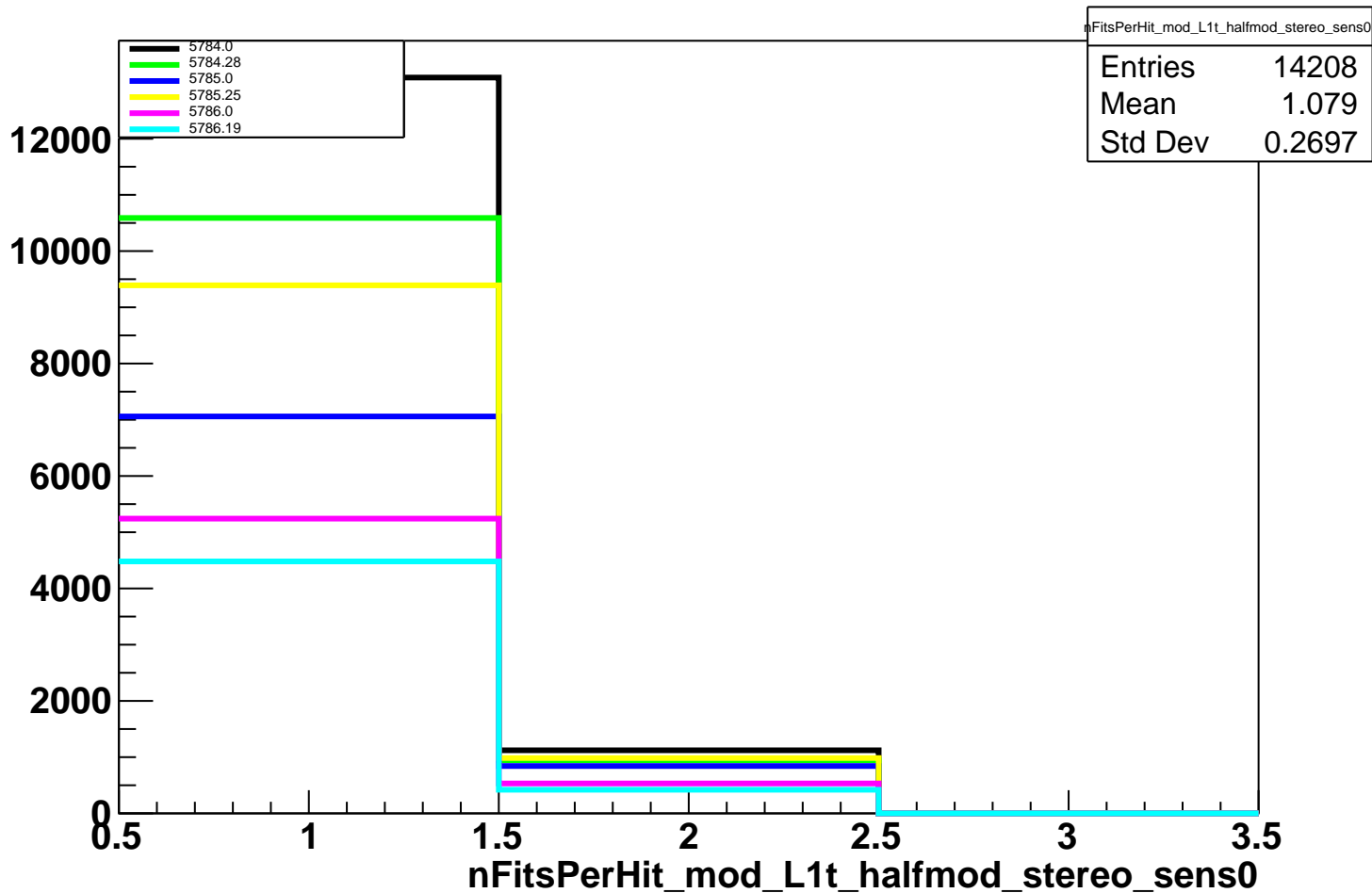
mod_L1b_halfmod_stereo_sens0



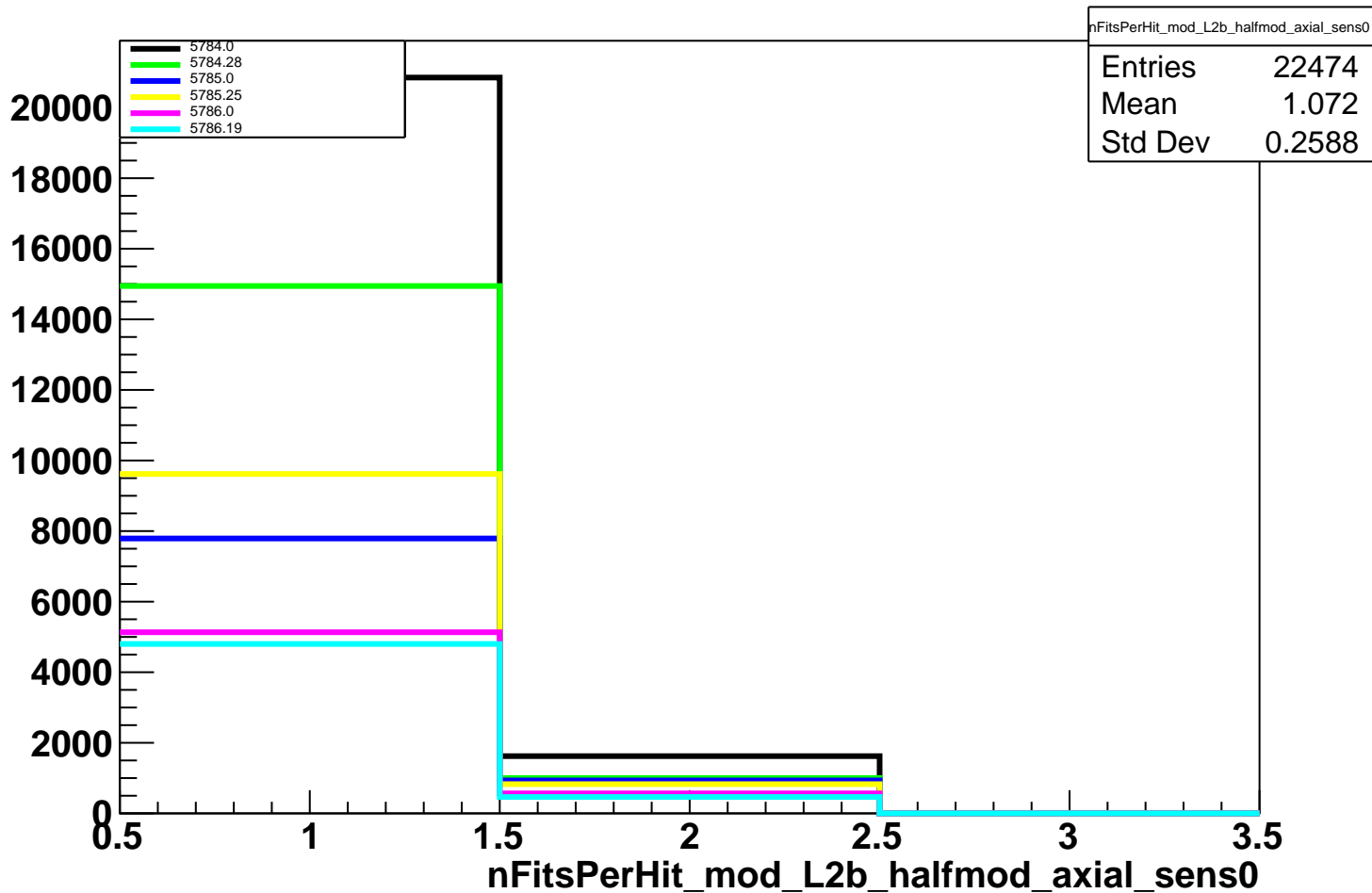
mod_L1t_halfmod_axial_sens0



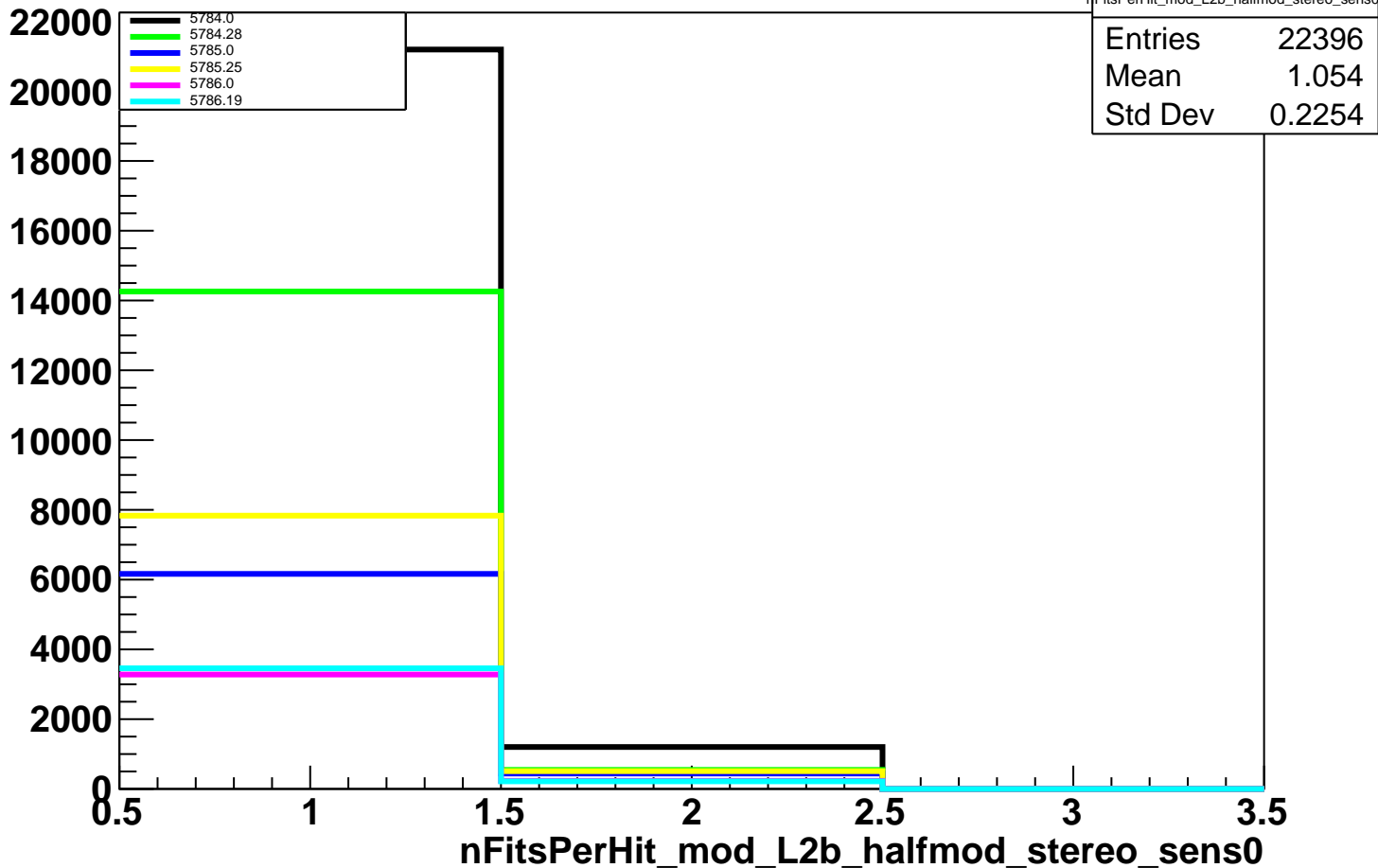
mod_L1t_halfmod_stereo_sens0



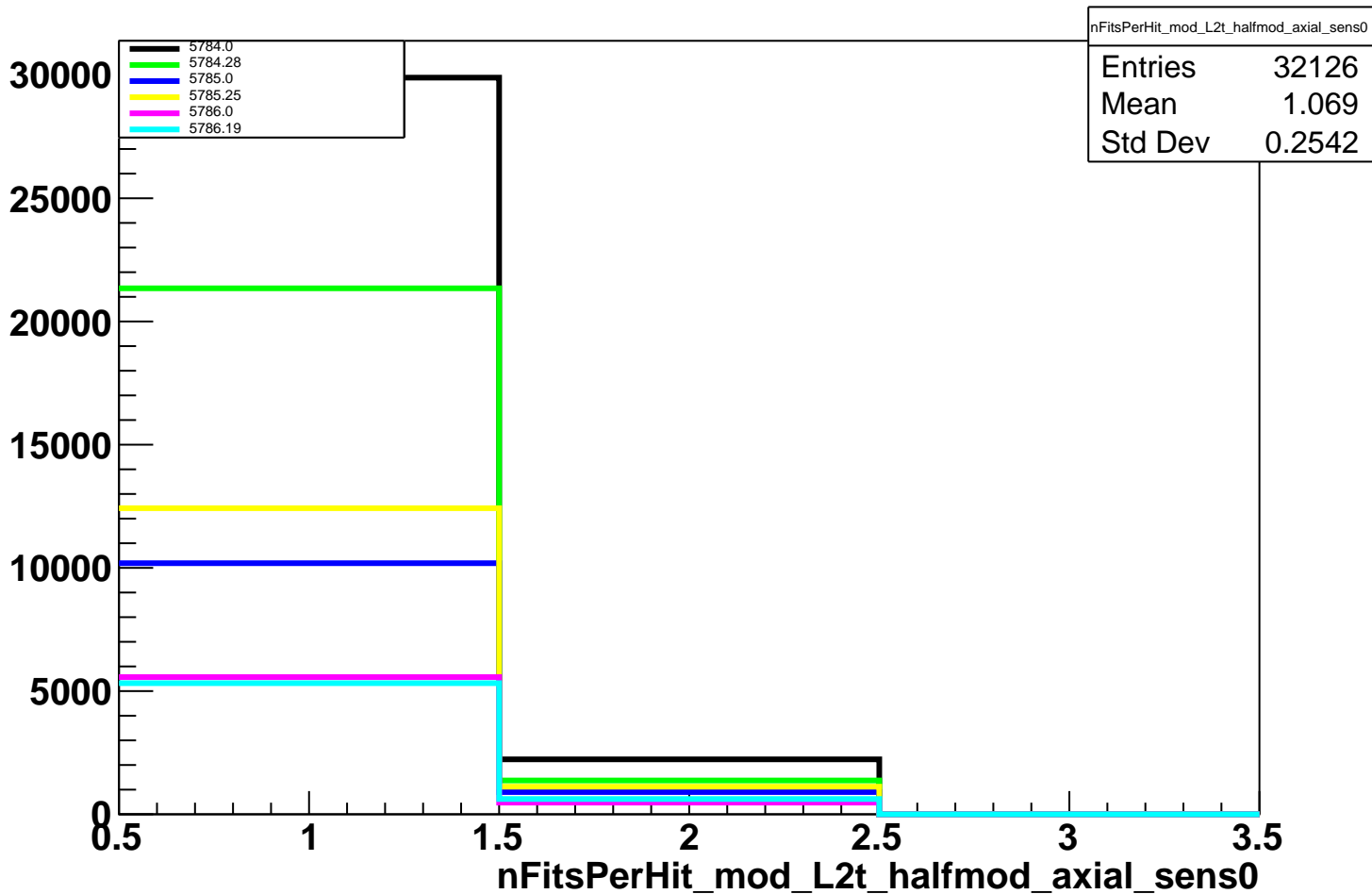
mod_L2b_halfmod_axial_sens0



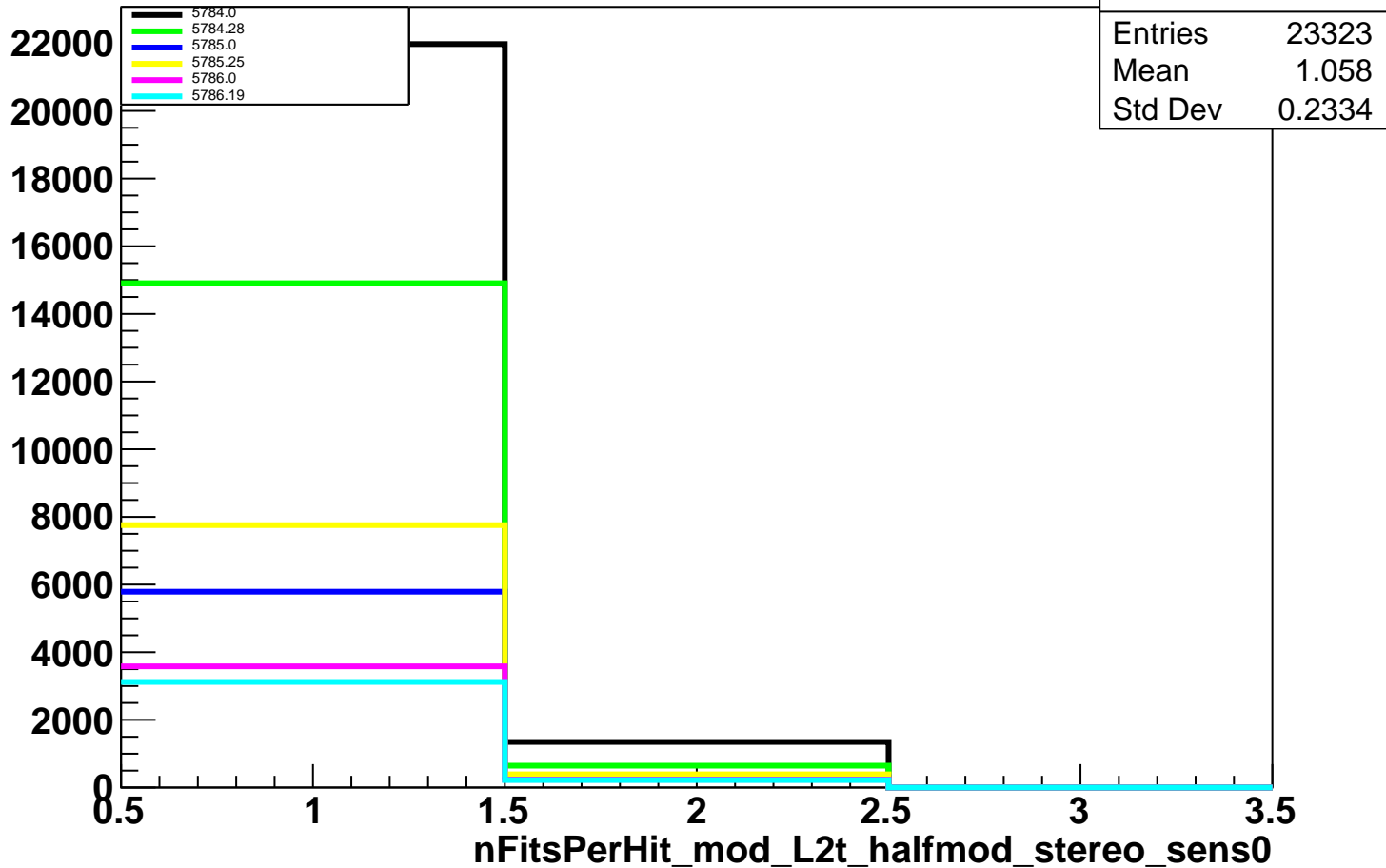
mod_L2b_halfmod_stereo_sens0



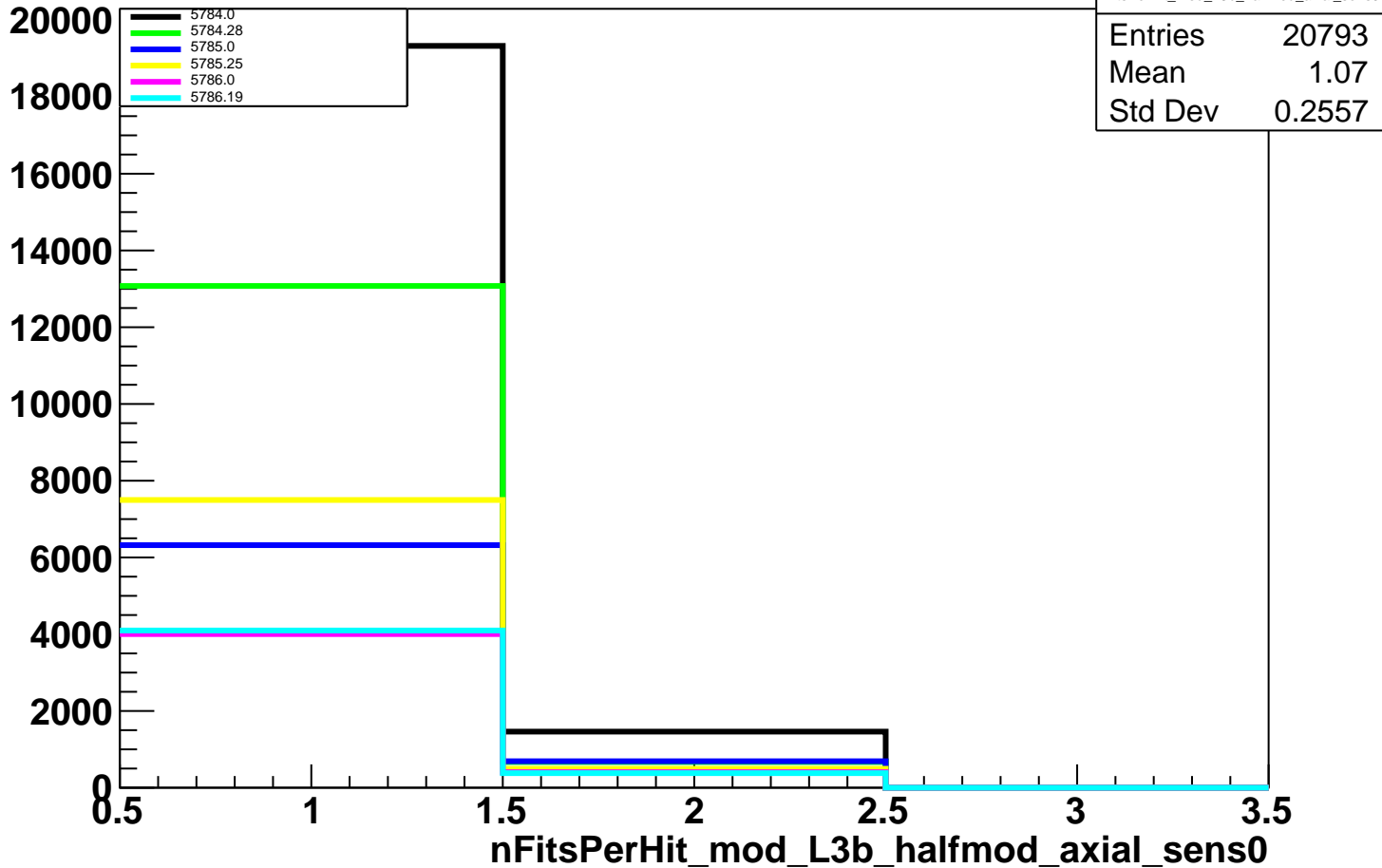
mod_L2t_halfmod_axial_sens0



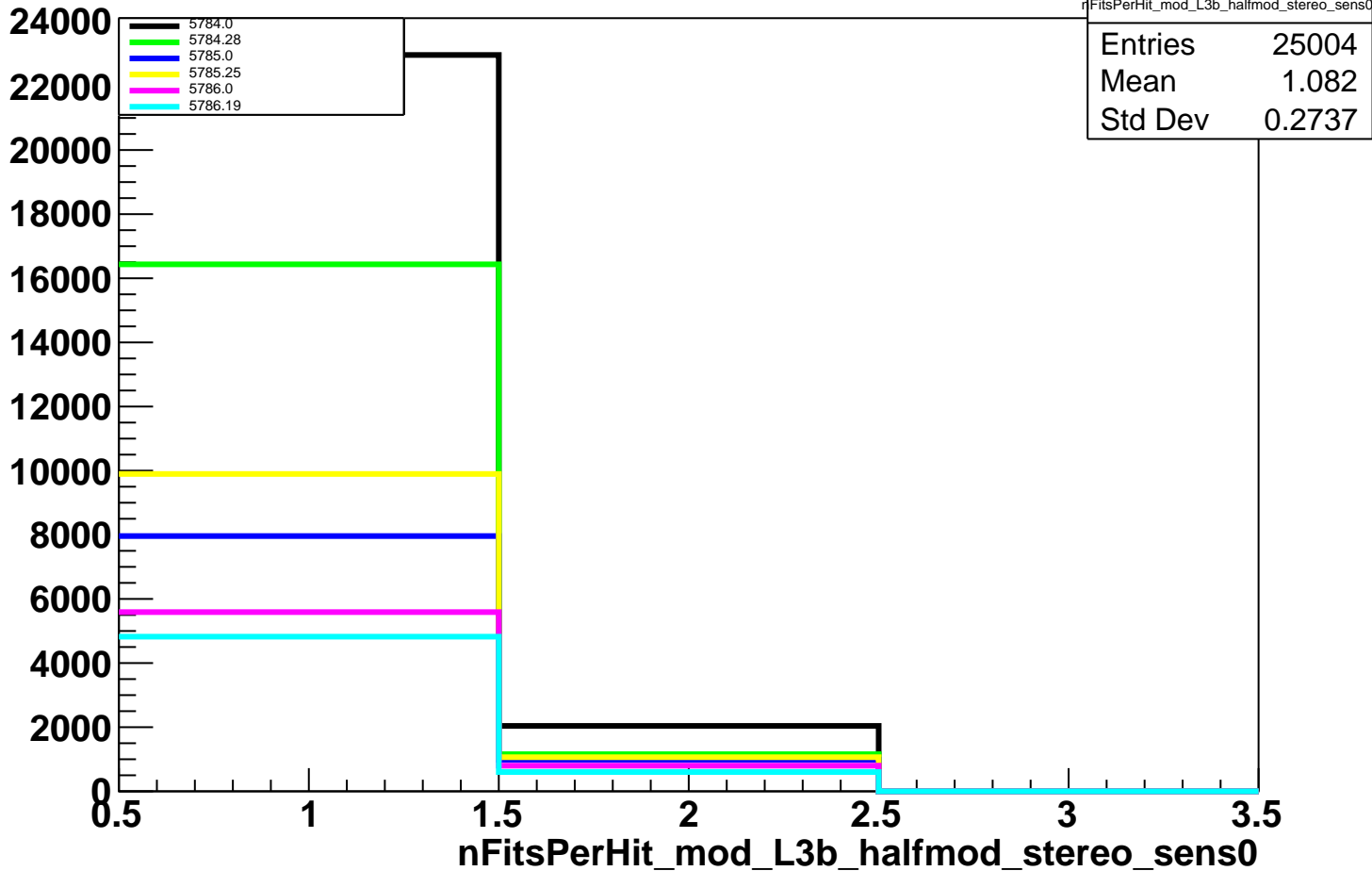
mod_L2t_halfmod_stereo_sens0



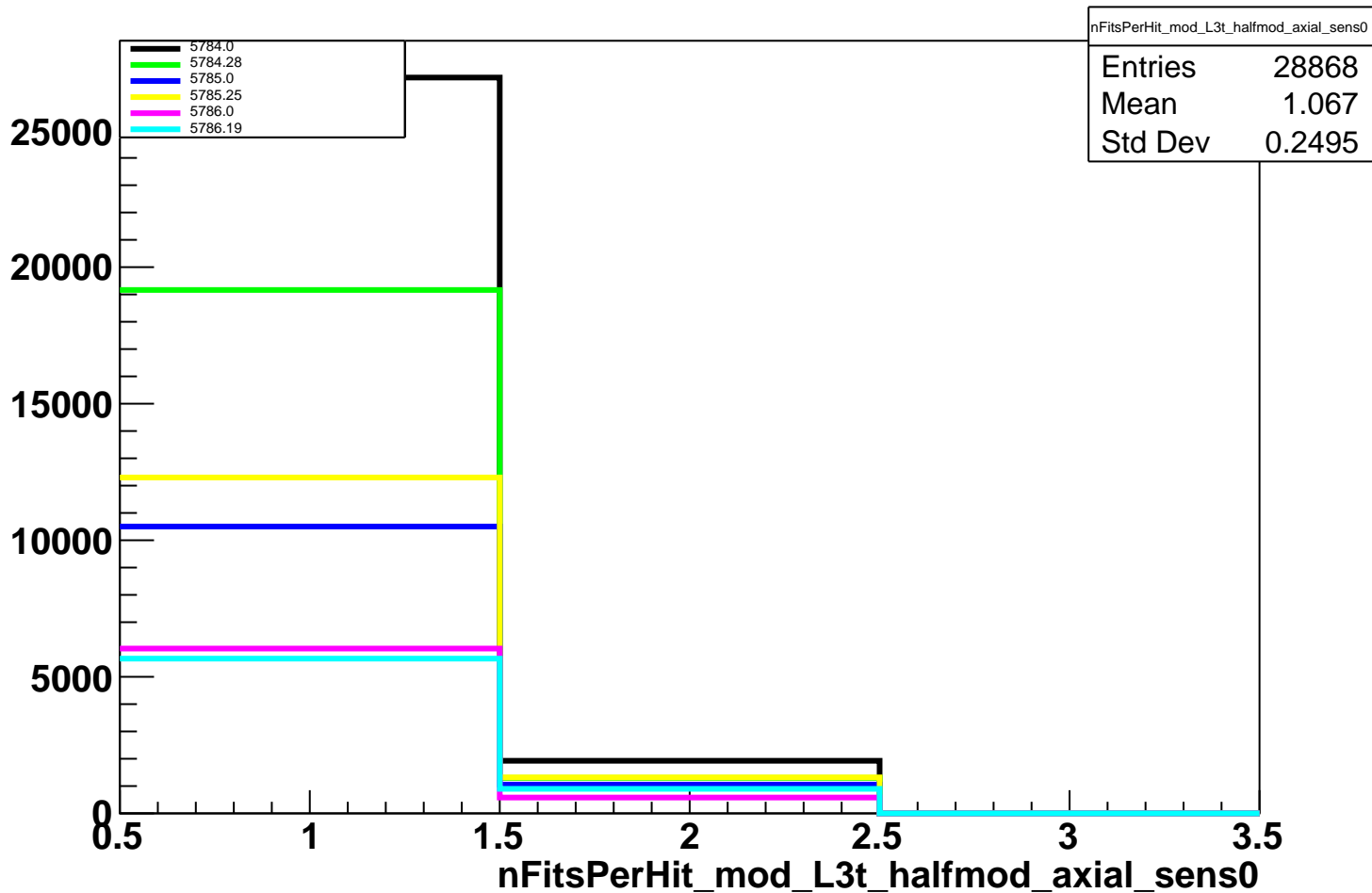
mod_L3b_halfmod_axial_sens0



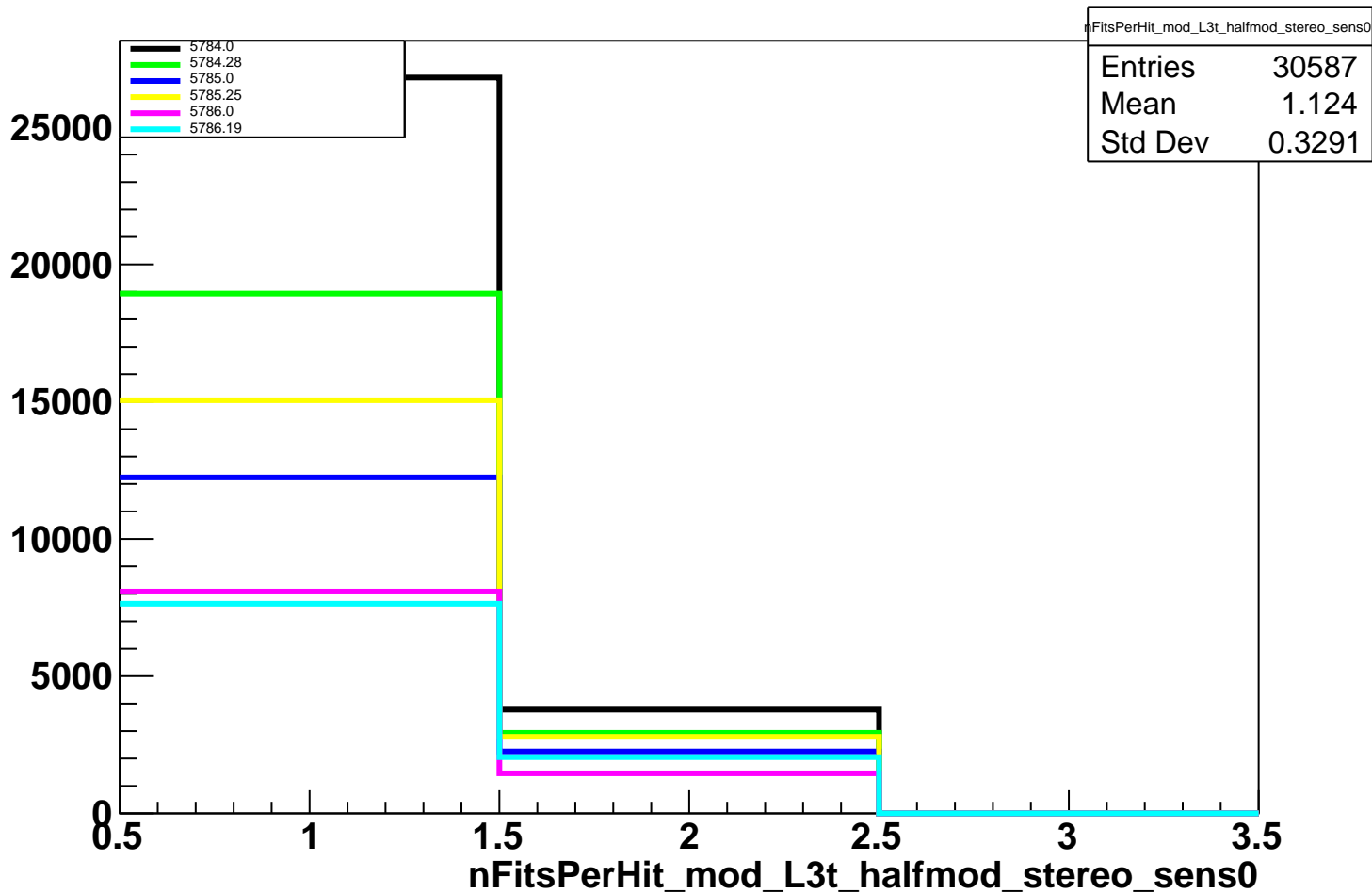
mod_L3b_halfmod_stereo_sens0



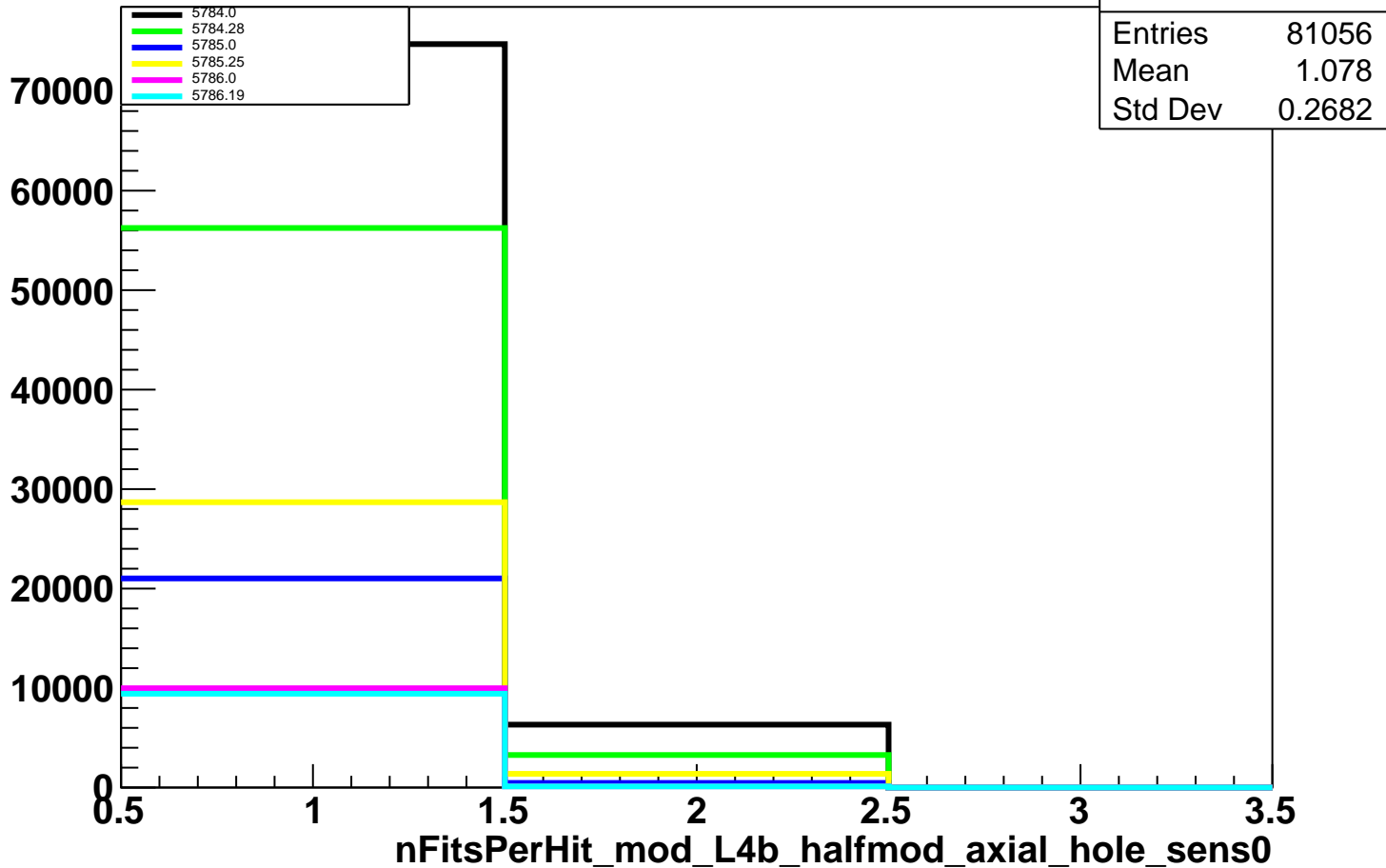
mod_L3t_halfmod_axial_sens0



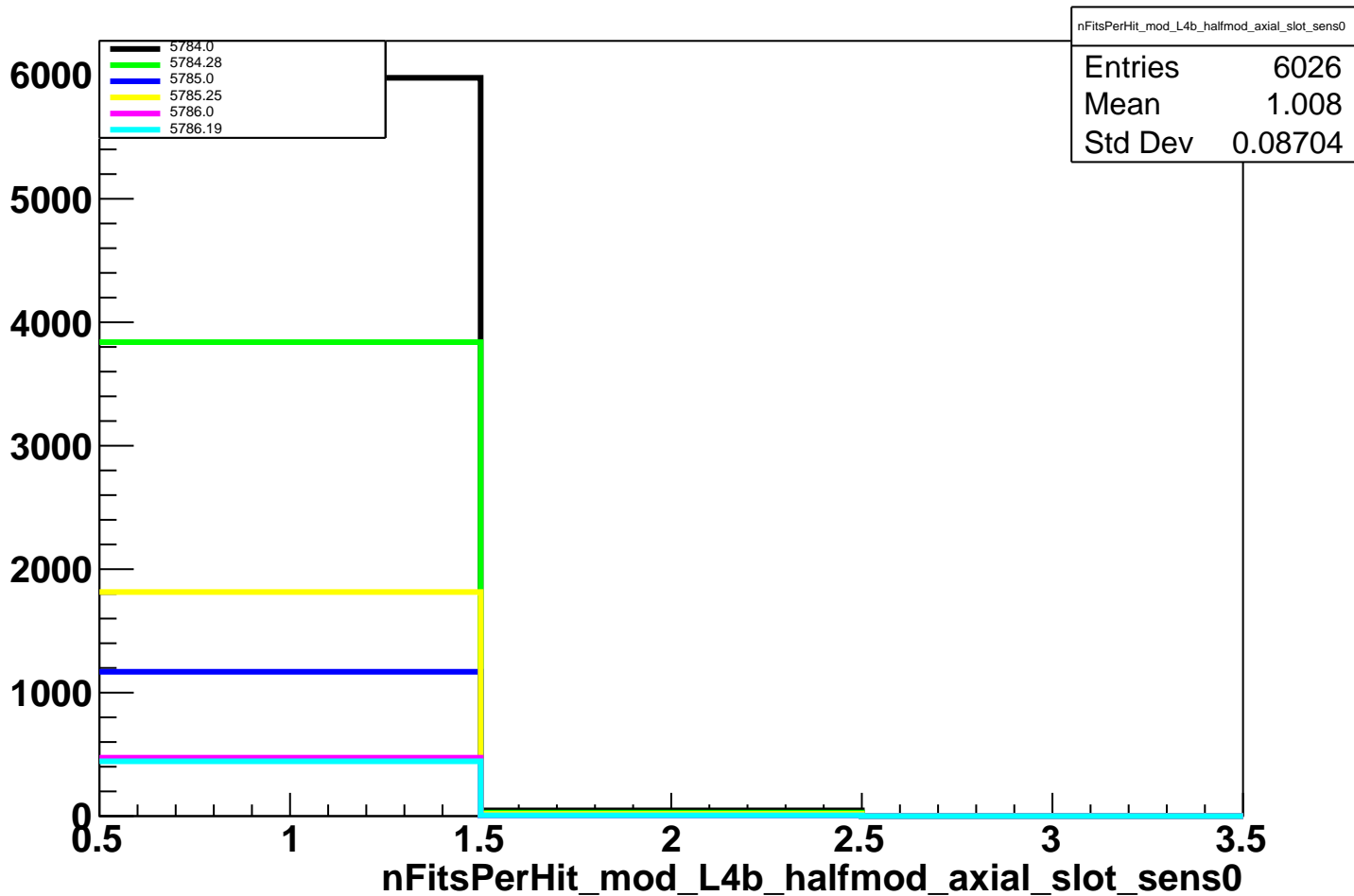
mod_L3t_halfmod_stereo_sens0



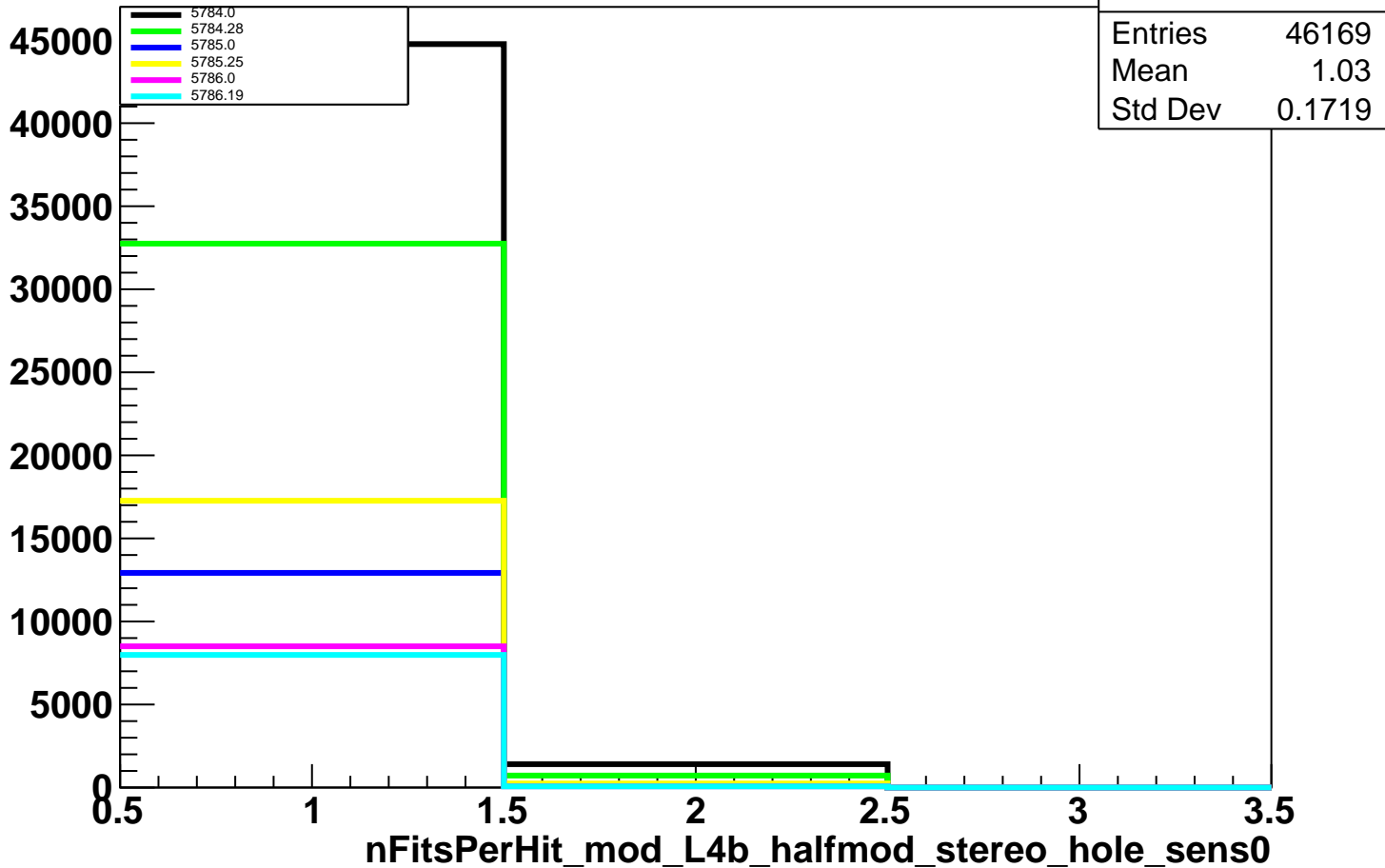
mod_L4b_halfmod_axial_hole_sens0



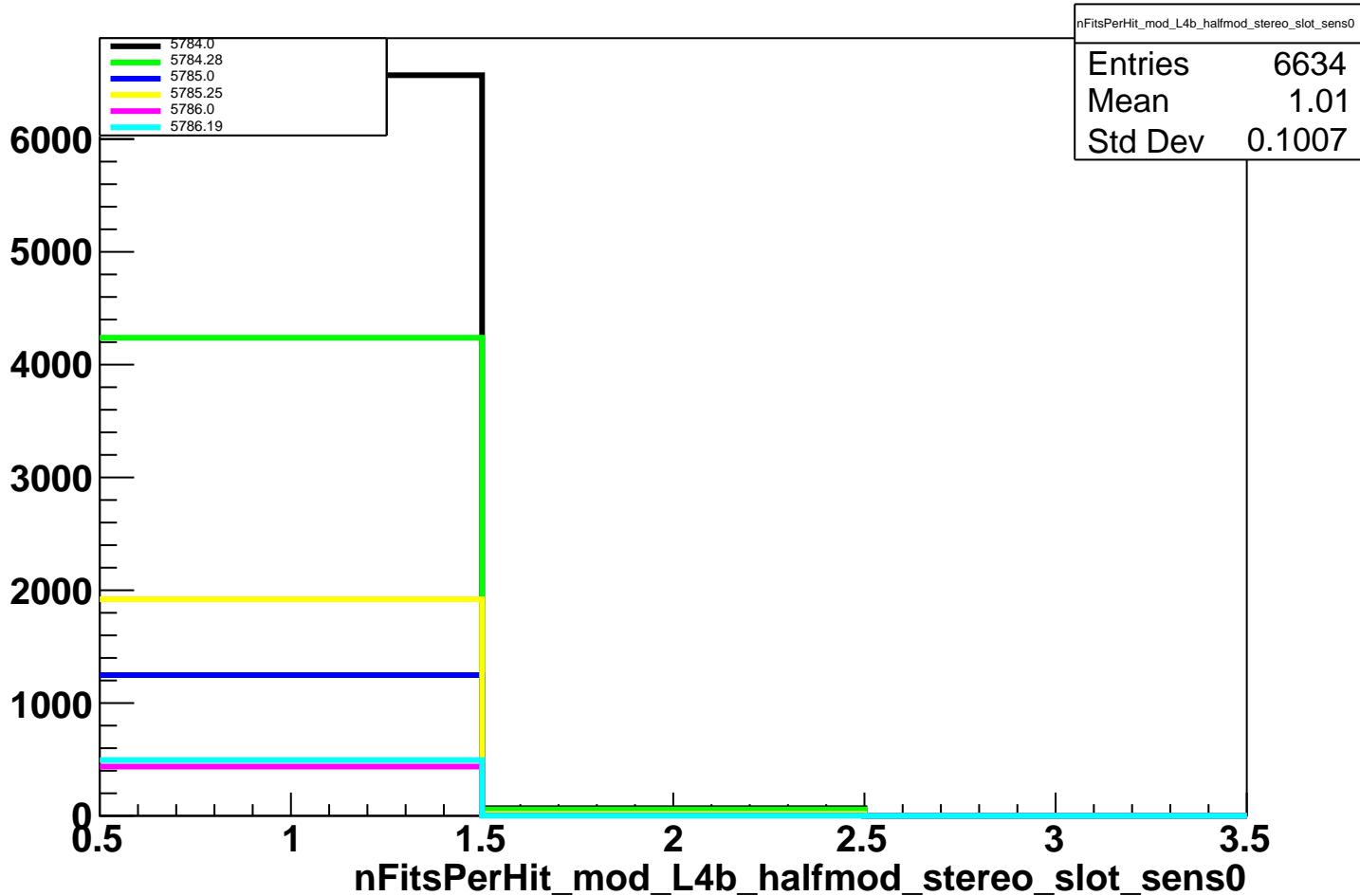
mod_L4b_halfmod_axial_slot_sens0



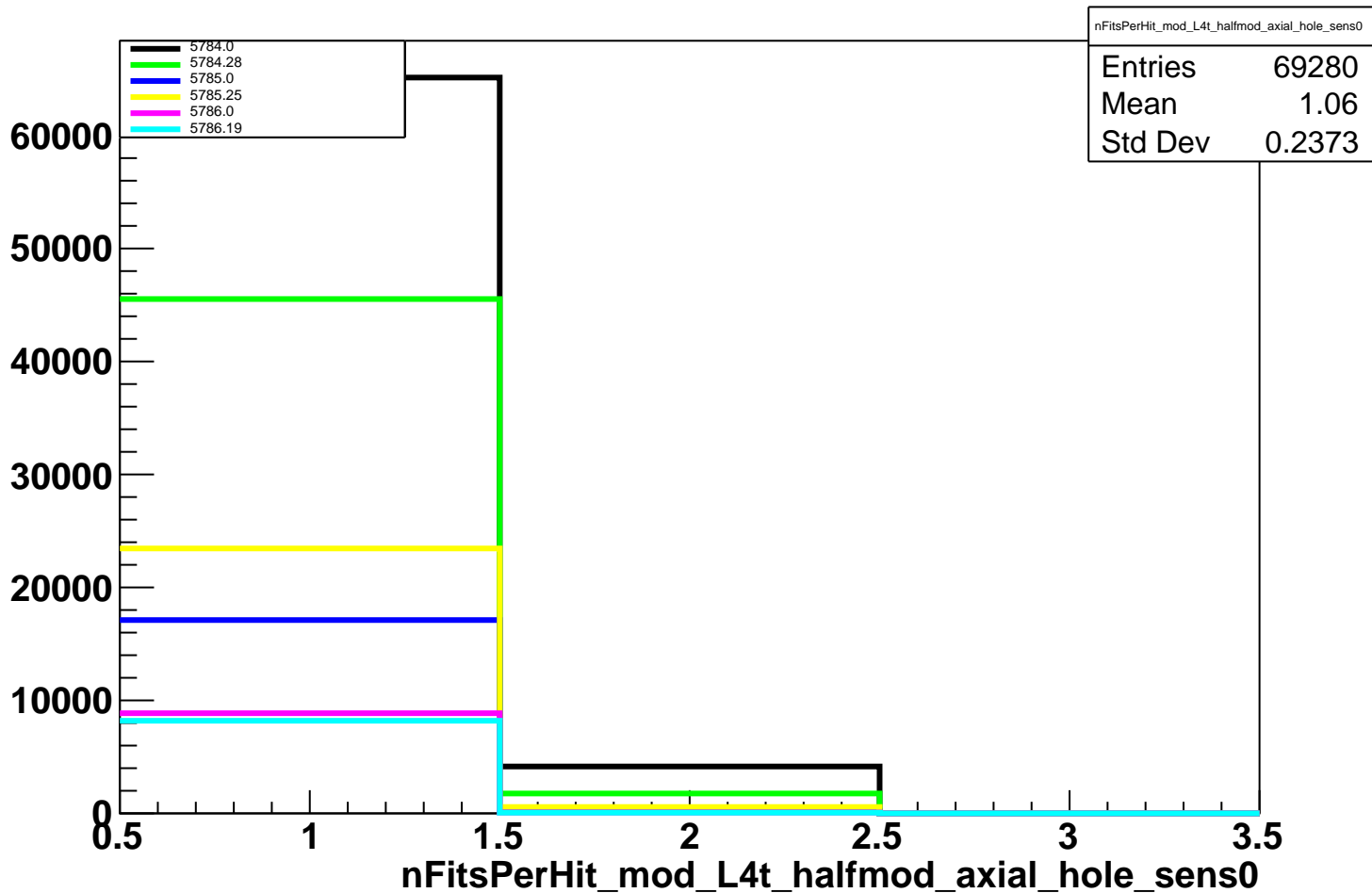
mod_L4b_halfmod_stereo_hole_sens0



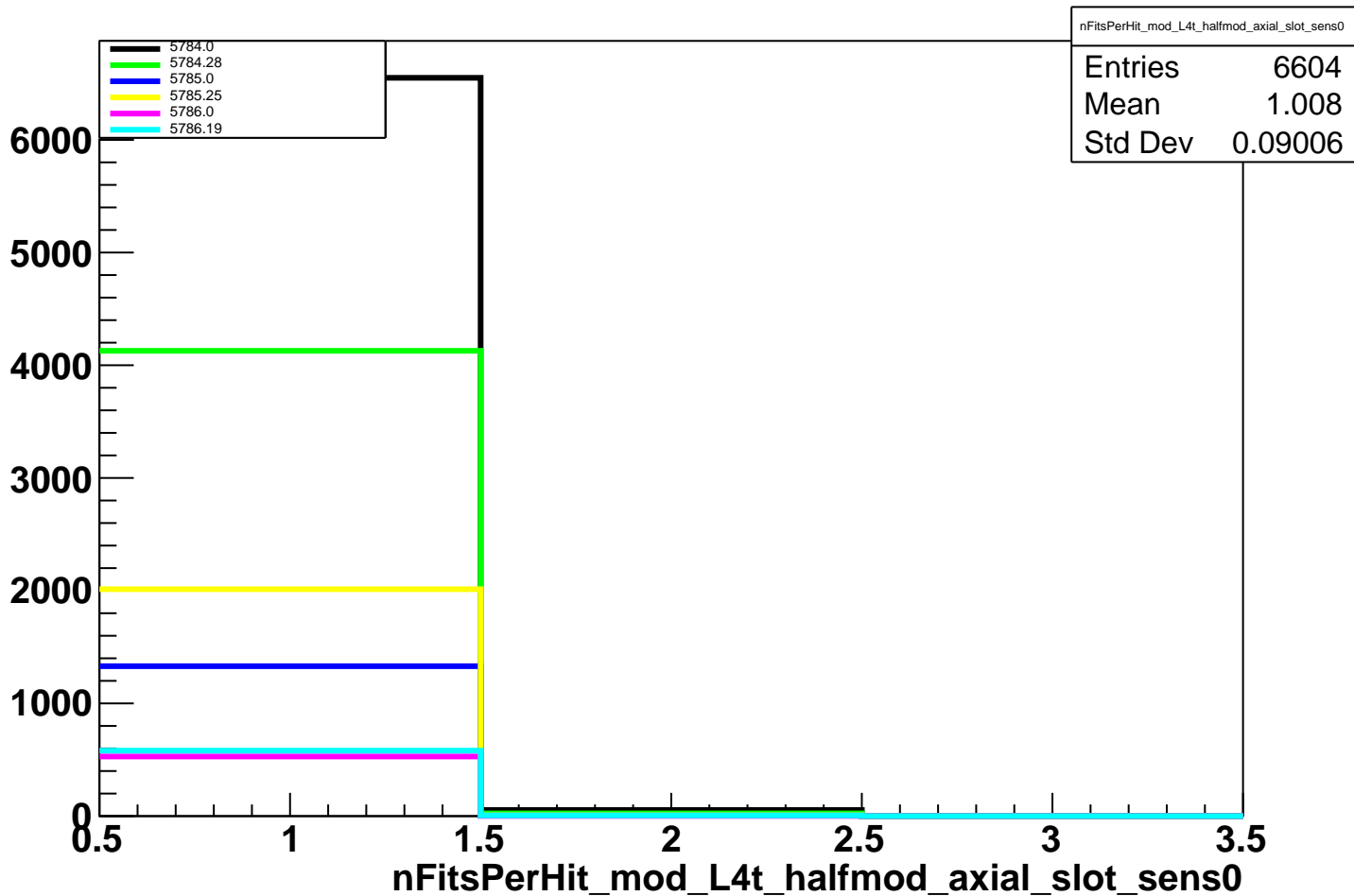
mod_L4b_halfmod_stereo_slot_sens0



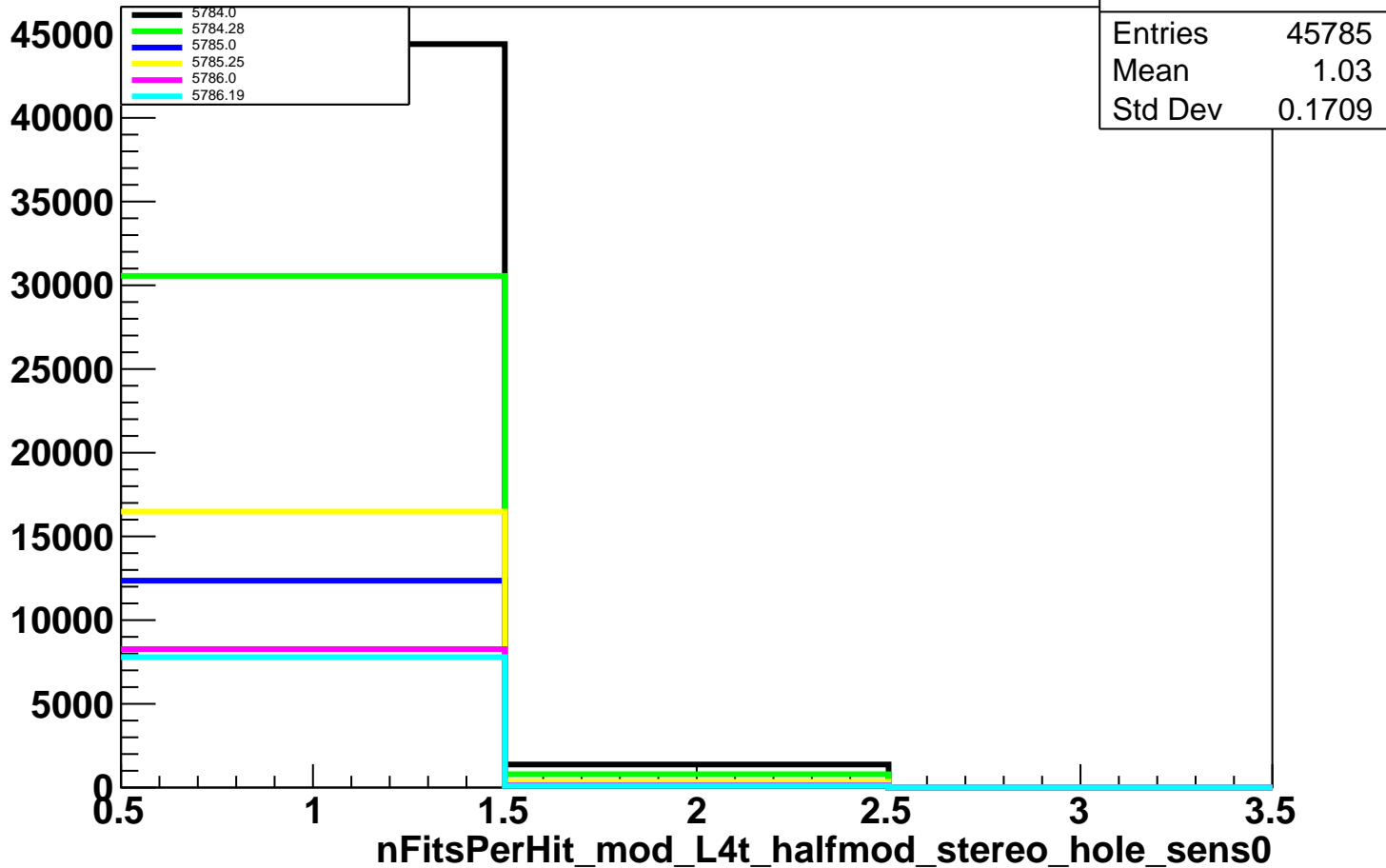
mod_L4t_halfmod_axial_hole_sens0



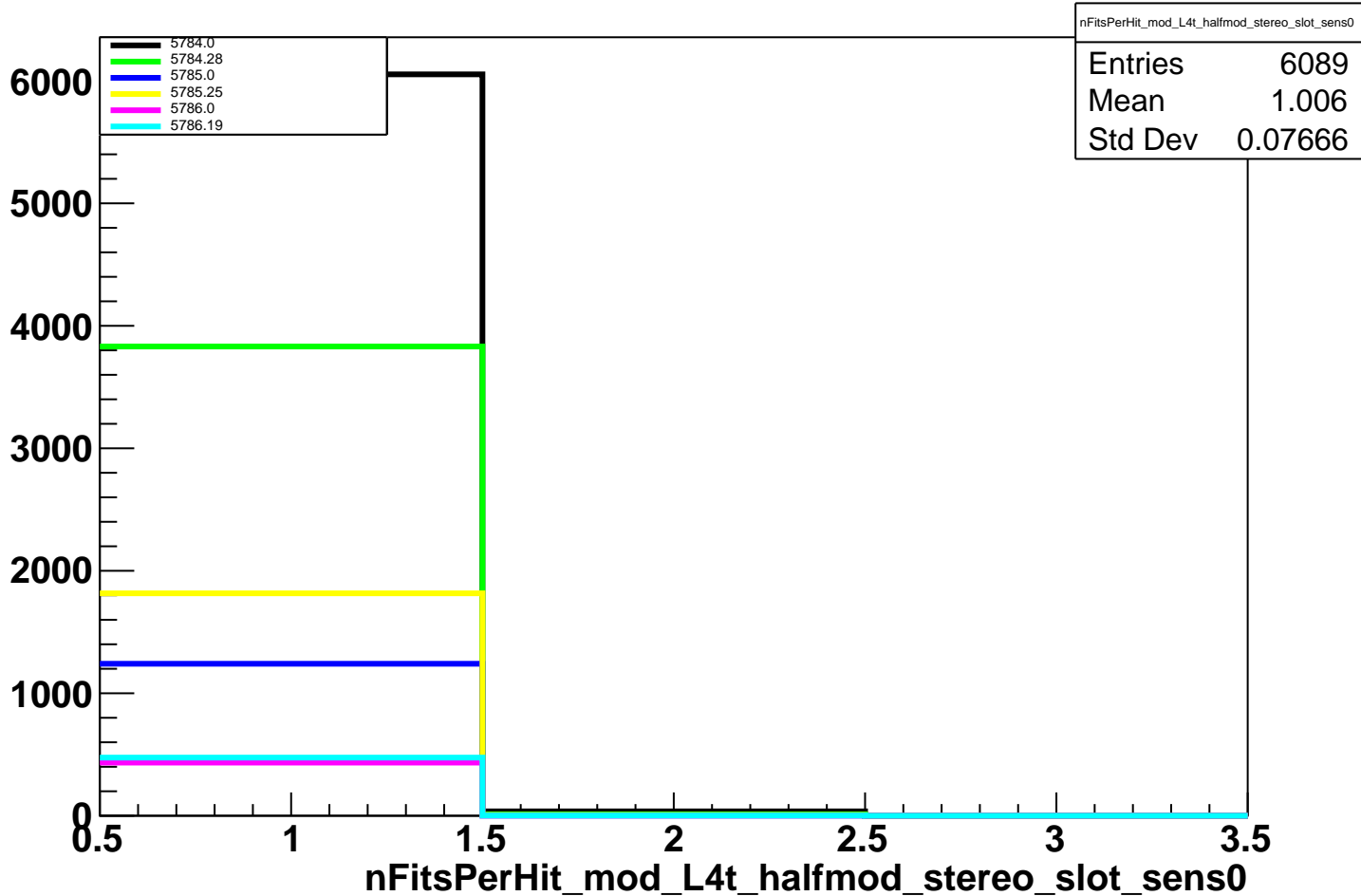
mod_L4t_halfmod_axial_slot_sens0



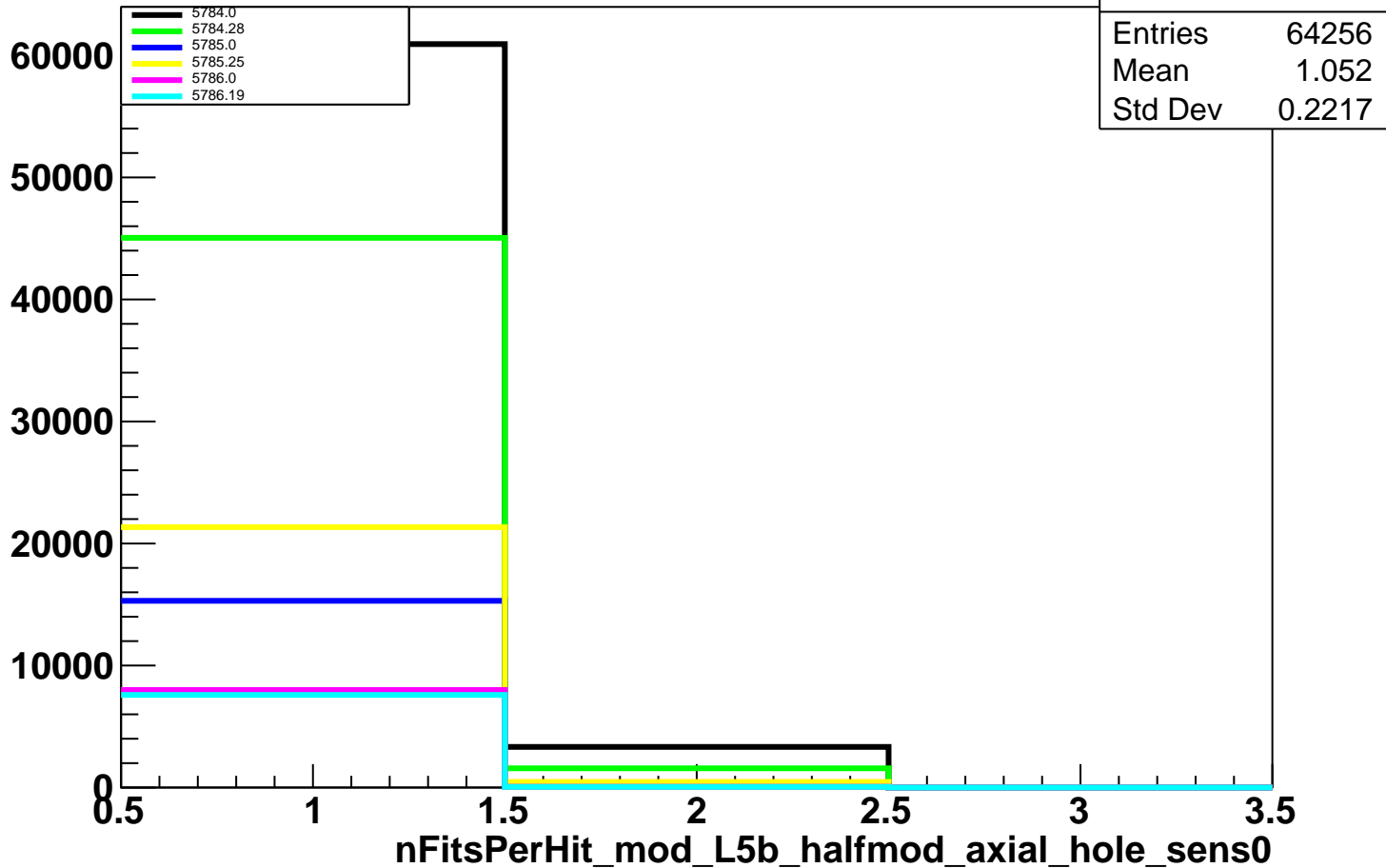
mod_L4t_halfmod_stereo_hole_sens0



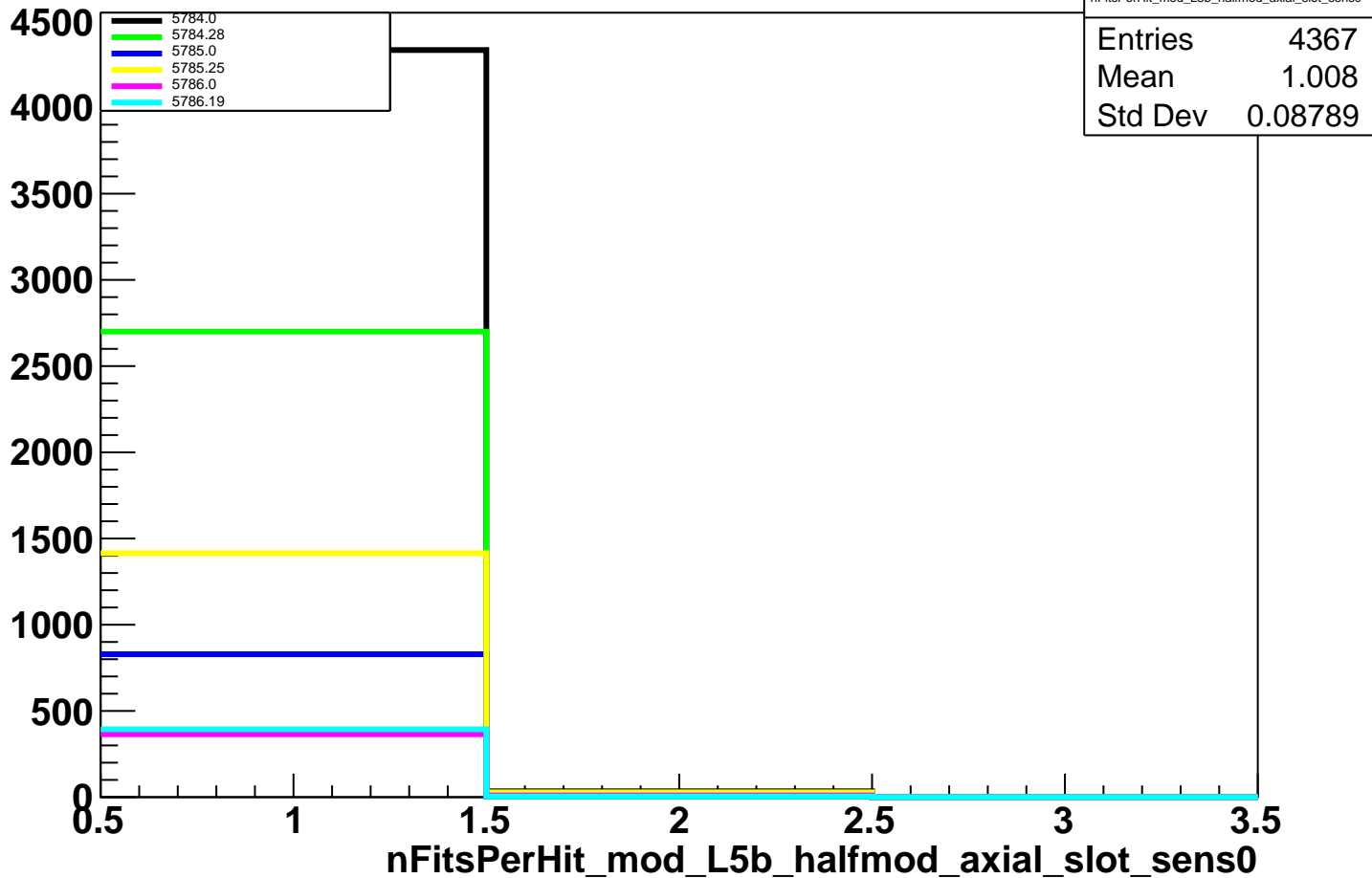
mod_L4t_halfmod_stereo_slot_sens0



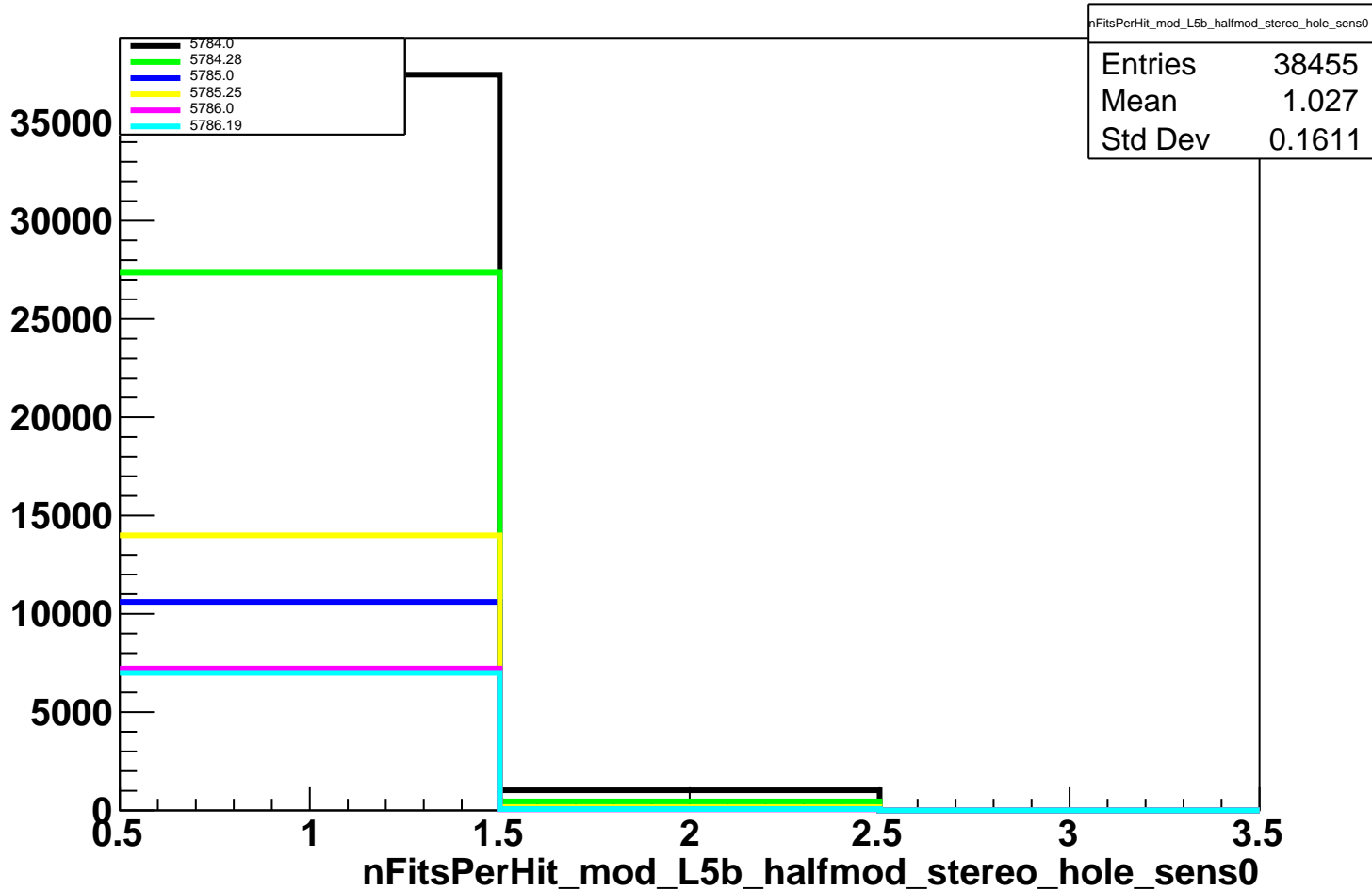
mod_L5b_halfmod_axial_hole_sens0



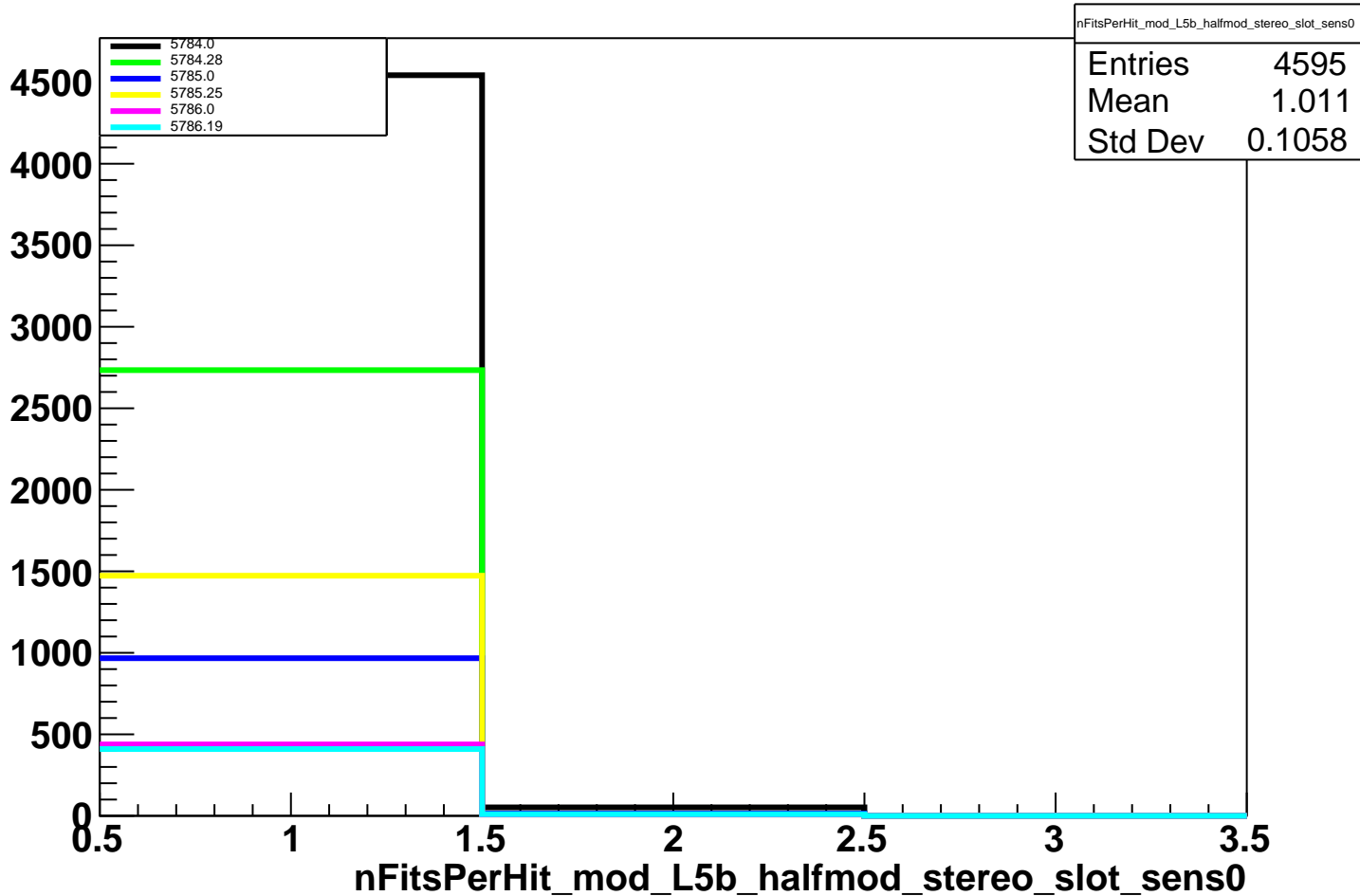
mod_L5b_halfmod_axial_slot_sens0



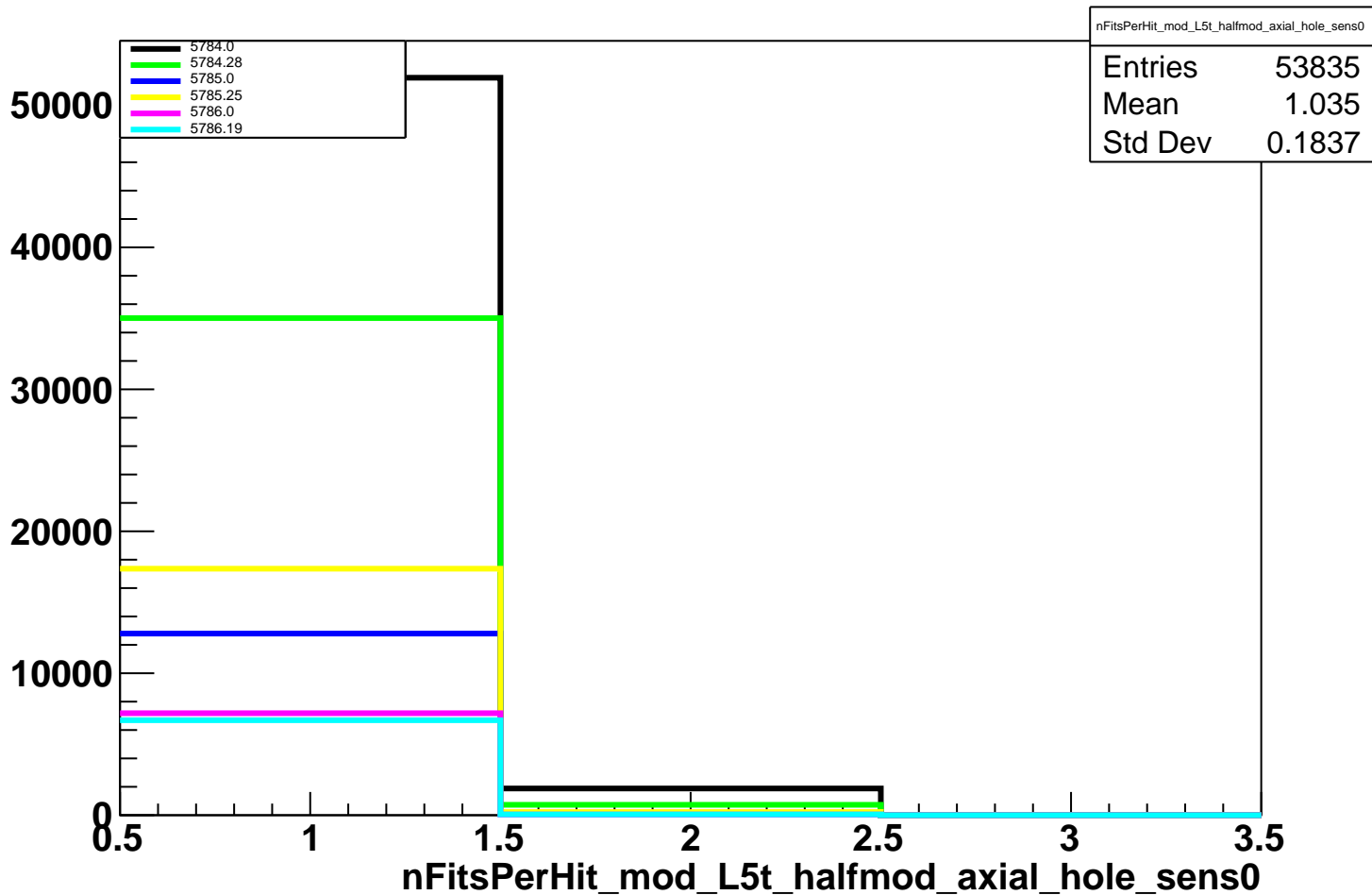
mod_L5b_halfmod_stereo_hole_sens0



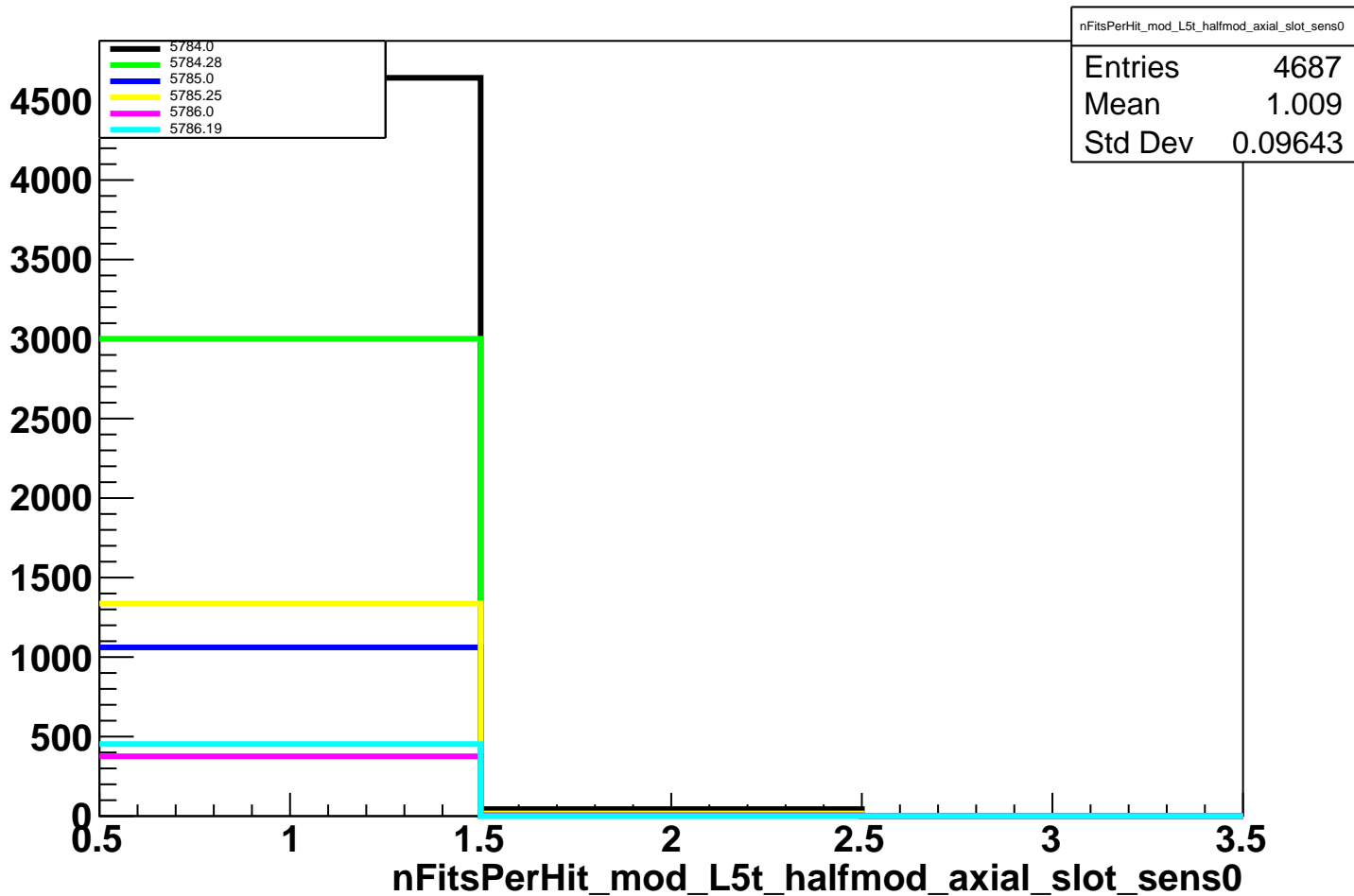
mod_L5b_halfmod_stereo_slot_sens0



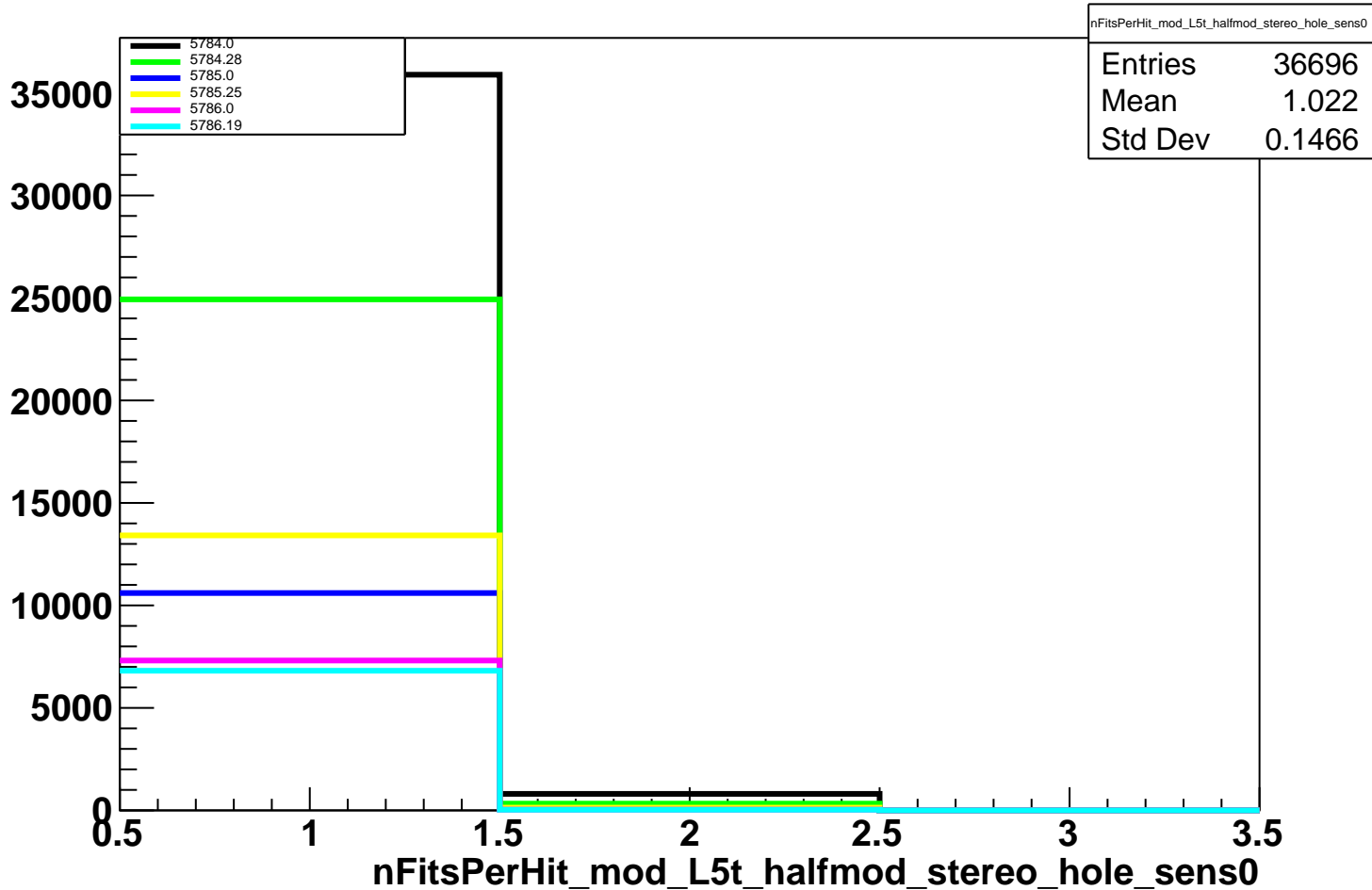
mod_L5t_halfmod_axial_hole_sens0



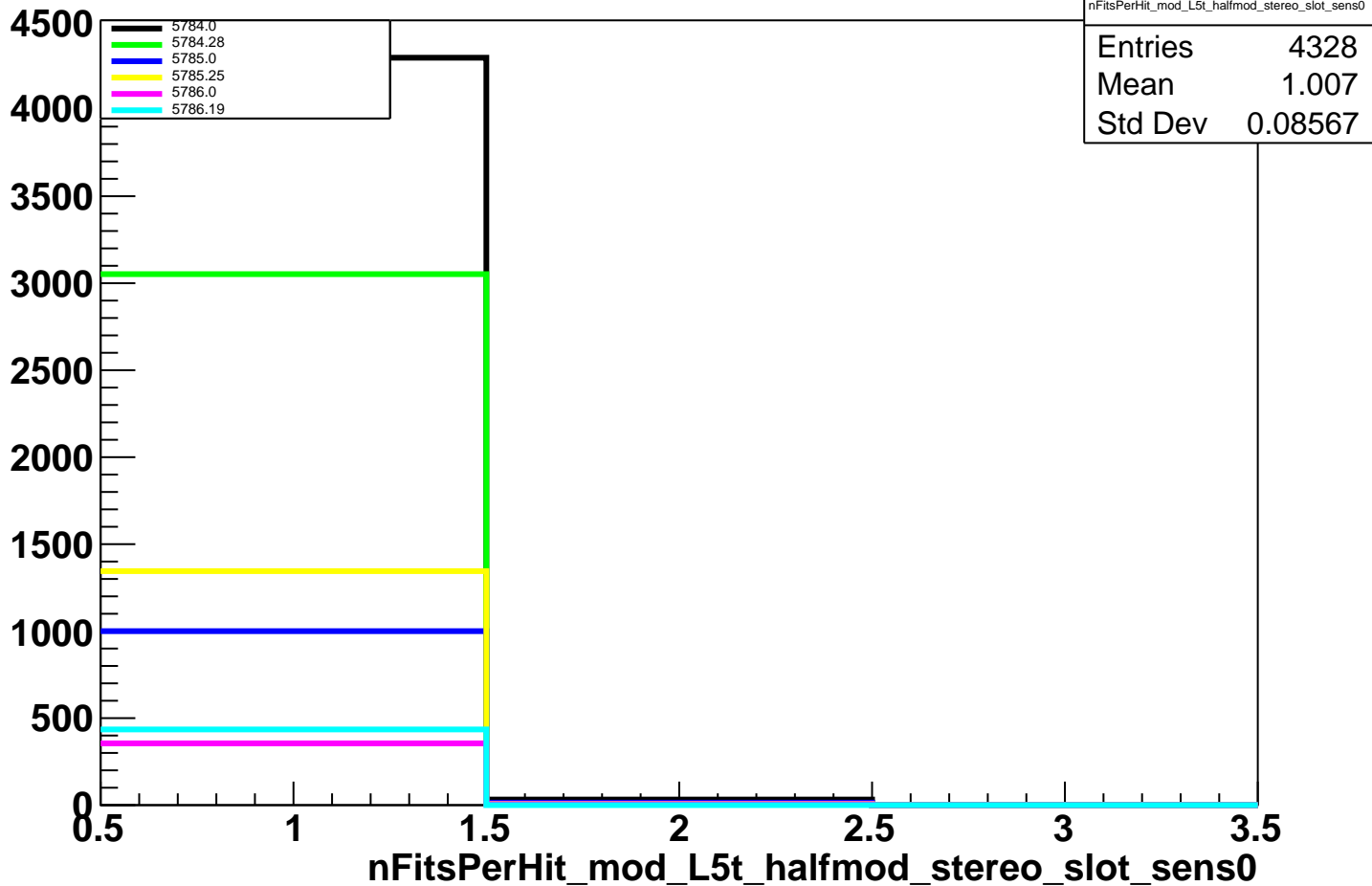
mod_L5t_halfmod_axial_slot_sens0



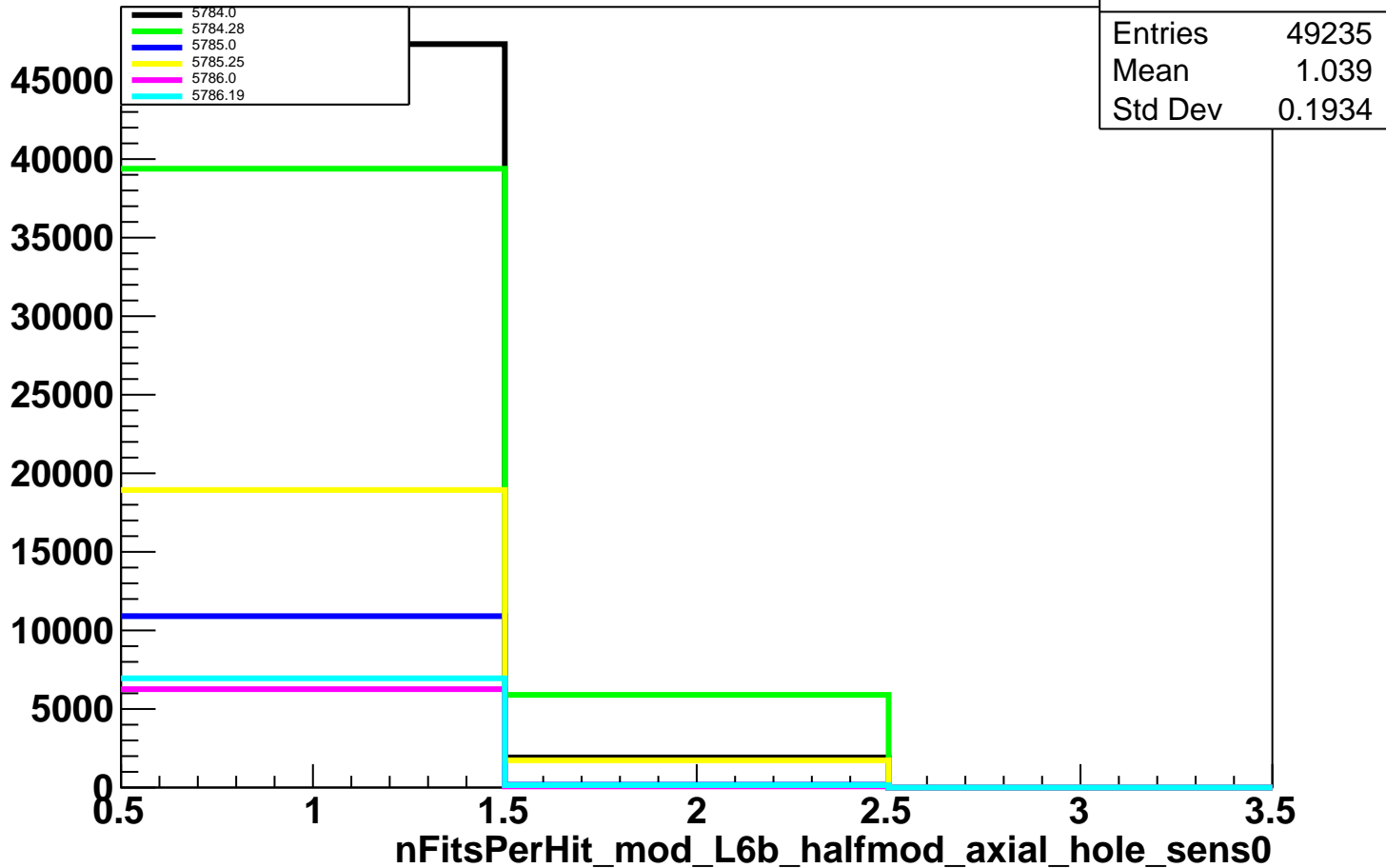
mod_L5t_halfmod_stereo_hole_sens0



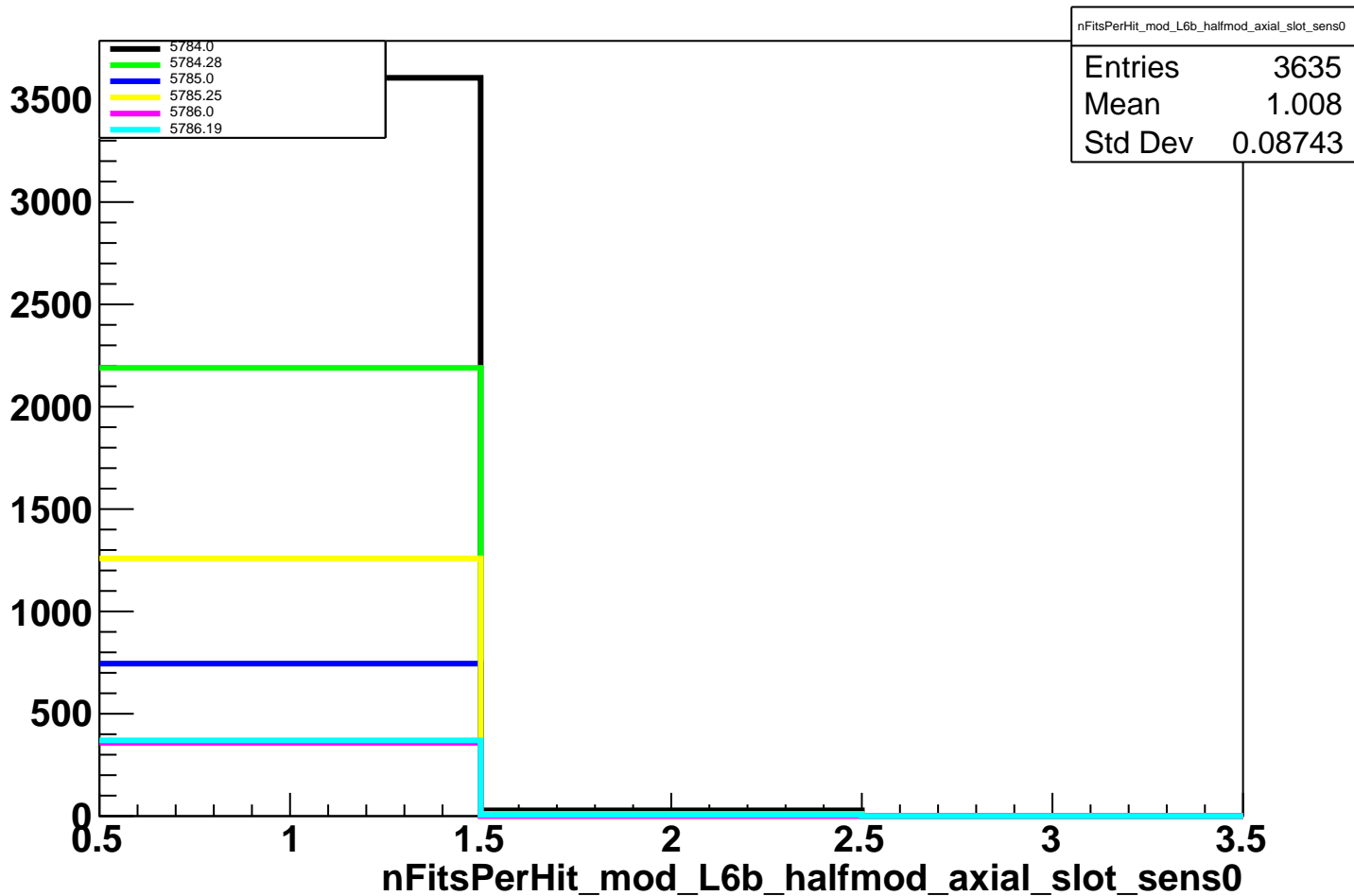
mod_L5t_halfmod_stereo_slot_sens0



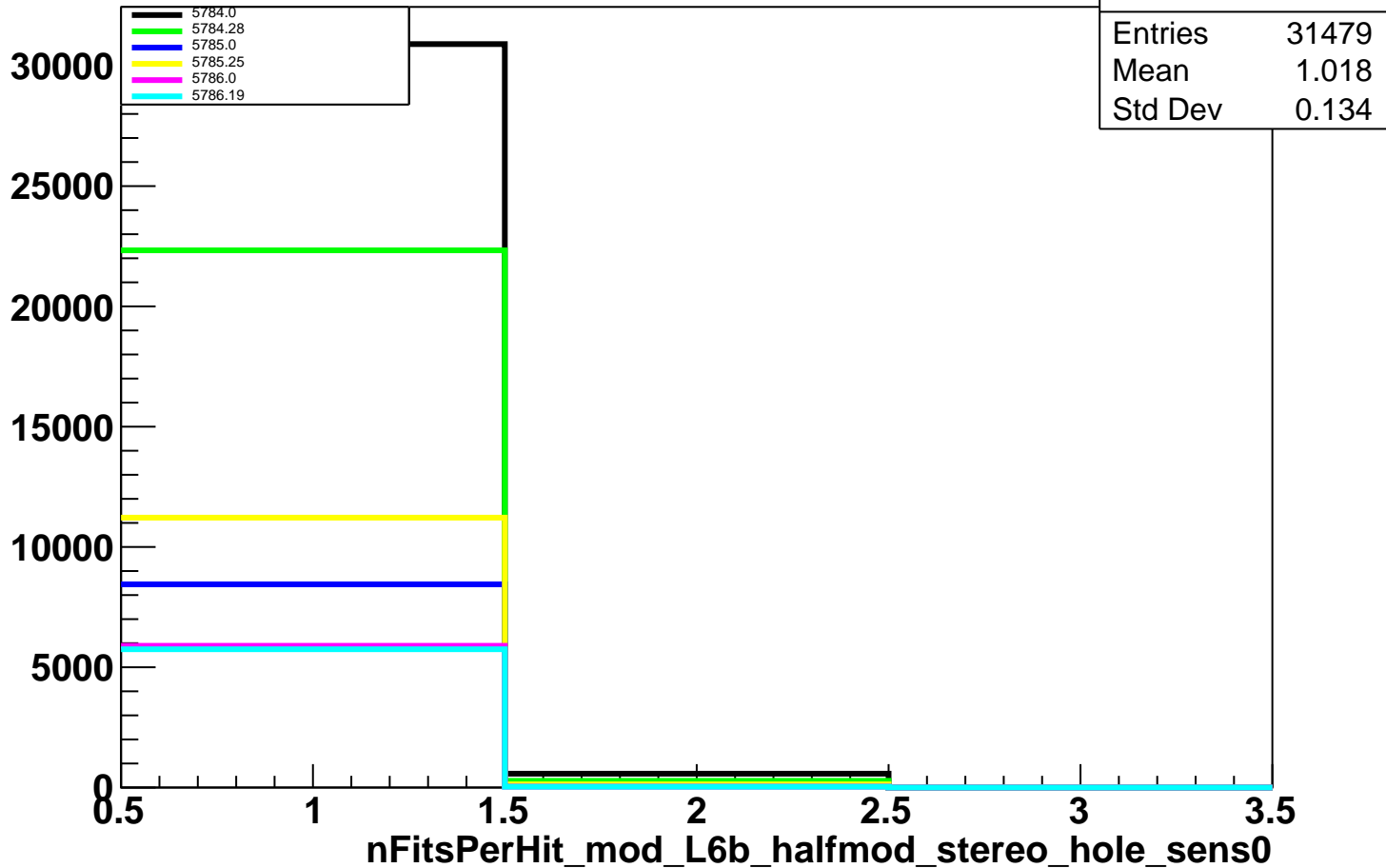
mod_L6b_halfmod_axial_hole_sens0



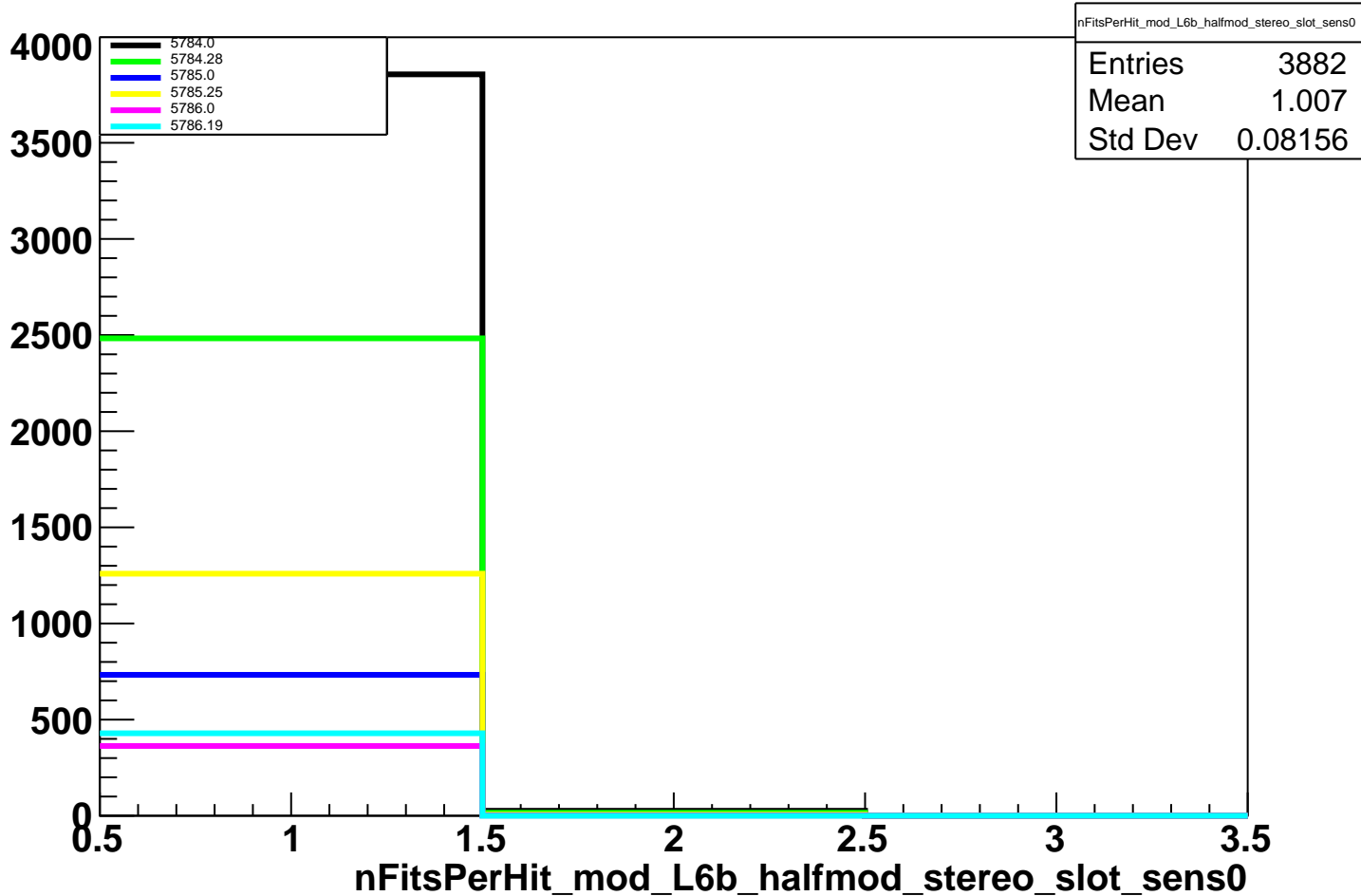
mod_L6b_halfmod_axial_slot_sens0



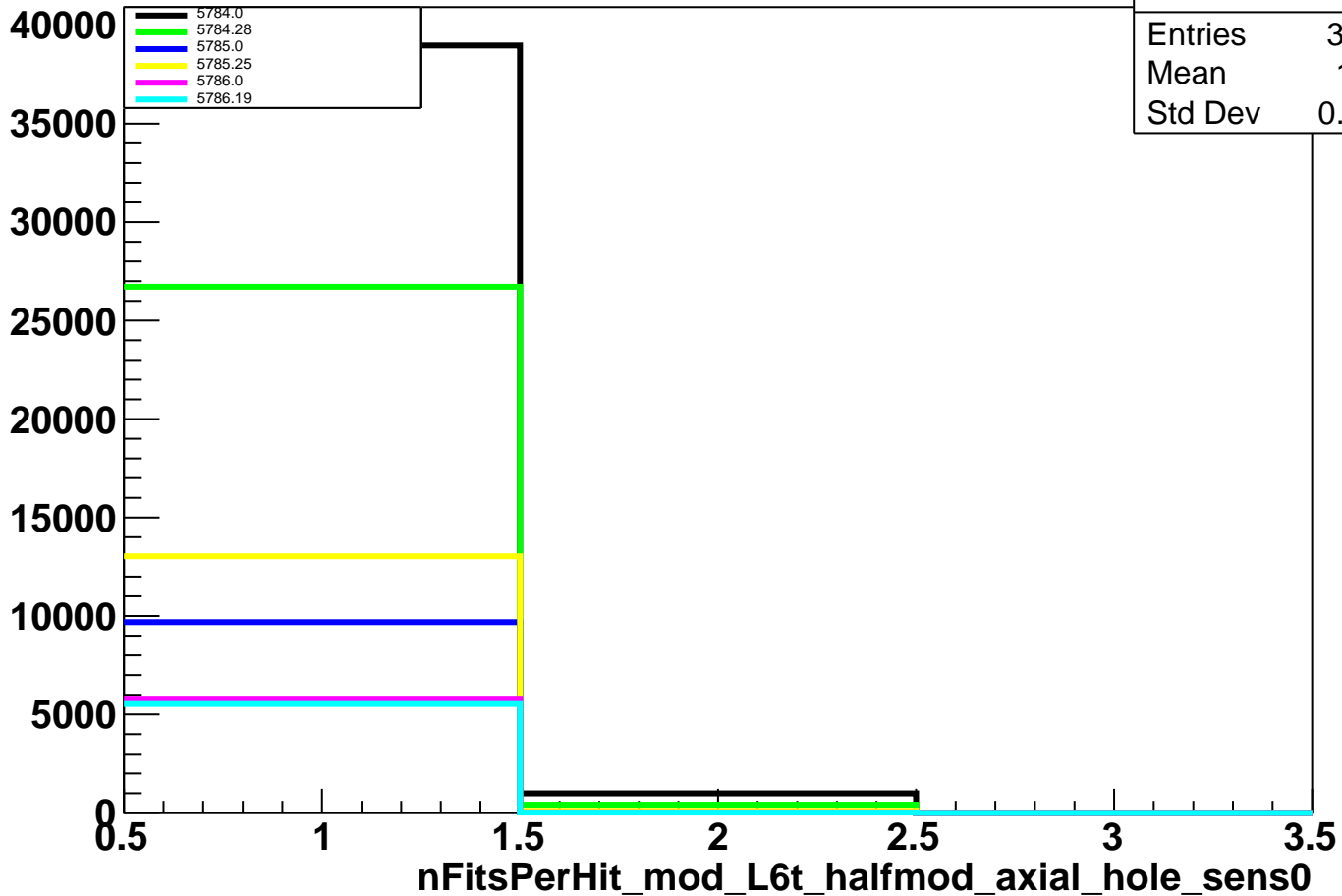
mod_L6b_halfmod_stereo_hole_sens0



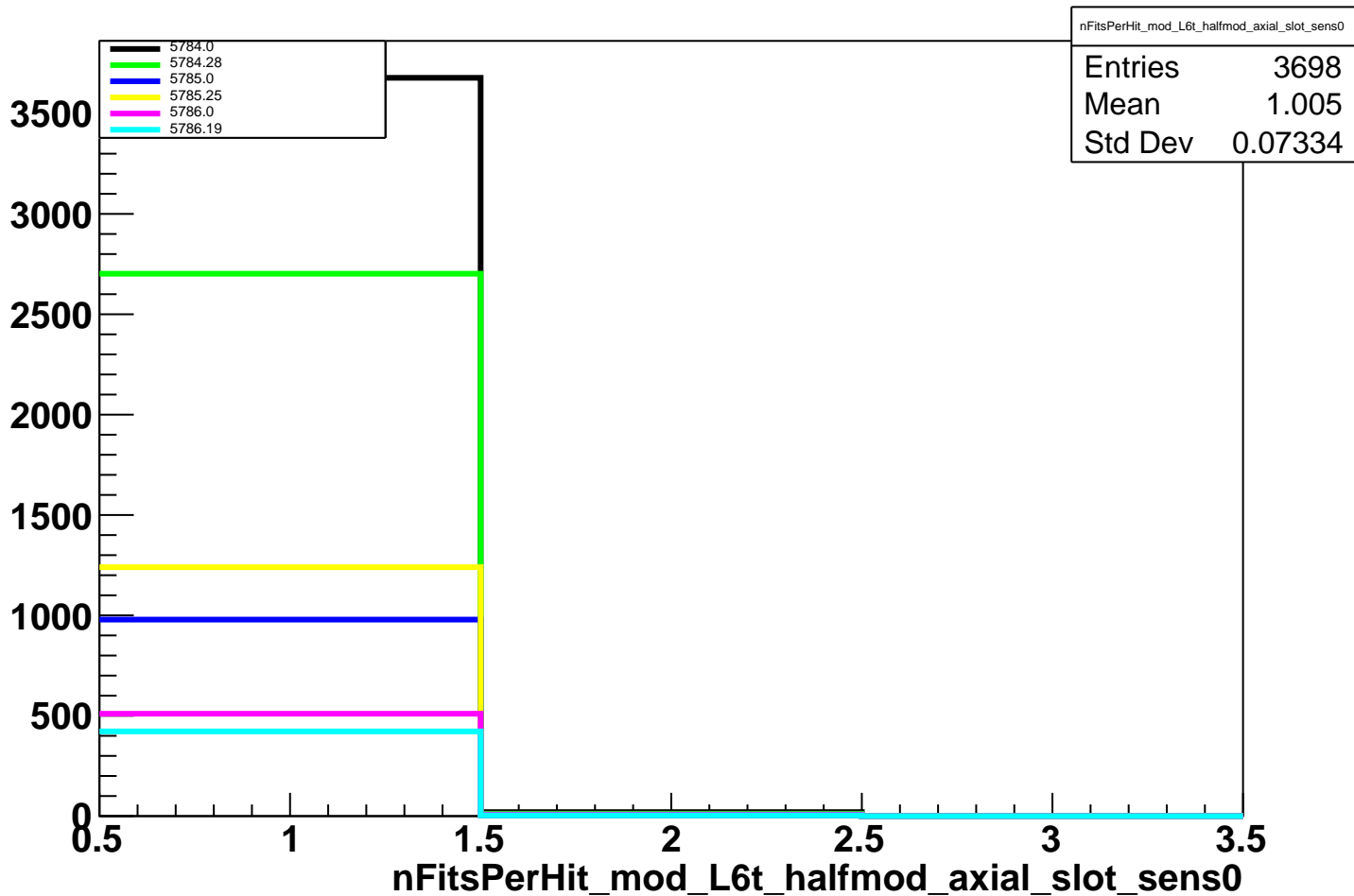
mod_L6b_halfmod_stereo_slot_sens0



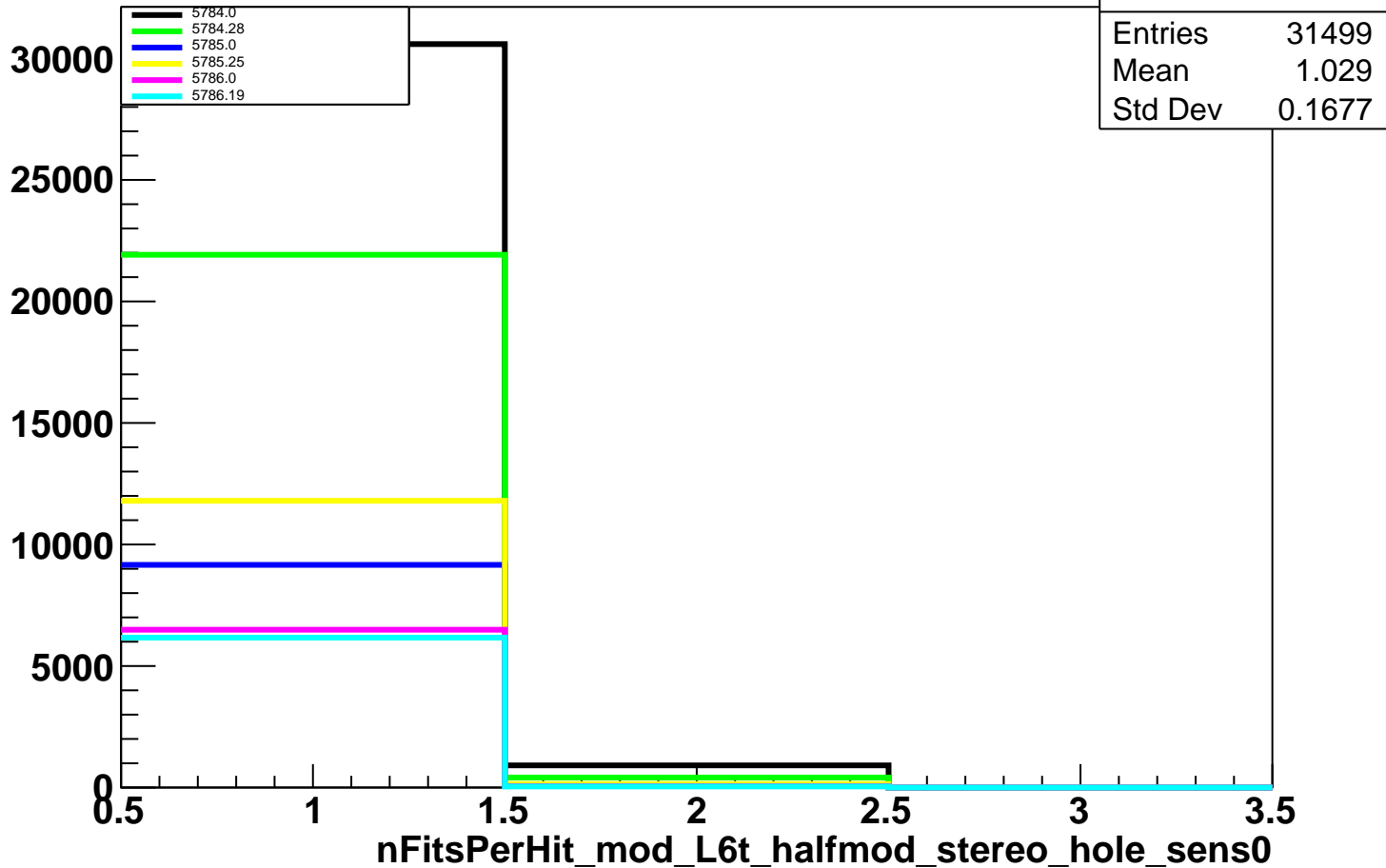
mod_L6t_halfmod_axial_hole_sens0



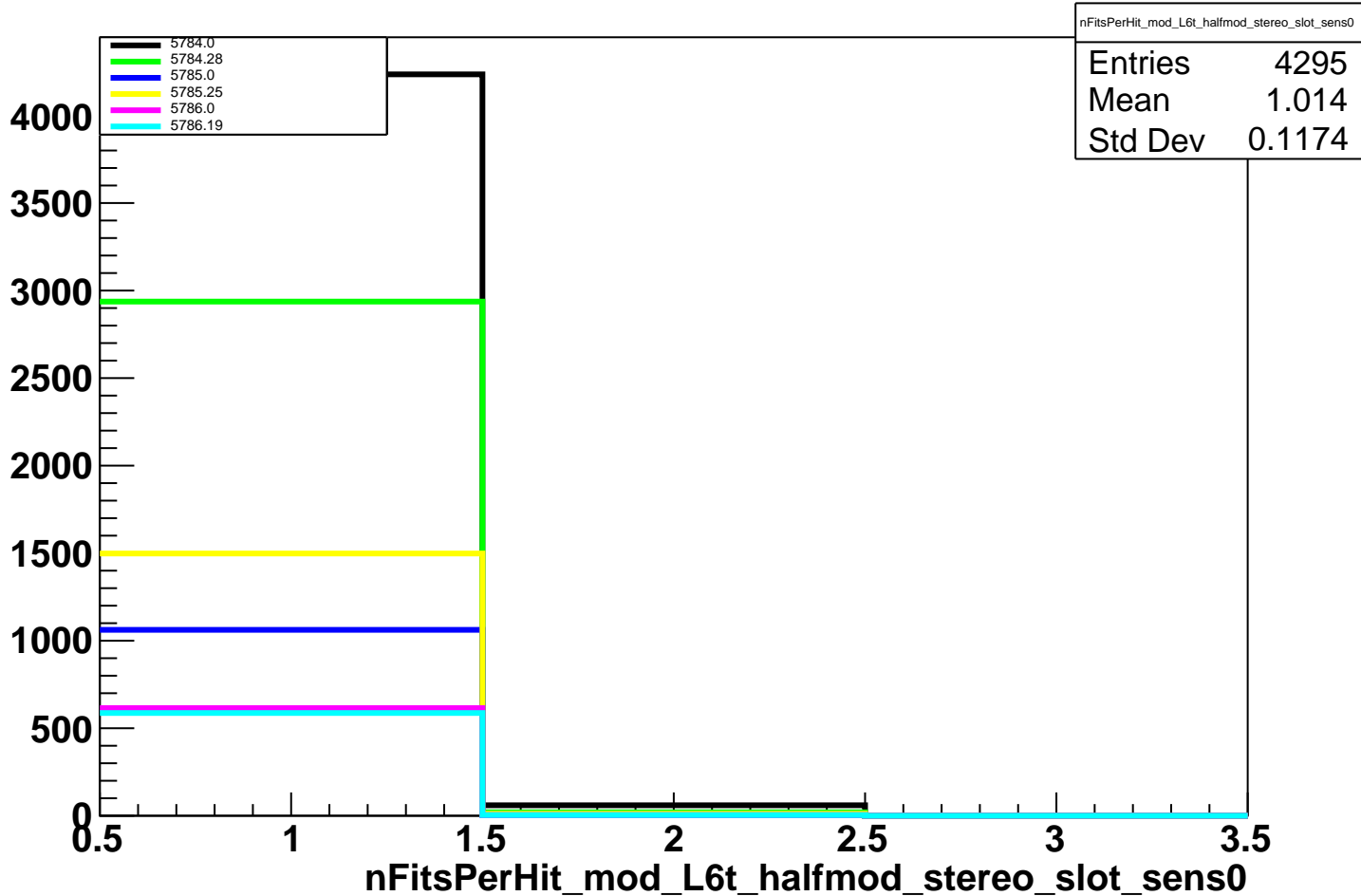
mod_L6t_halfmod_axial_slot_sens0



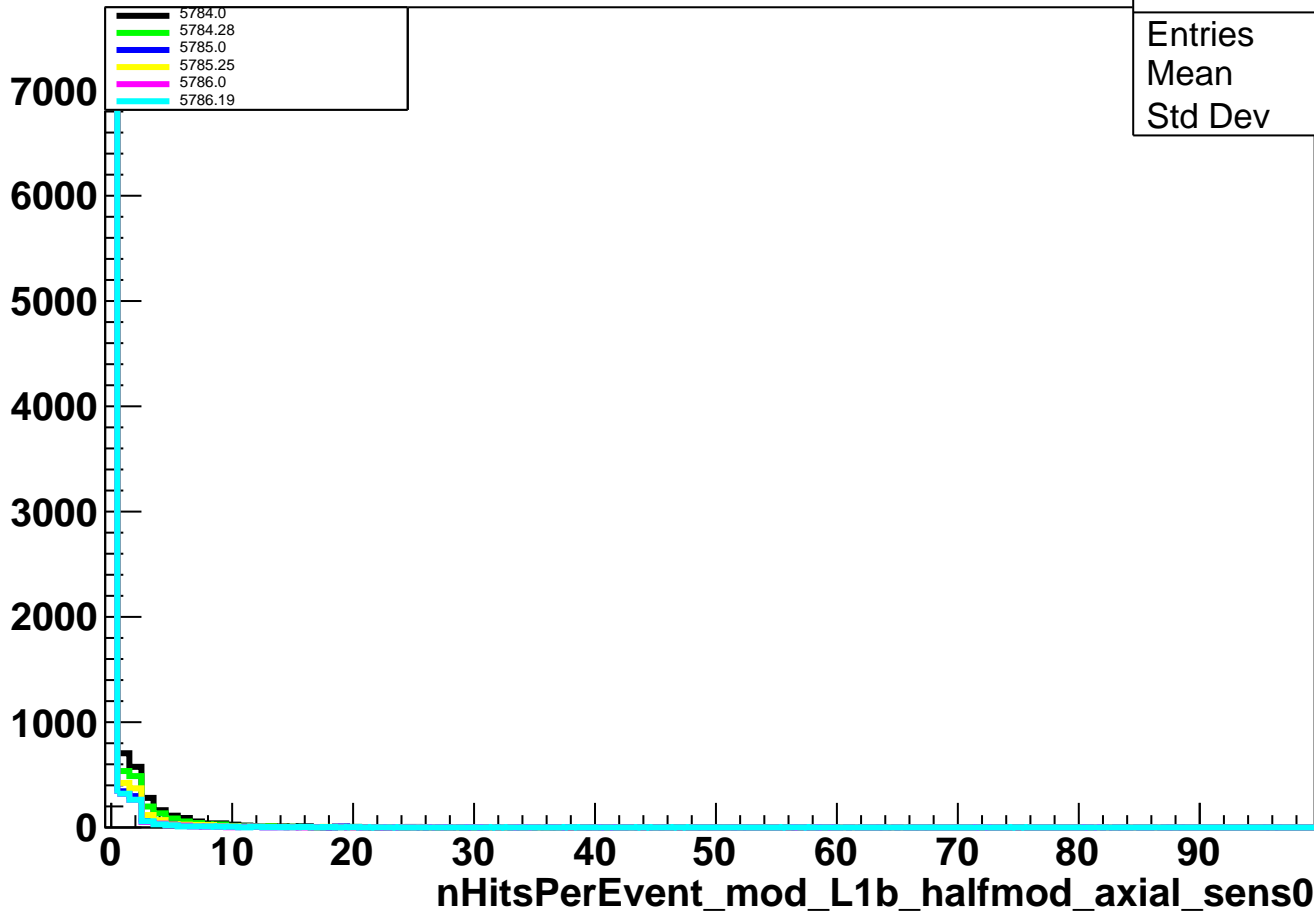
mod_L6t_halfmod_stereo_hole_sens0



mod_L6t_halfmod_stereo_slot_sens0

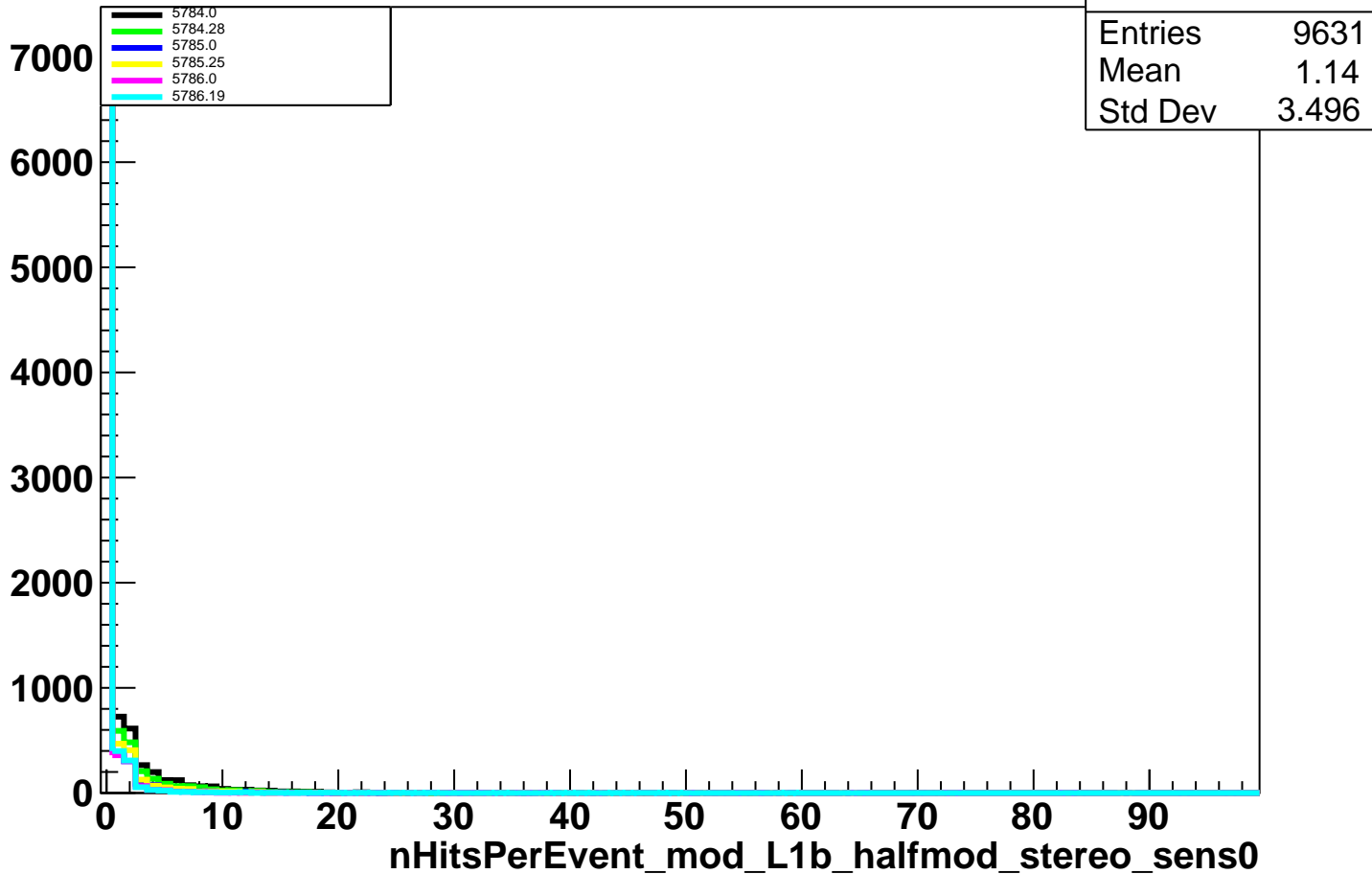


mod_L1b_halfmod_axial_sens0

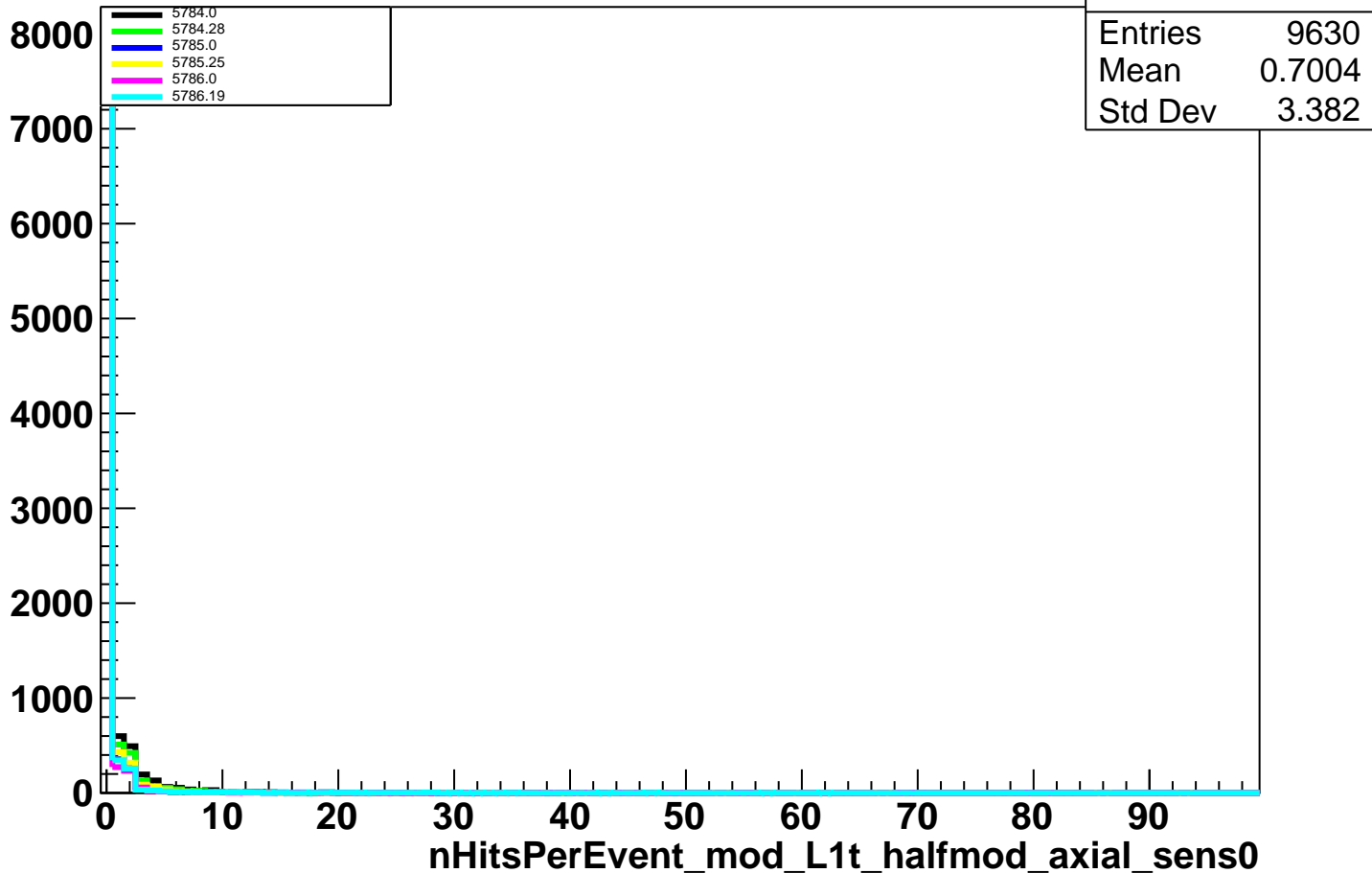


nHitsPerEvent_mod_L1b_halfmod_axial_sens0	
Entries	9631
Mean	0.8694
Std Dev	3.167

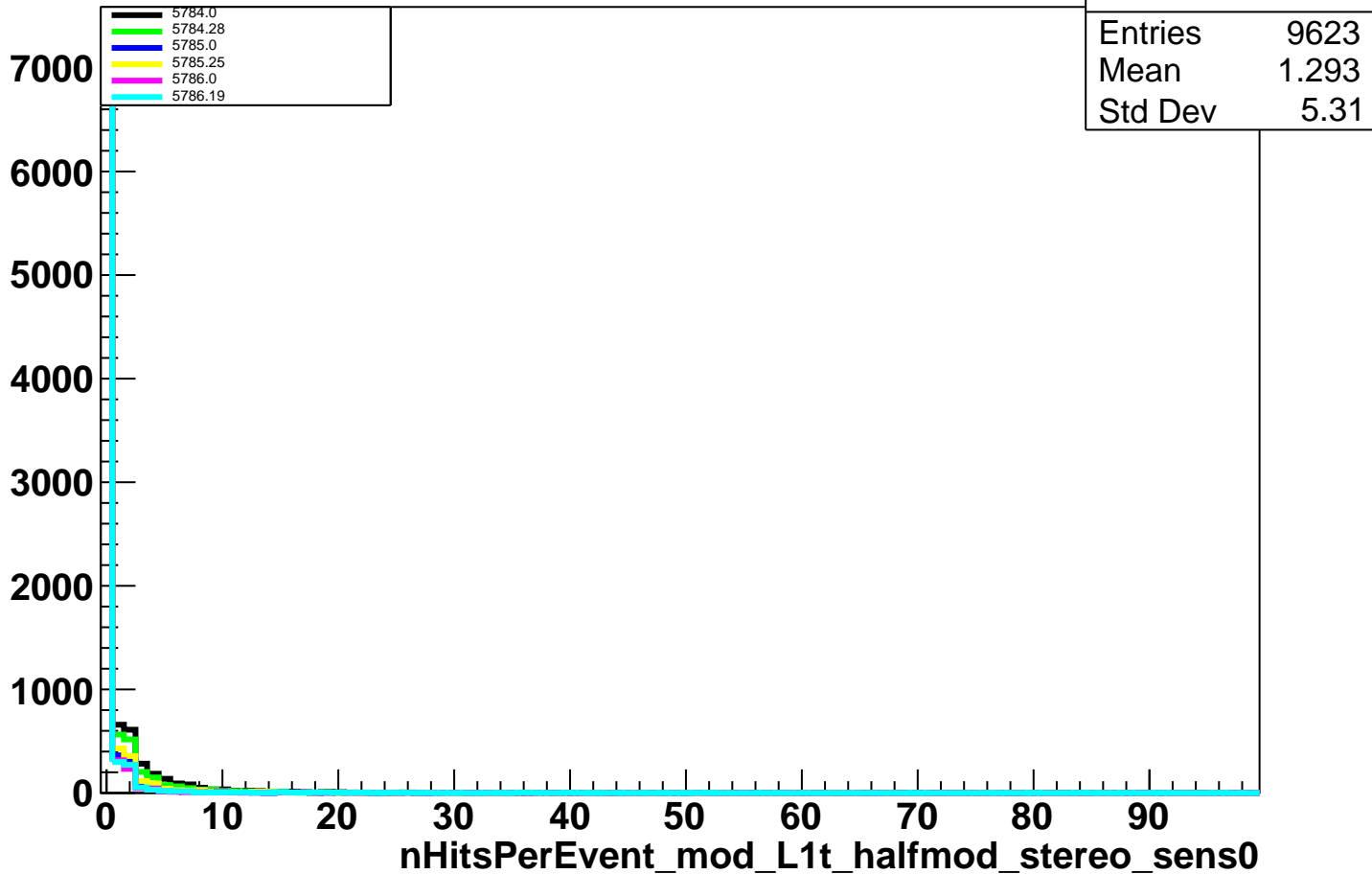
mod_L1b_halfmod_stereo_sens0



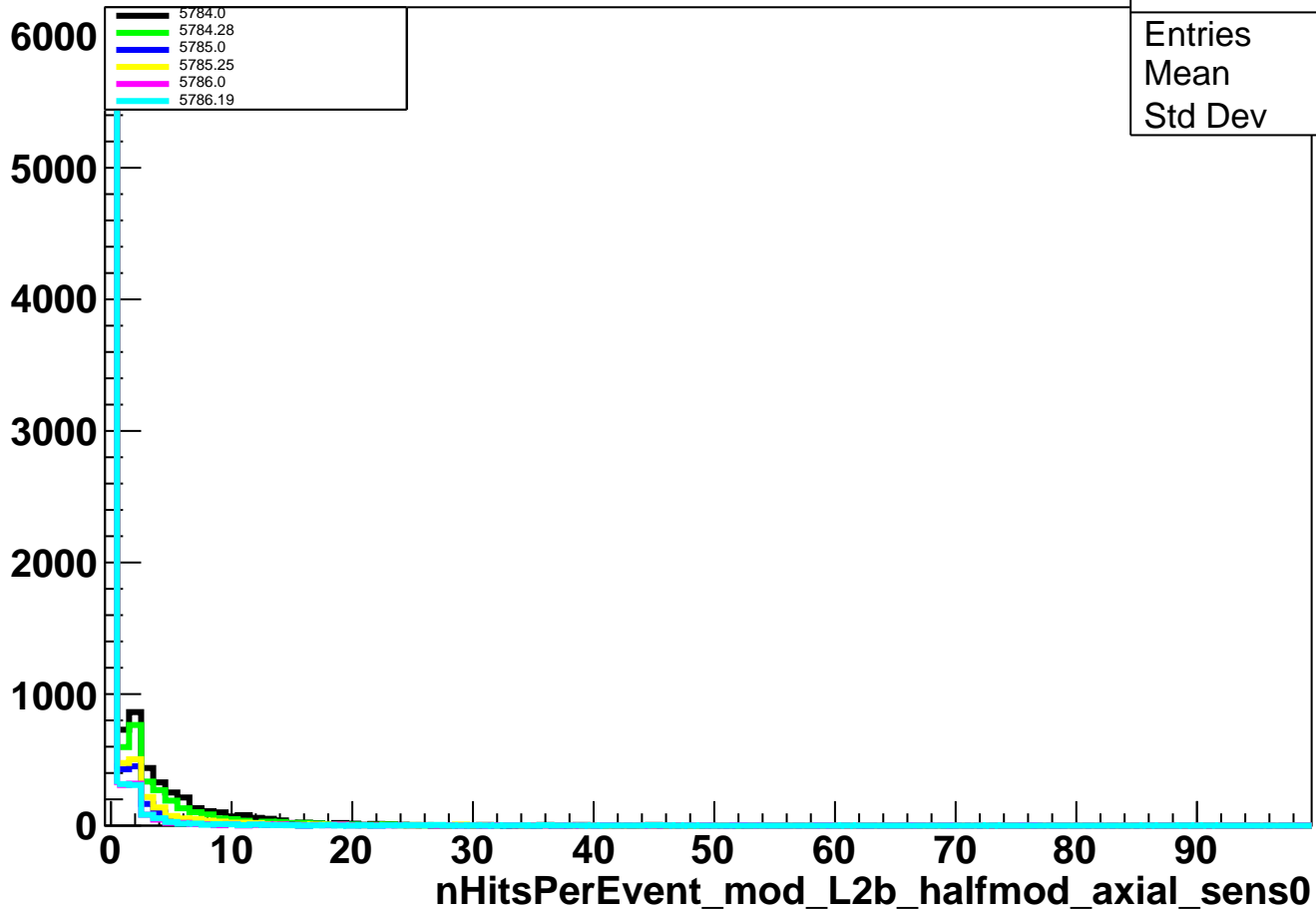
mod_L1t_halfmod_axial_sens0



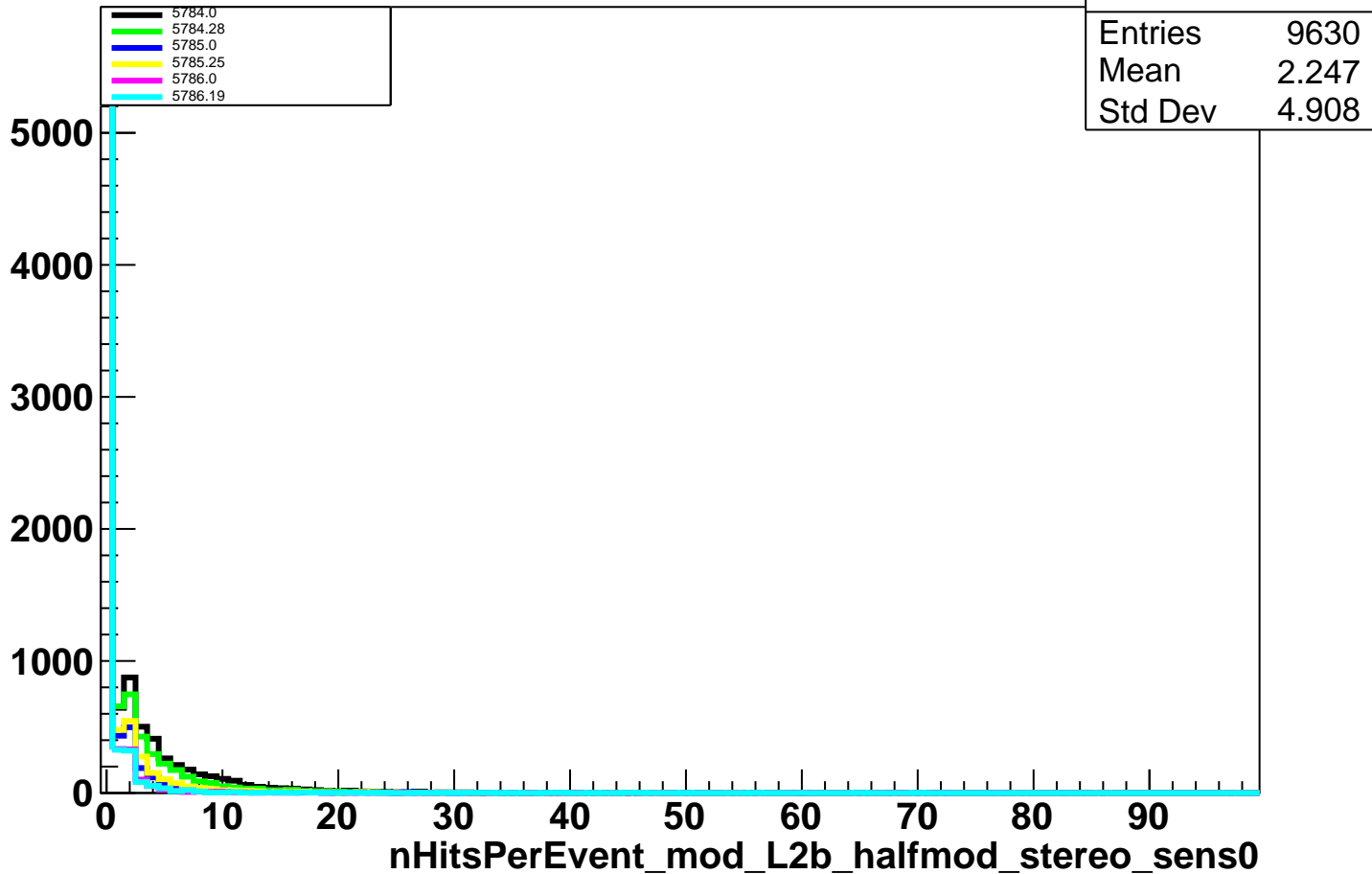
mod_L1t_halfmod_stereo_sens0



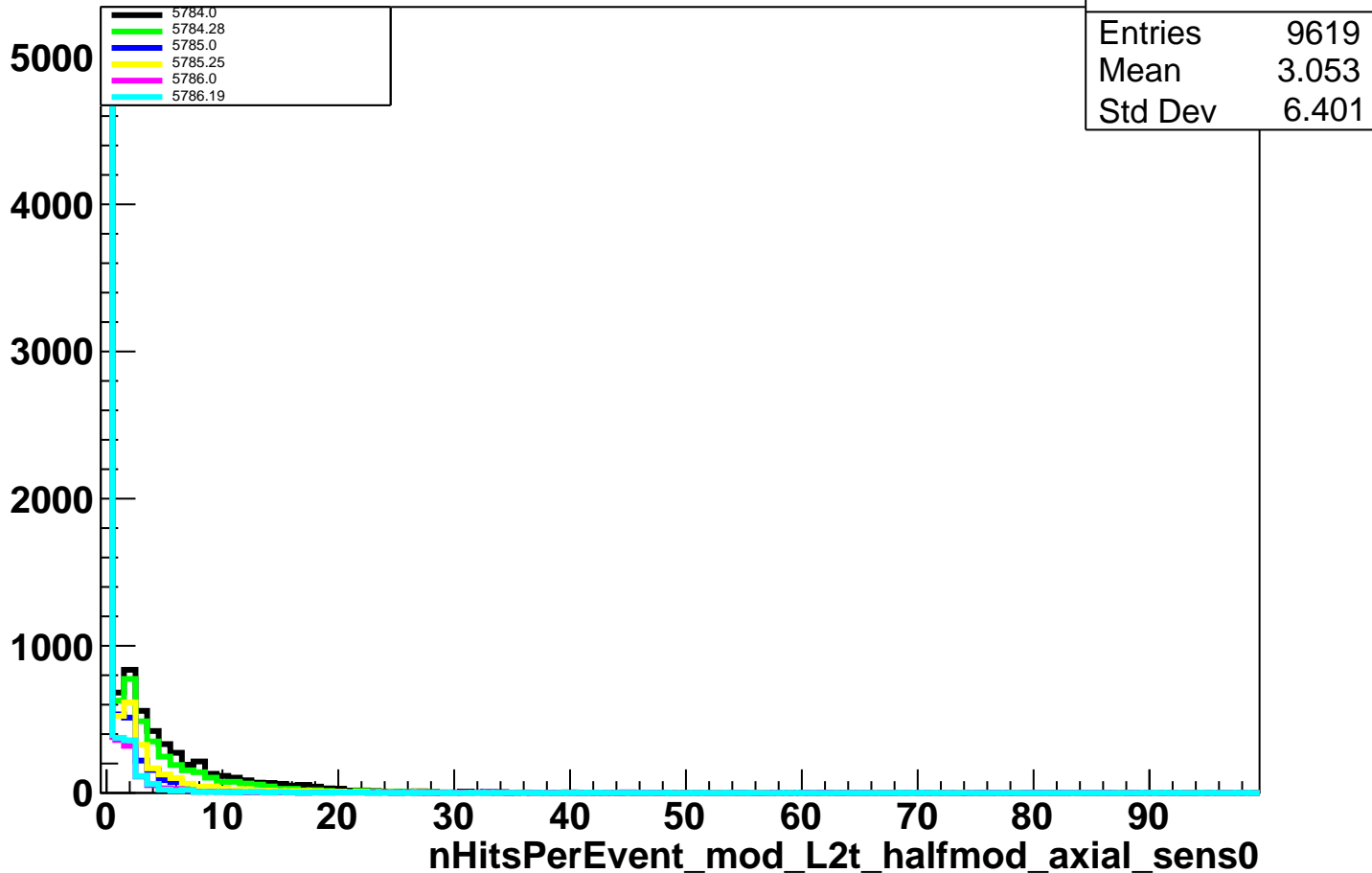
mod_L2b_halfmod_axial_sens0



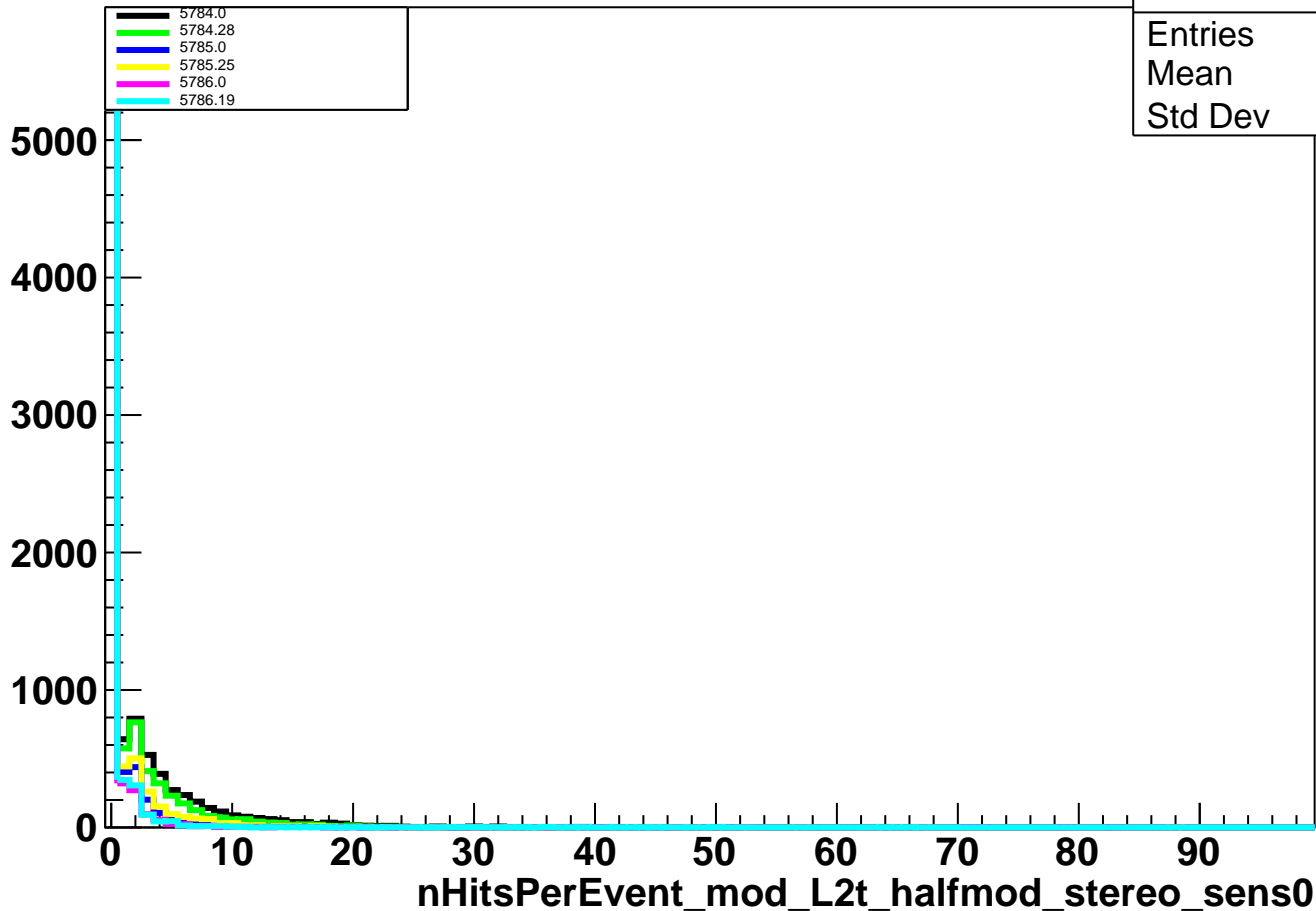
mod_L2b_halfmod_stereo_sens0



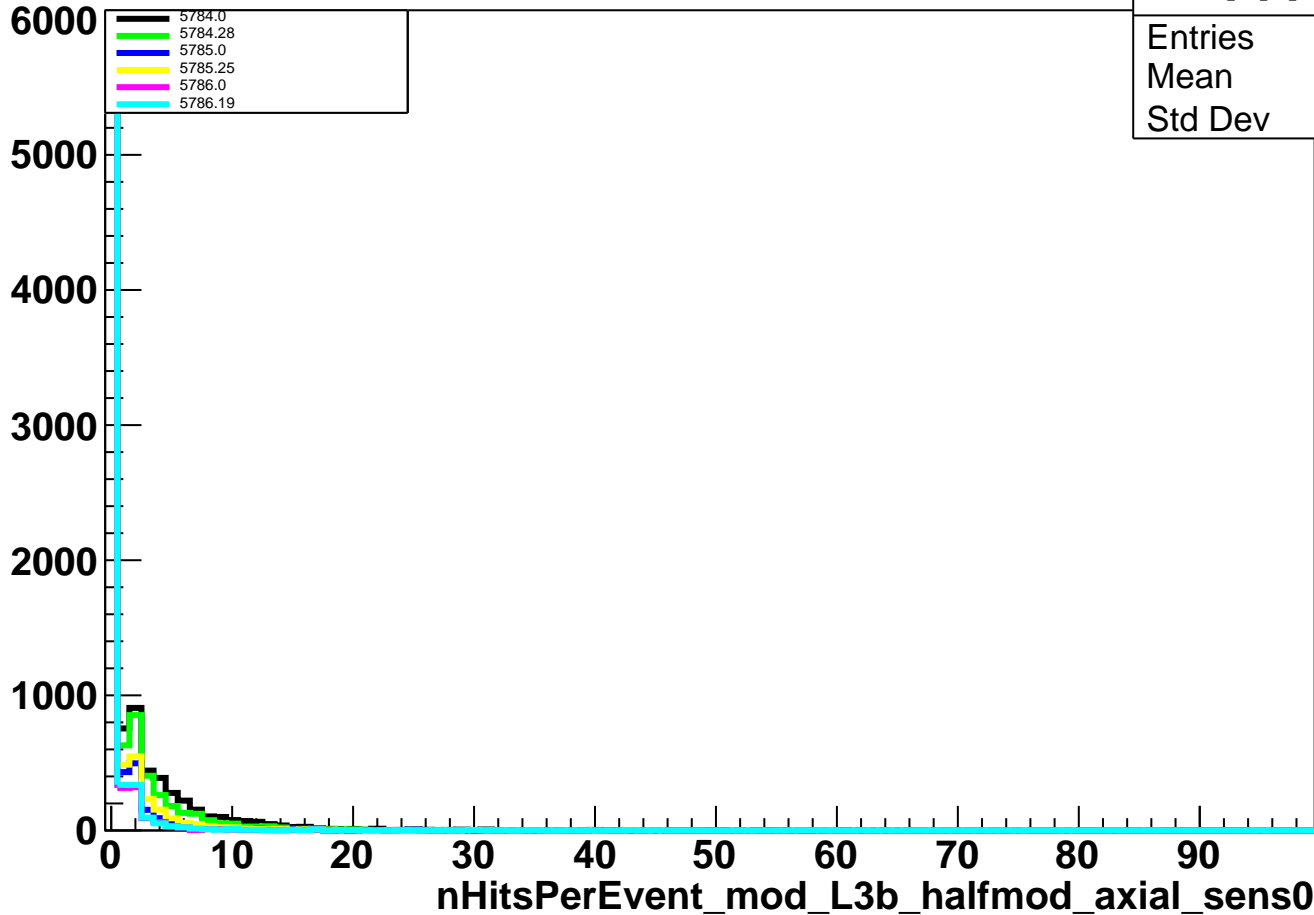
mod_L2t_halfmod_axial_sens0



mod_L2t_halfmod_stereo_sens0

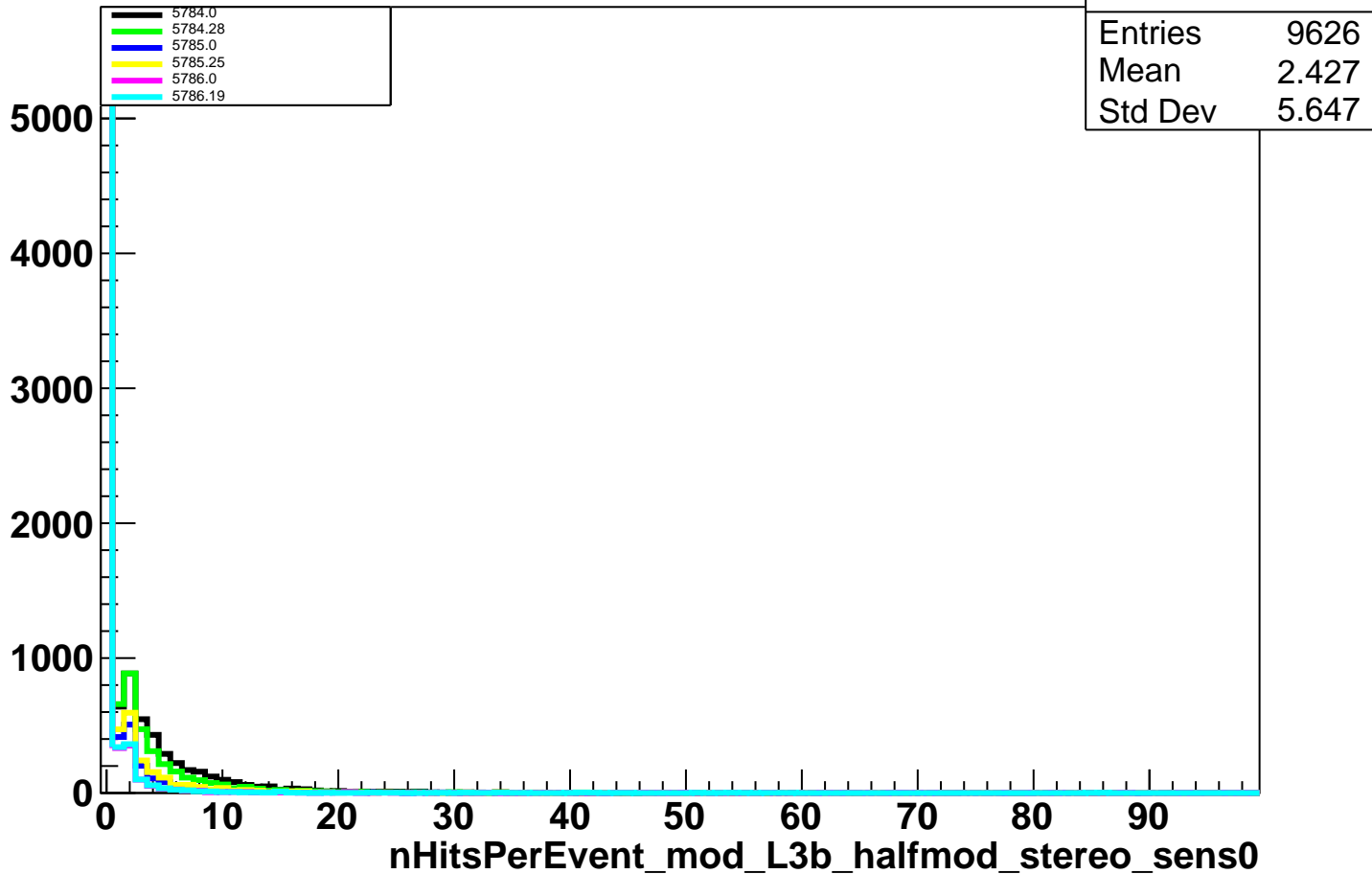


mod_L3b_halfmod_axial_sens0

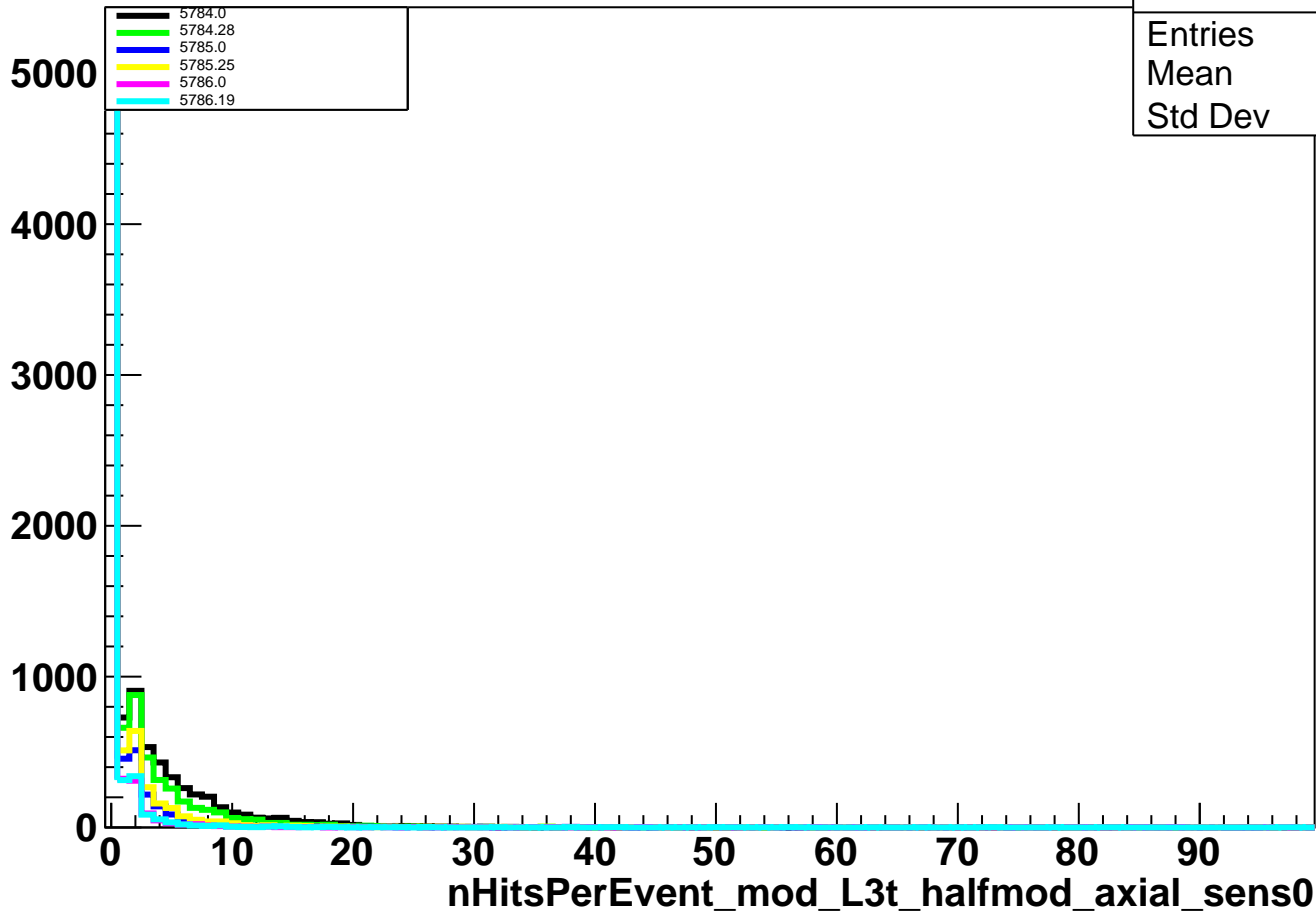


nHitsPerEvent_mod_L3b_halfmod_axial_sens0	
Entries	9627
Mean	2.033
Std Dev	4.882

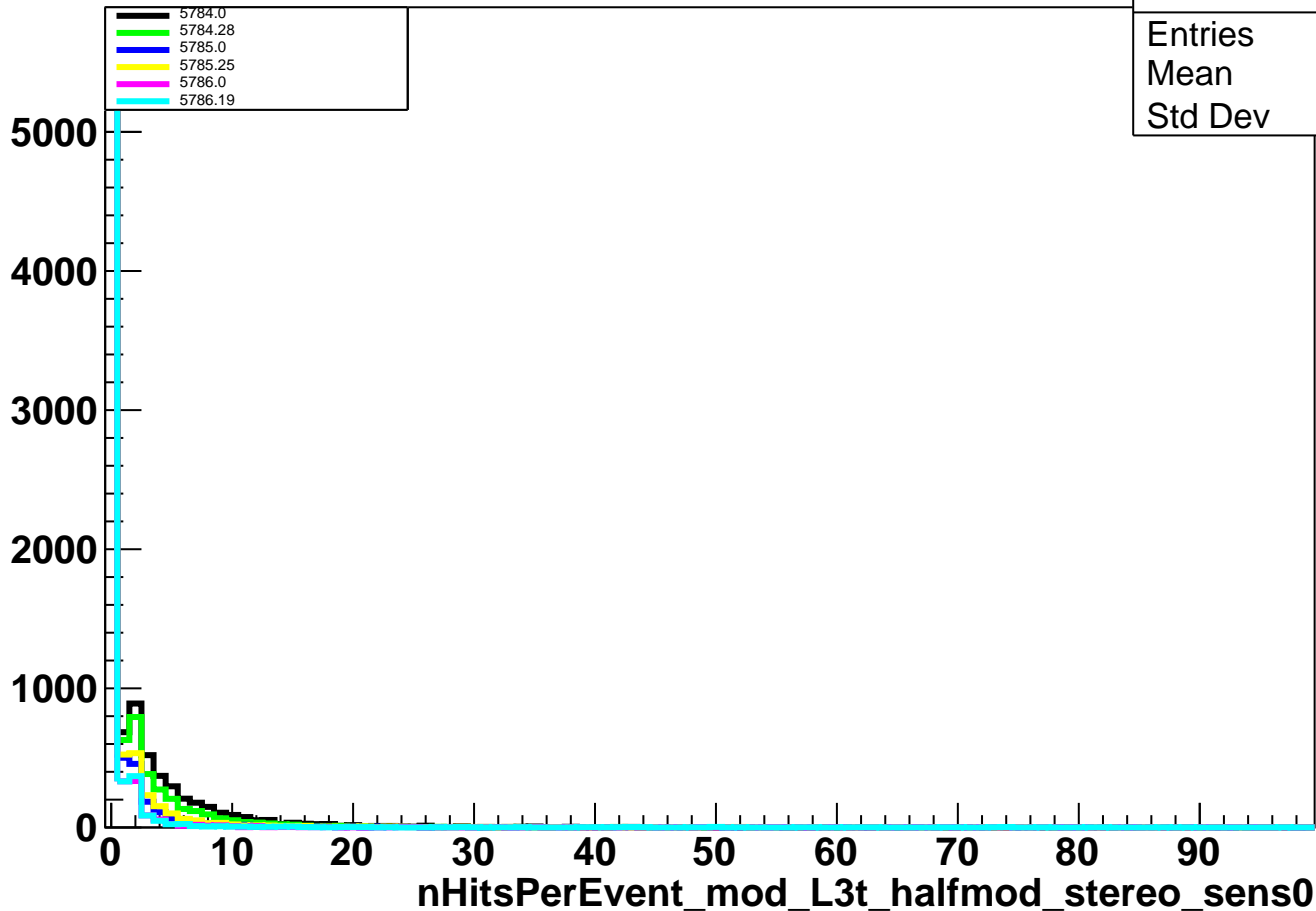
mod_L3b_halfmod_stereo_sens0



mod_L3t_halfmod_axial_sens0



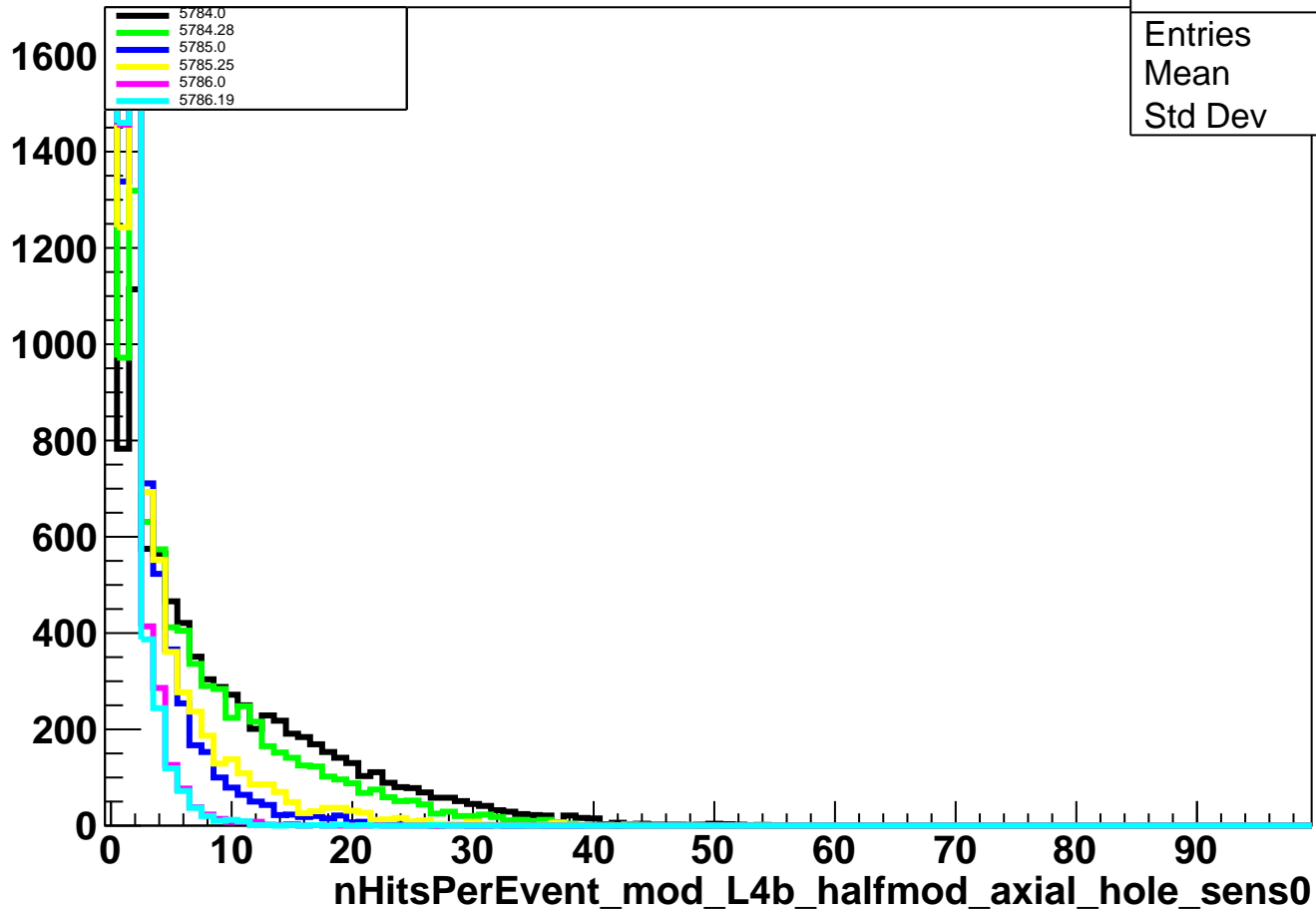
mod_L3t_halfmod_stereo_sens0



nHitsPerEvent_mod_L3t_halfmod_stereo_sens0

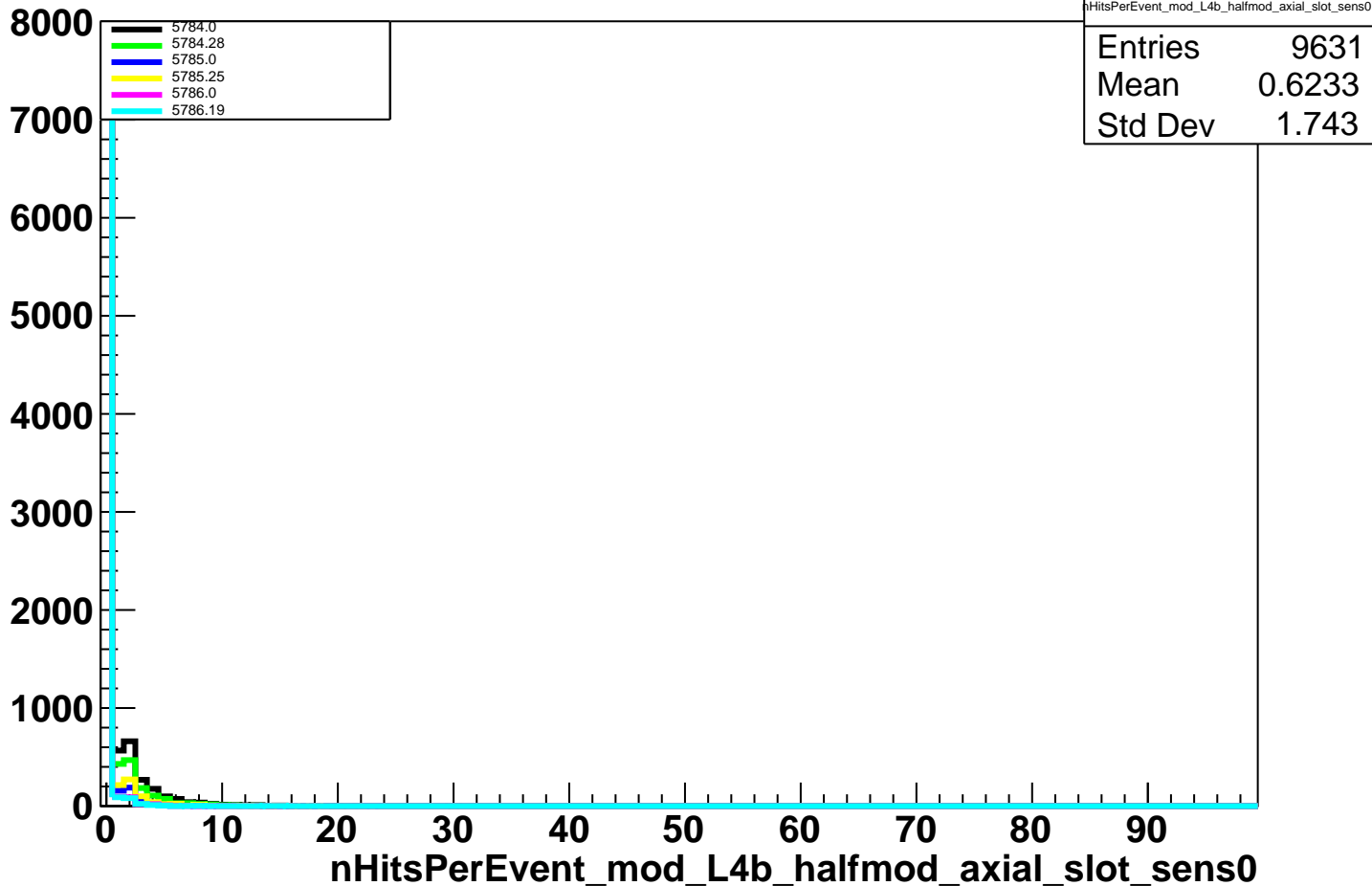
Entries	9595
Mean	2.371
Std Dev	5.826

mod_L4b_halfmod_axial_hole_sens0

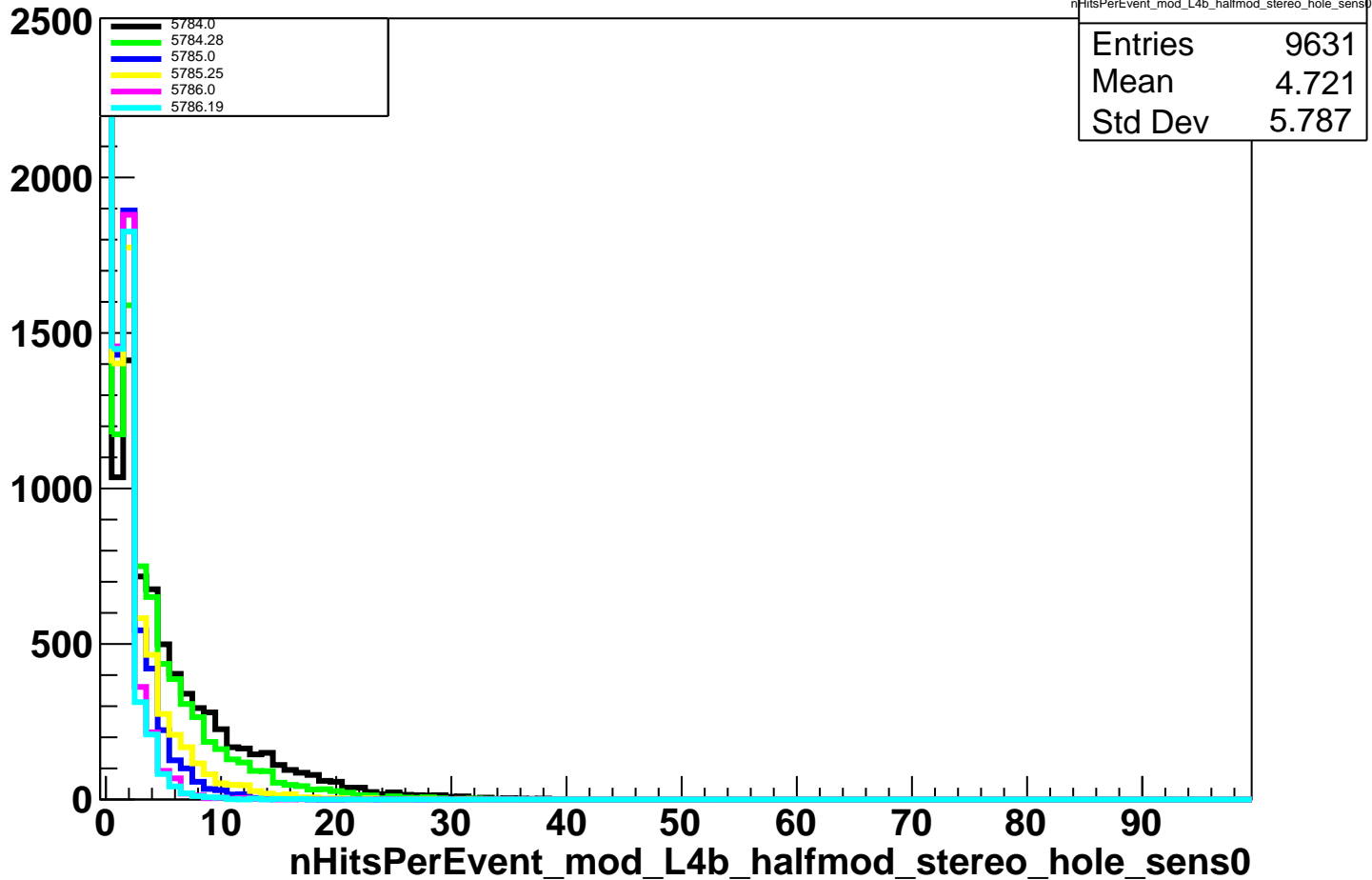


nHitsPerEvent_mod_L4b_halfmod_axial_hole_sens0	
Entries	9631
Mean	8.088
Std Dev	8.821

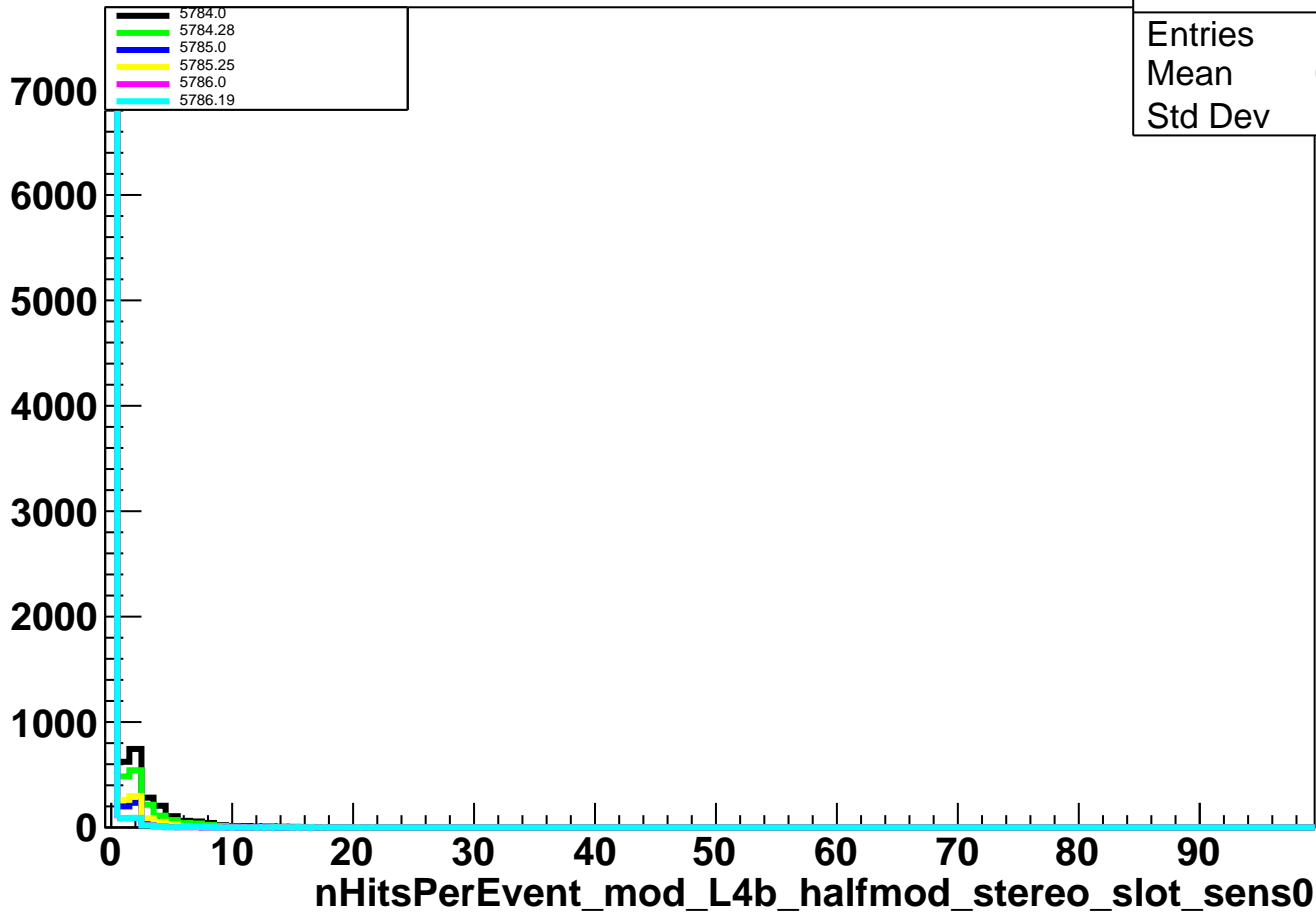
mod_L4b_halfmod_axial_slot_sens0



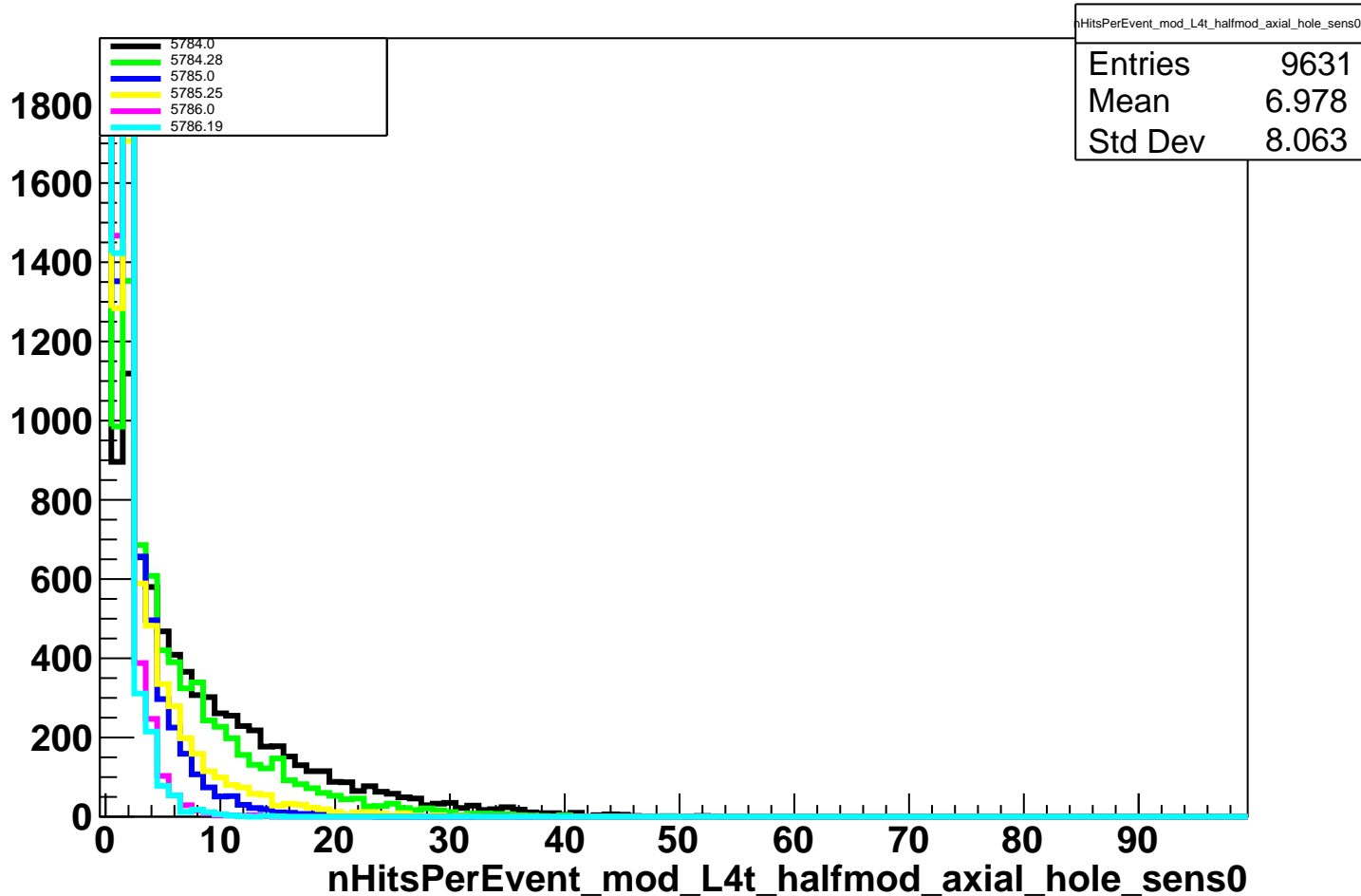
mod_L4b_halfmod_stereo_hole_sens0



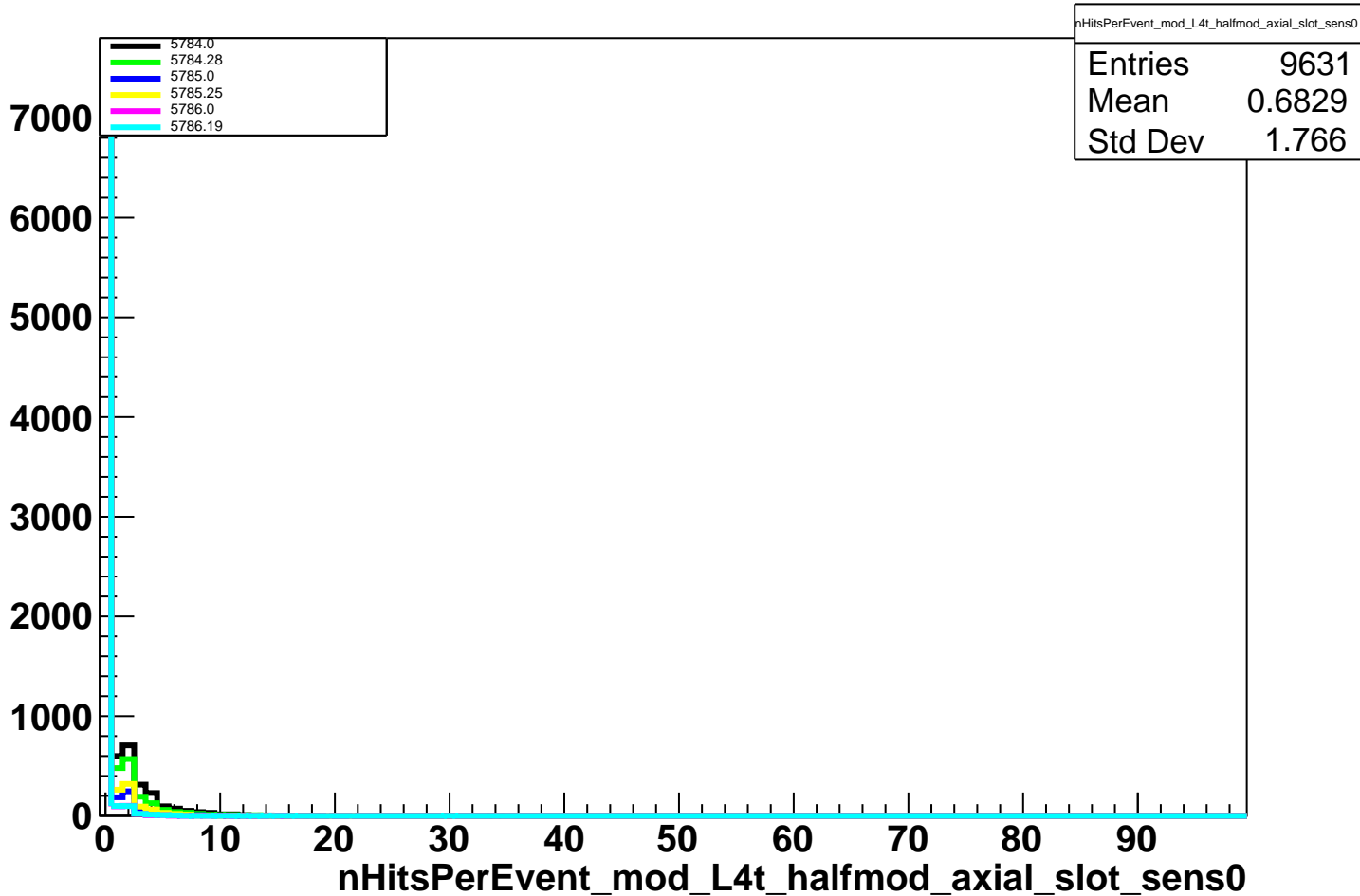
mod_L4b_halfmod_stereo_slot_sens0



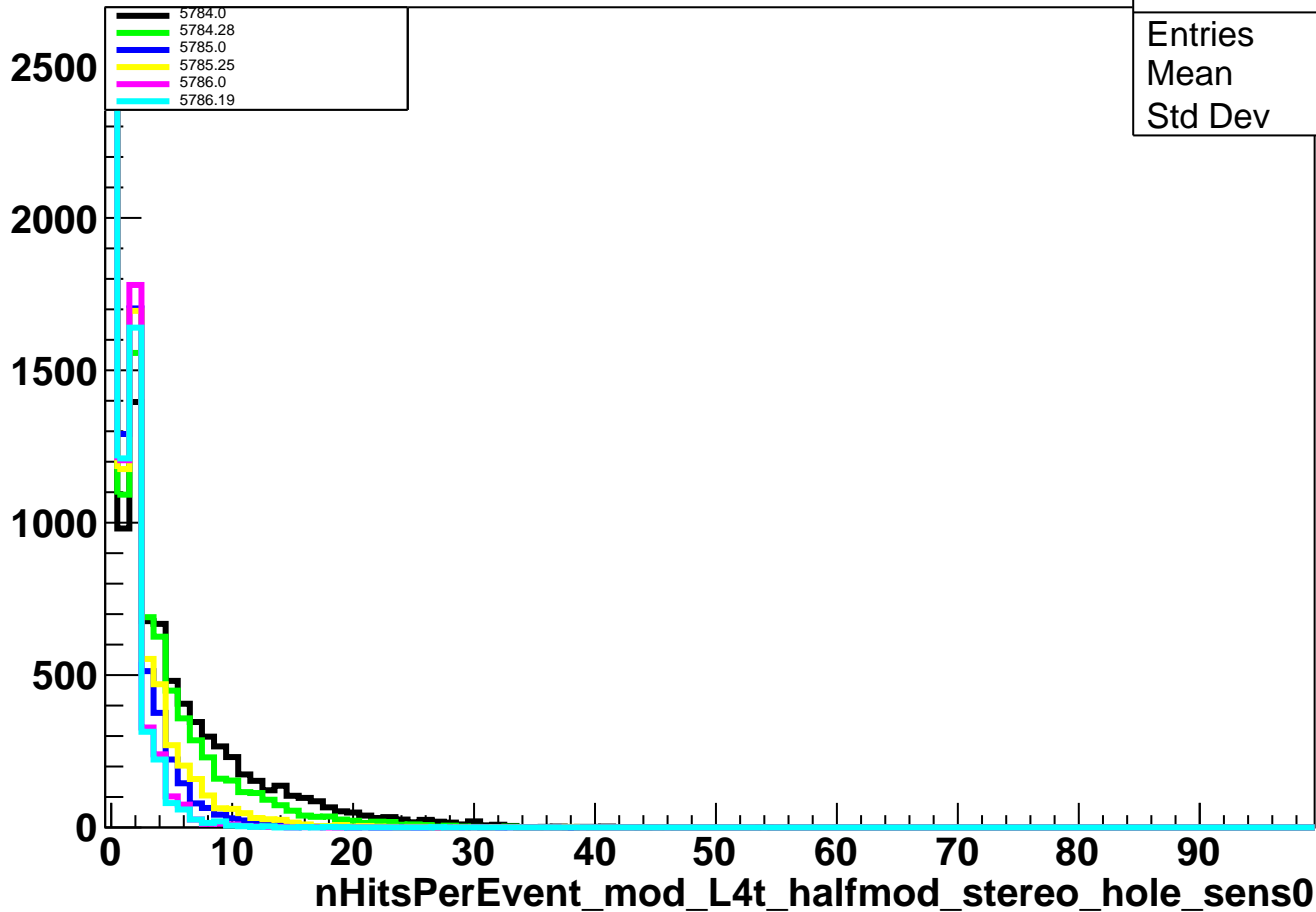
mod_L4t_halfmod_axial_hole_sens0



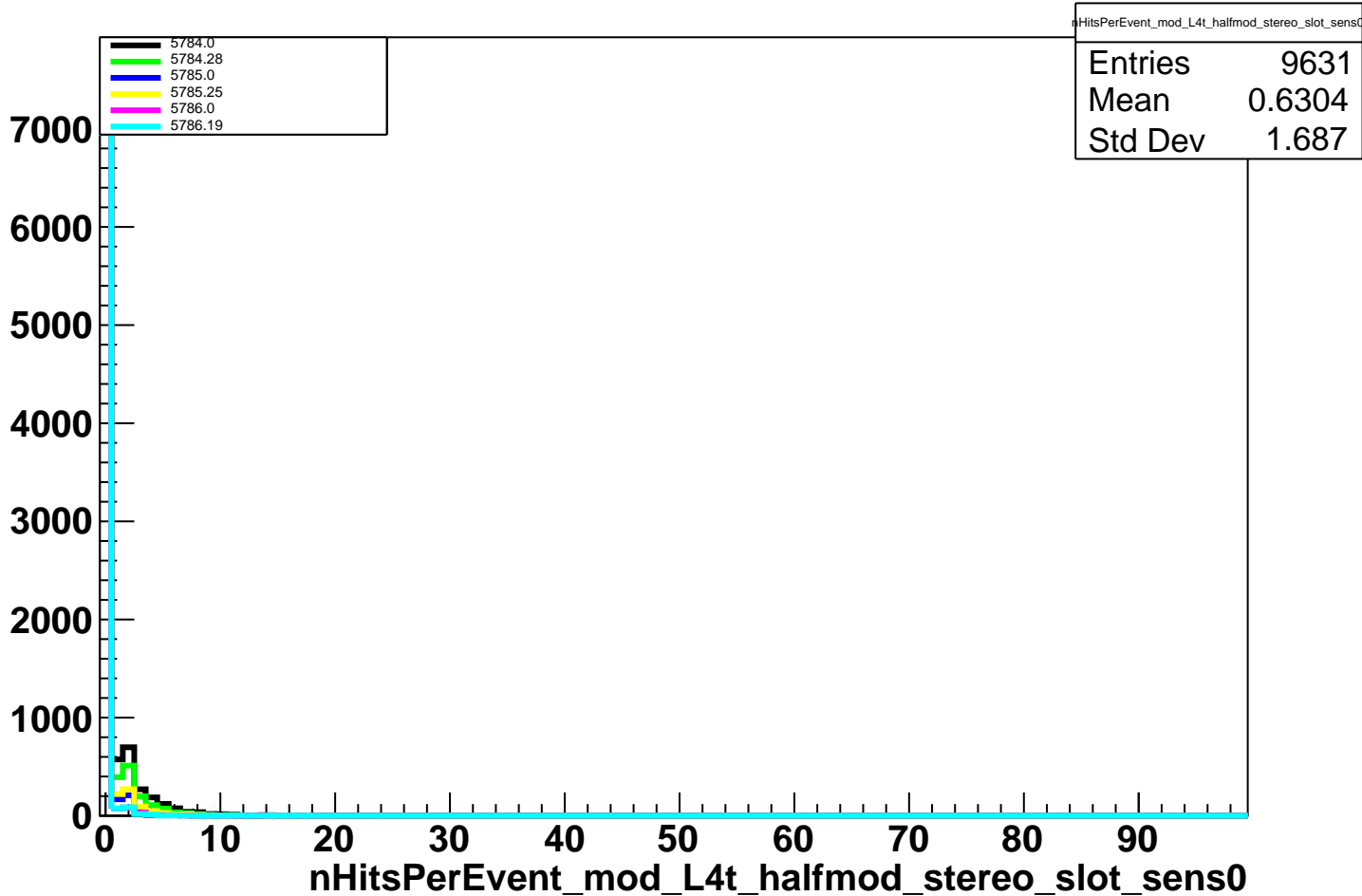
mod_L4t_halfmod_axial_slot_sens0



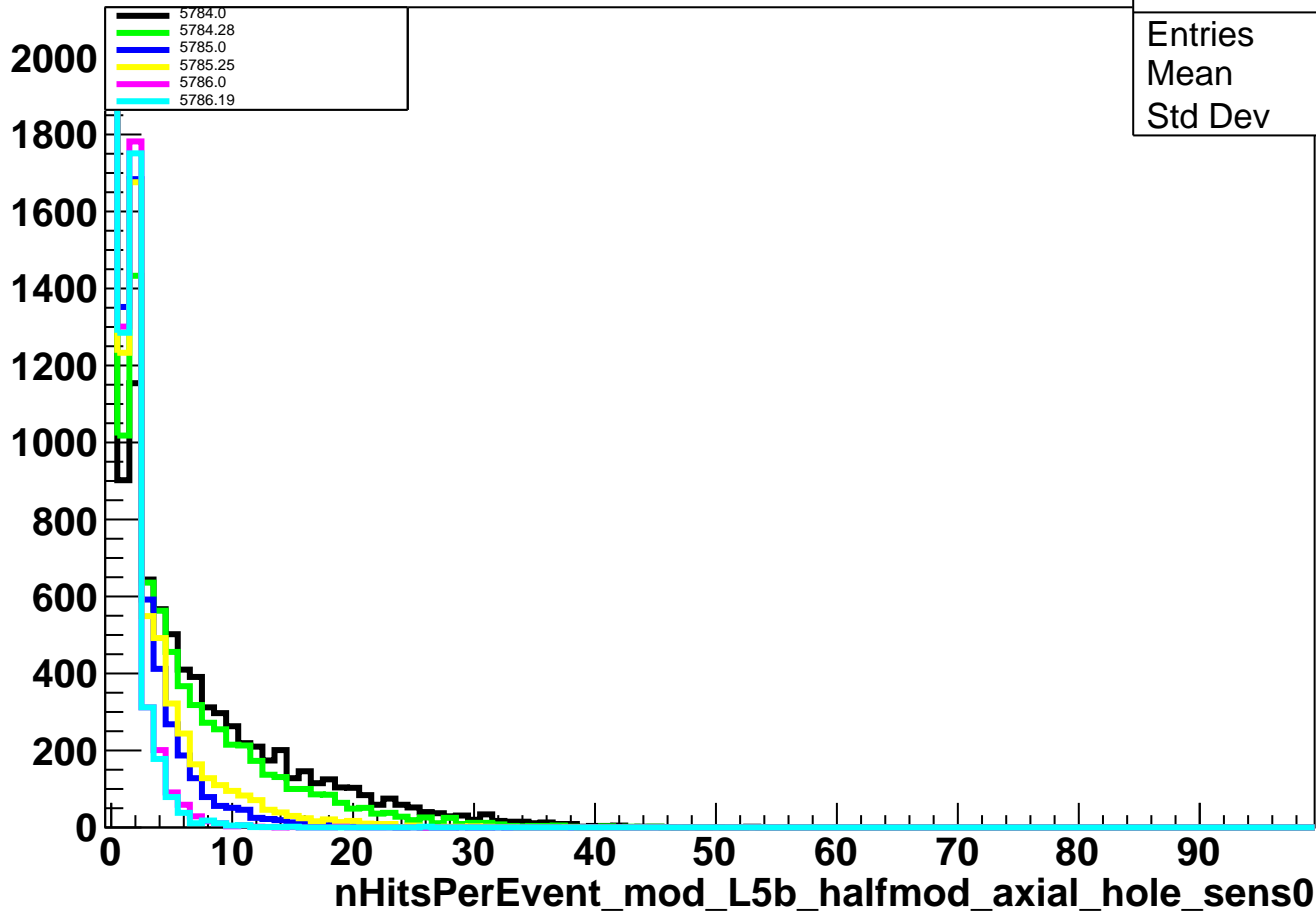
mod_L4t_halfmod_stereo_hole_sens0



mod_L4t_halfmod_stereo_slot_sens0

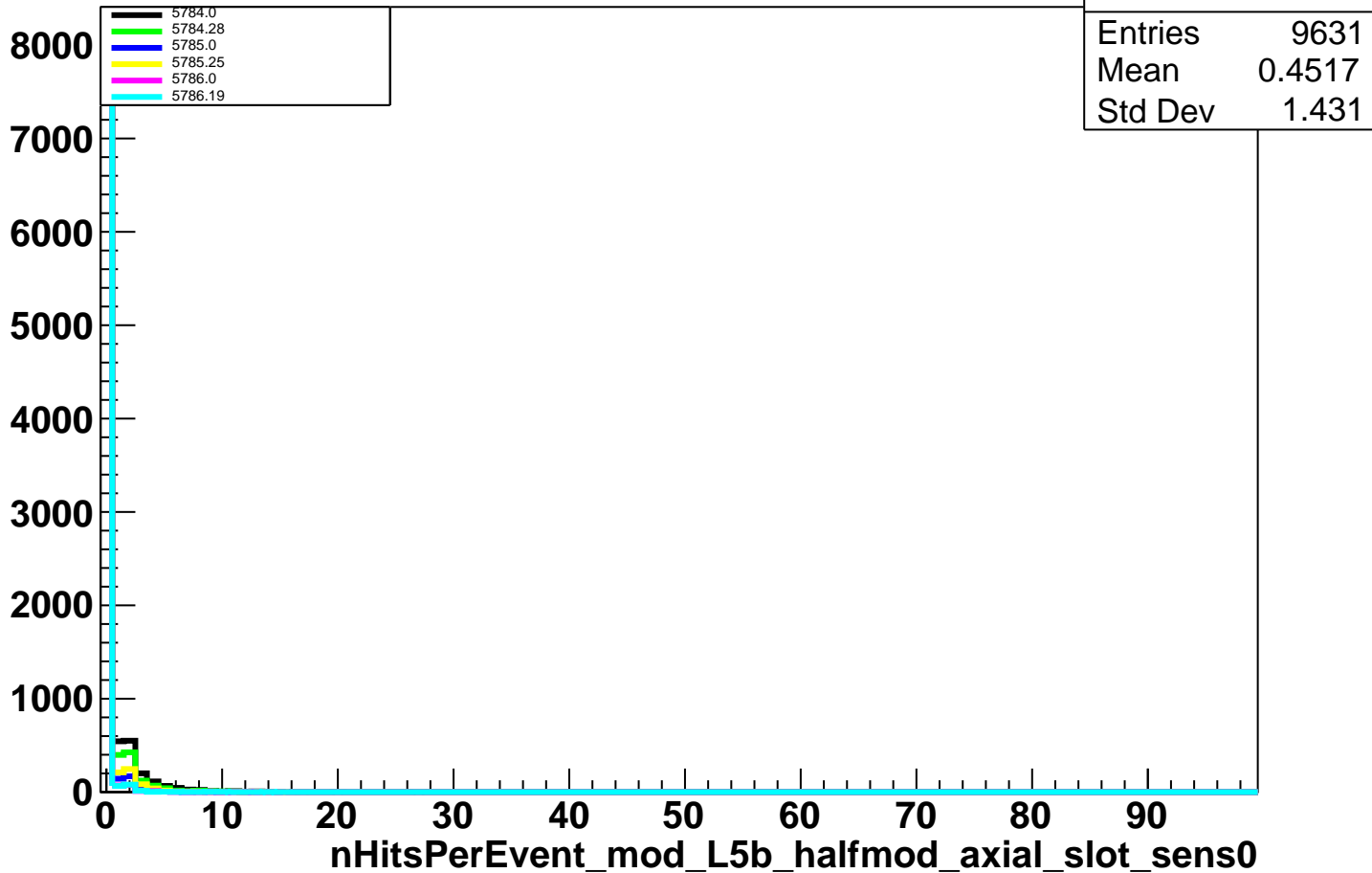


mod_L5b_halfmod_axial_hole_sens0

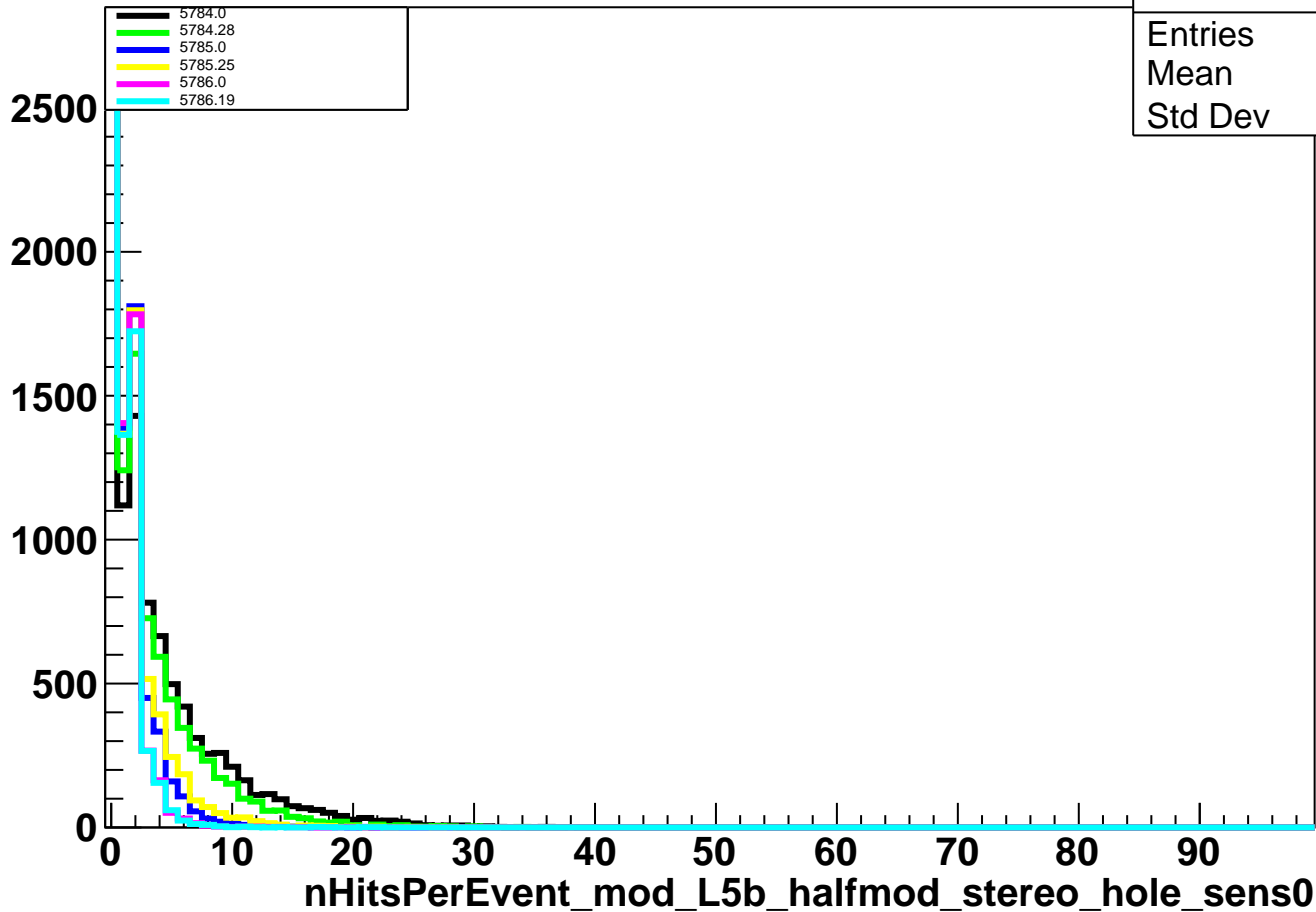


#HitsPerEvent_mod_L5b_halfmod_axial_hole_sens0	
Entries	9630
Mean	6.489
Std Dev	7.593

mod_L5b_halfmod_axial_slot_sens0

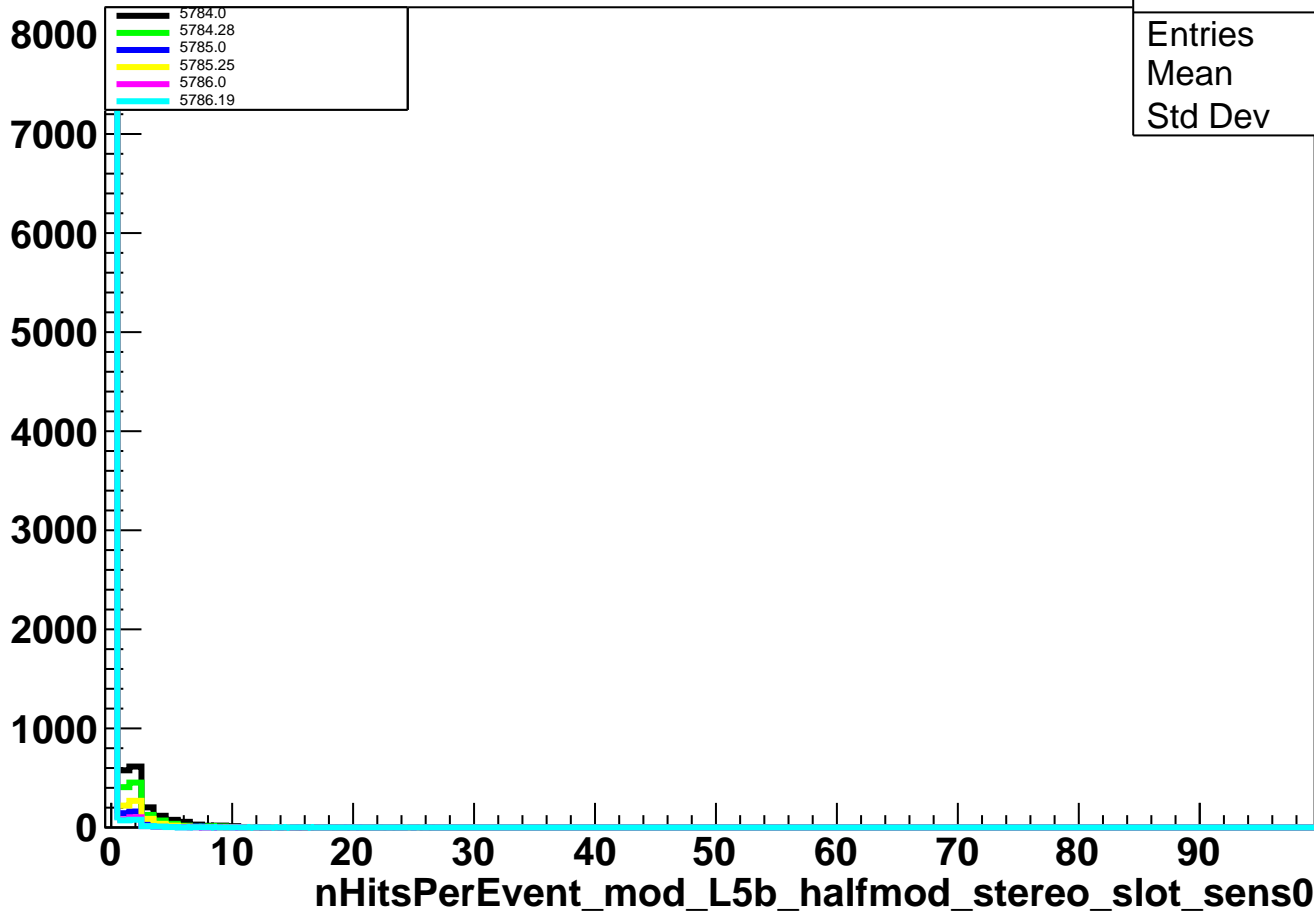


mod_L5b_halfmod_stereo_hole_sens0

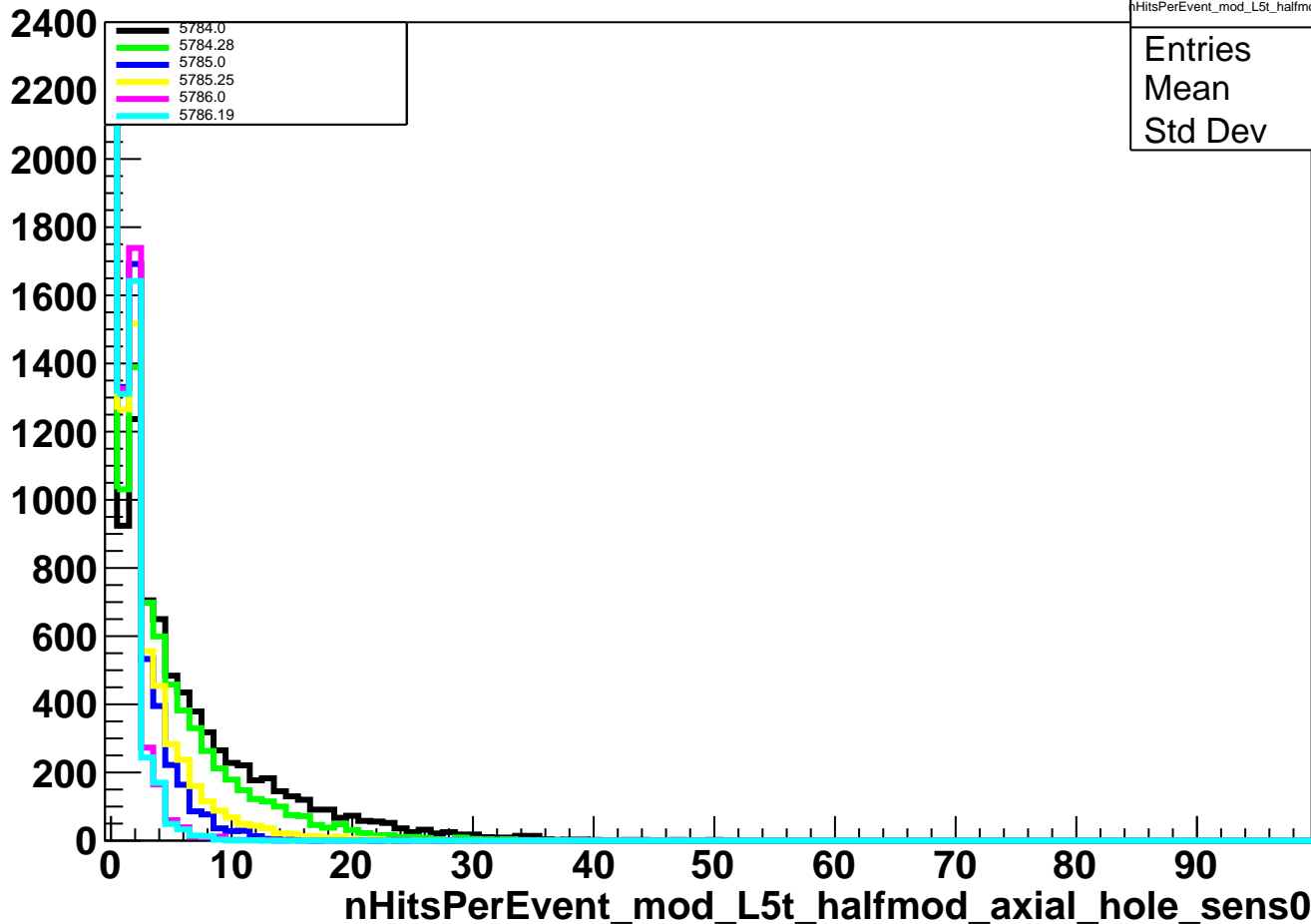


nHitsPerEvent_mod_L5b_halfmod_stereo_hole_sens0	
Entries	9631
Mean	3.94
Std Dev	4.984

mod_L5b_halfmod_stereo_slot_sens0

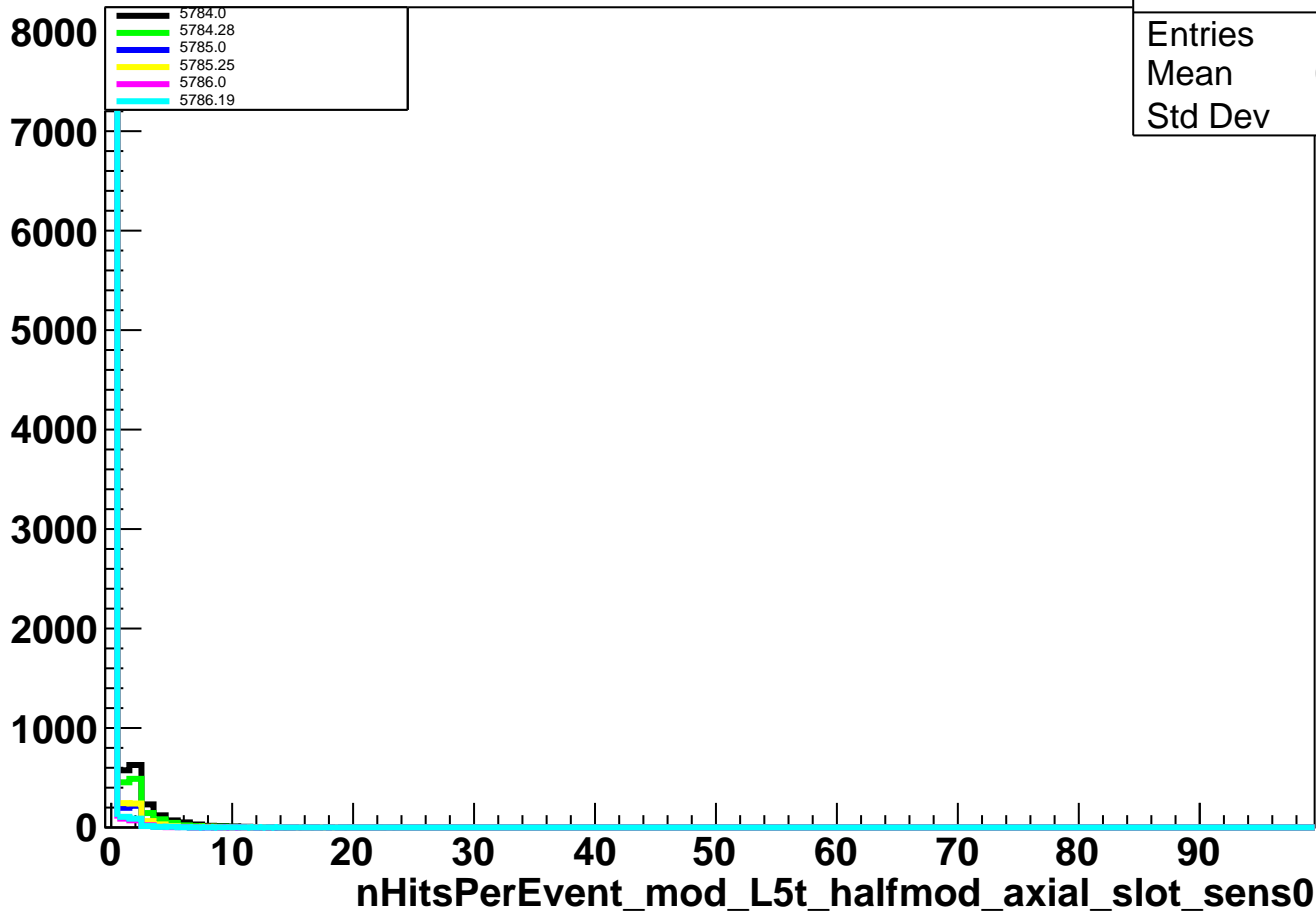


mod_L5t_halfmod_axial_hole_sens0

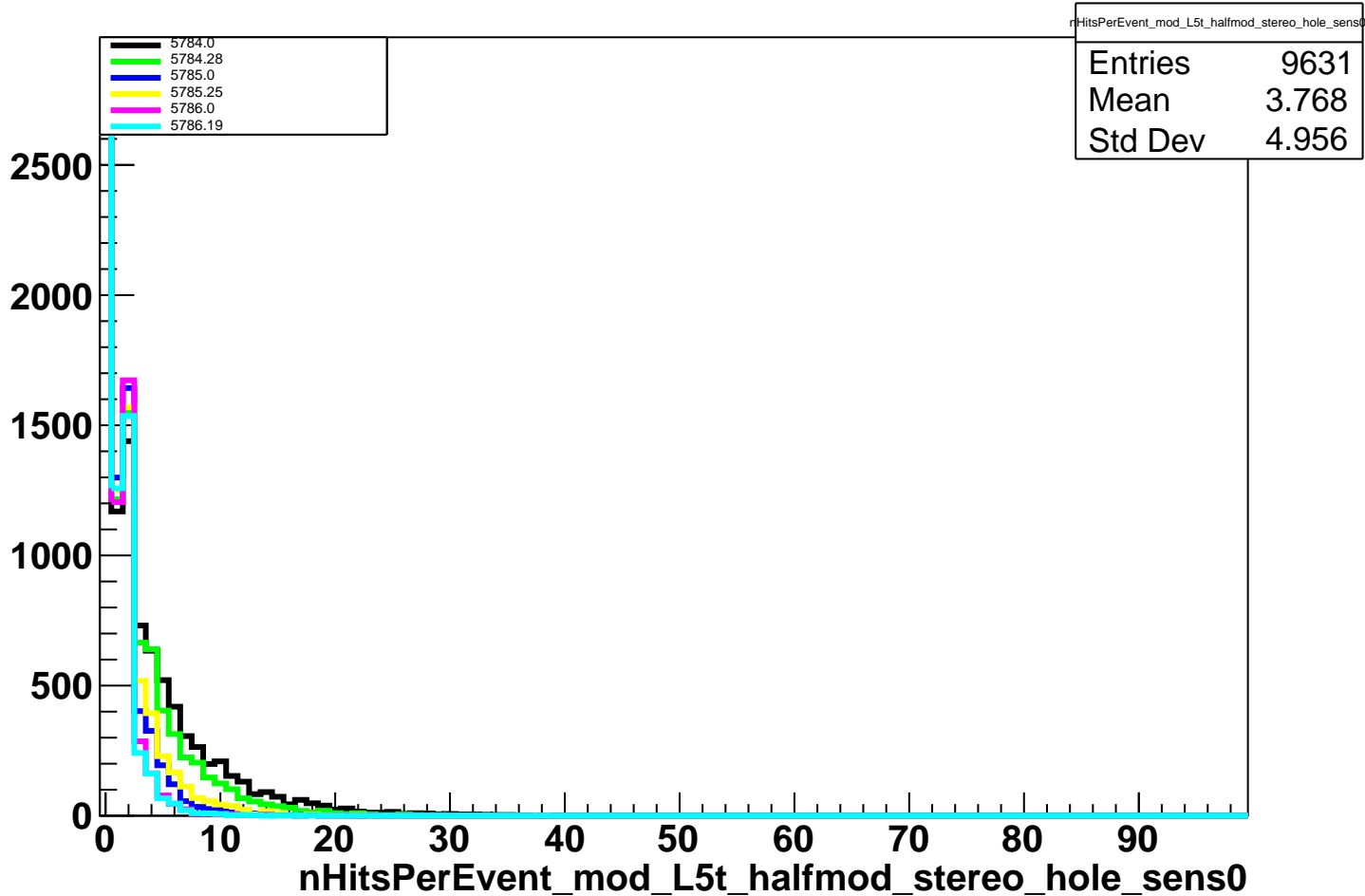


hHitsPerEvent_mod_L5t_halfmod_axial_hole_sens0	
Entries	9631
Mean	5.492
Std Dev	6.687

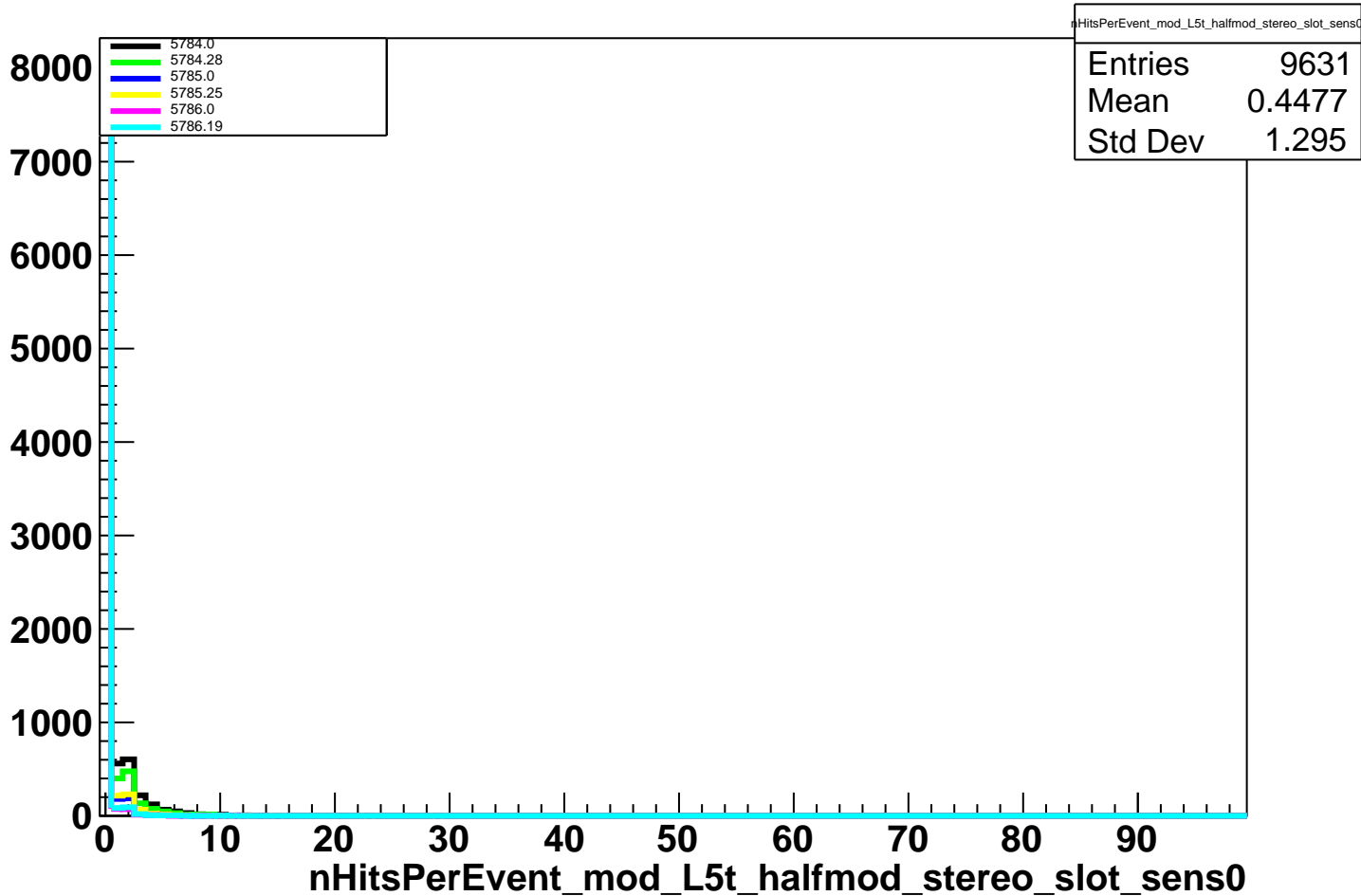
mod_L5t_halfmod_axial_slot_sens0



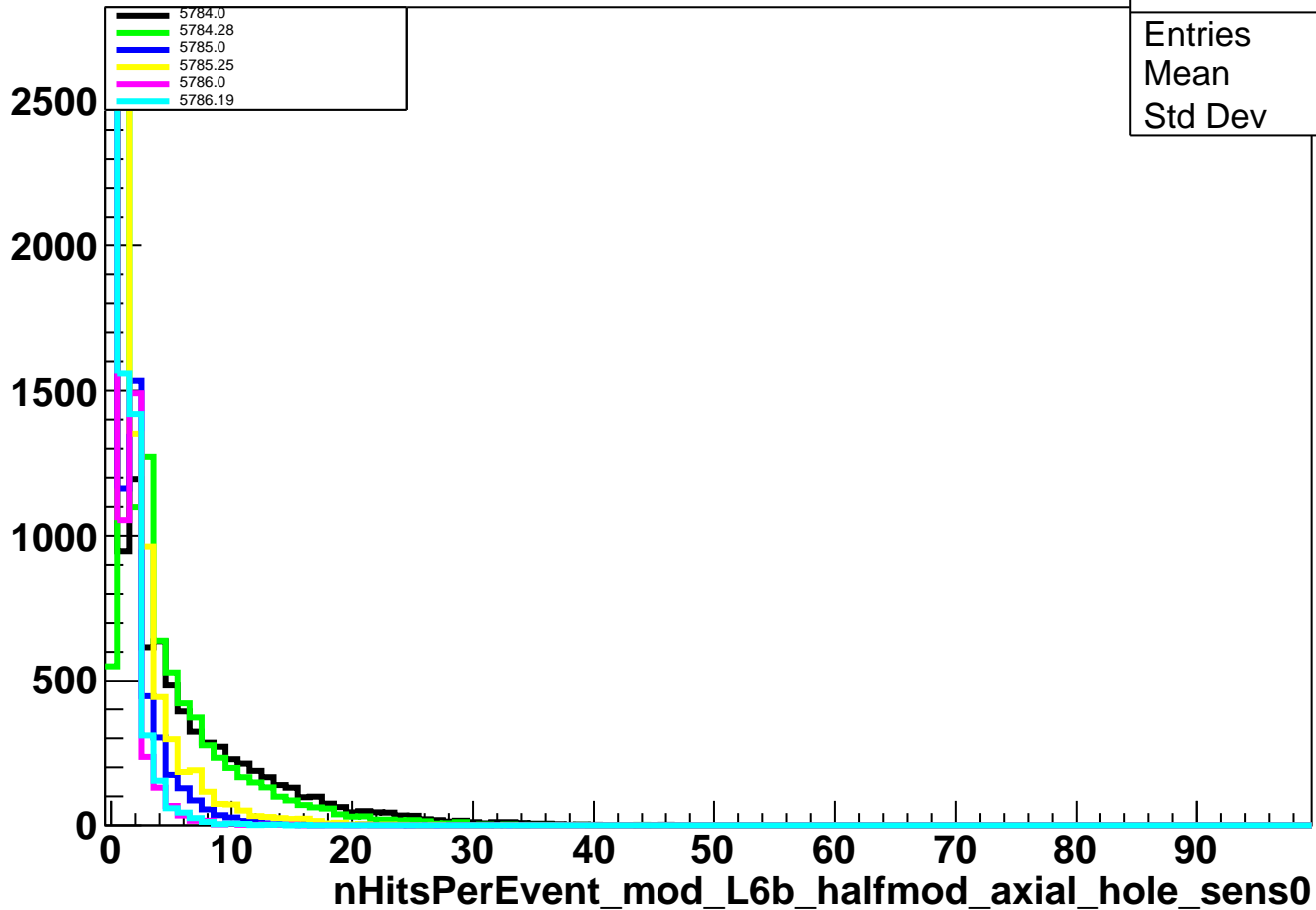
mod_L5t_halfmod_stereo_hole_sens0



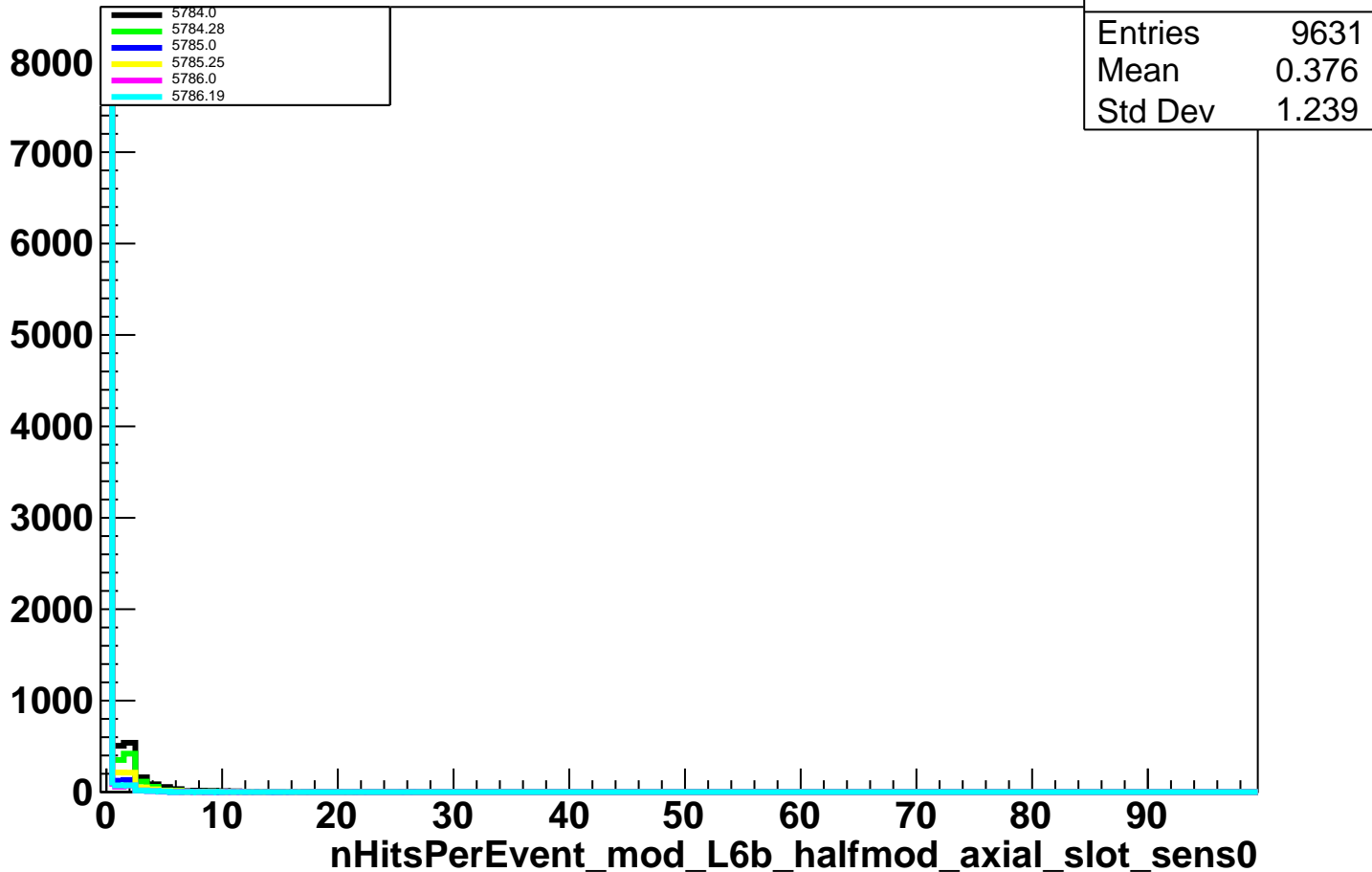
mod_L5t_halfmod_stereo_slot_sens0



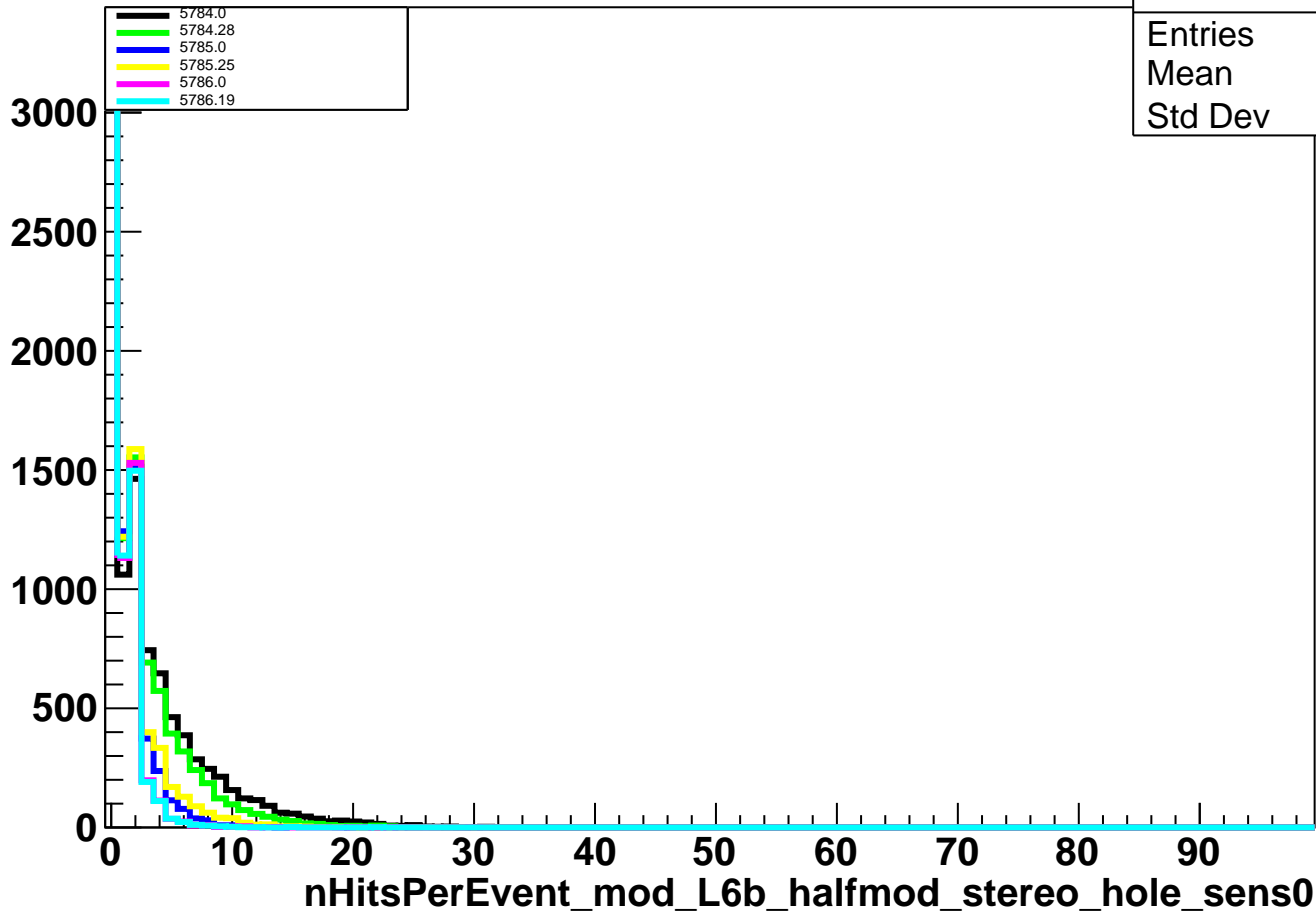
mod_L6b_halfmod_axial_hole_sens0



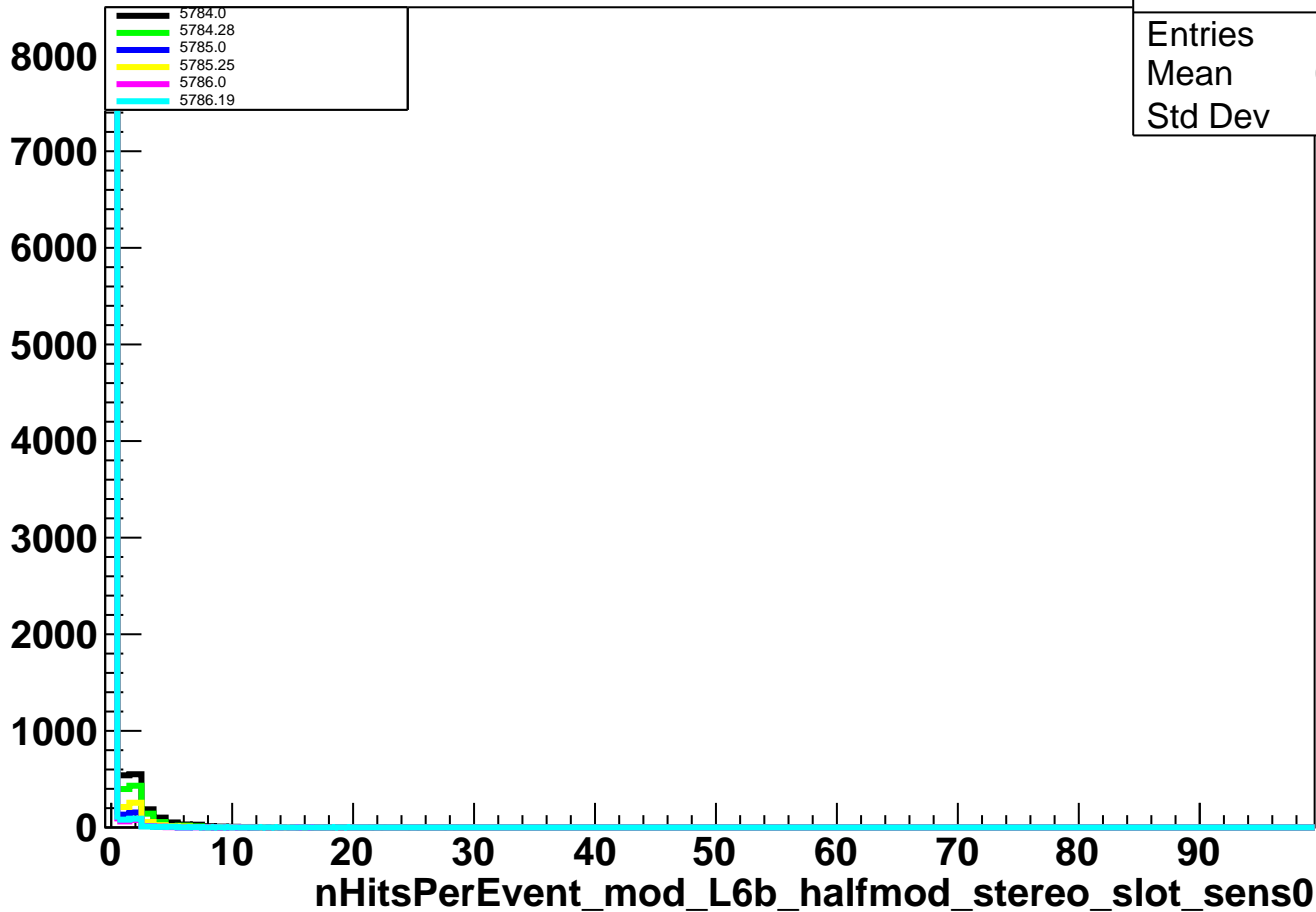
mod_L6b_halfmod_axial_slot_sens0



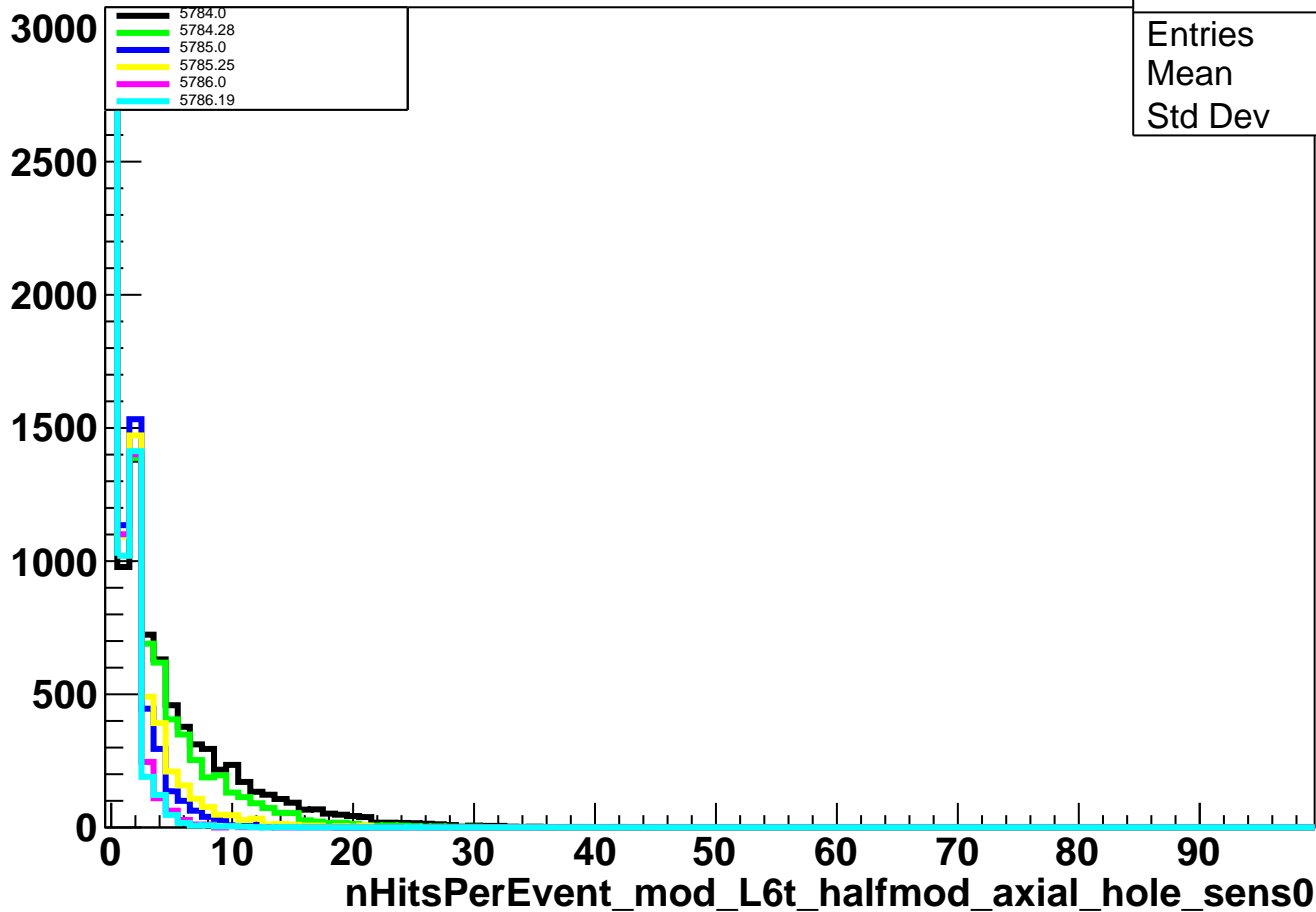
mod_L6b_halfmod_stereo_hole_sens0



mod_L6b_halfmod_stereo_slot_sens0

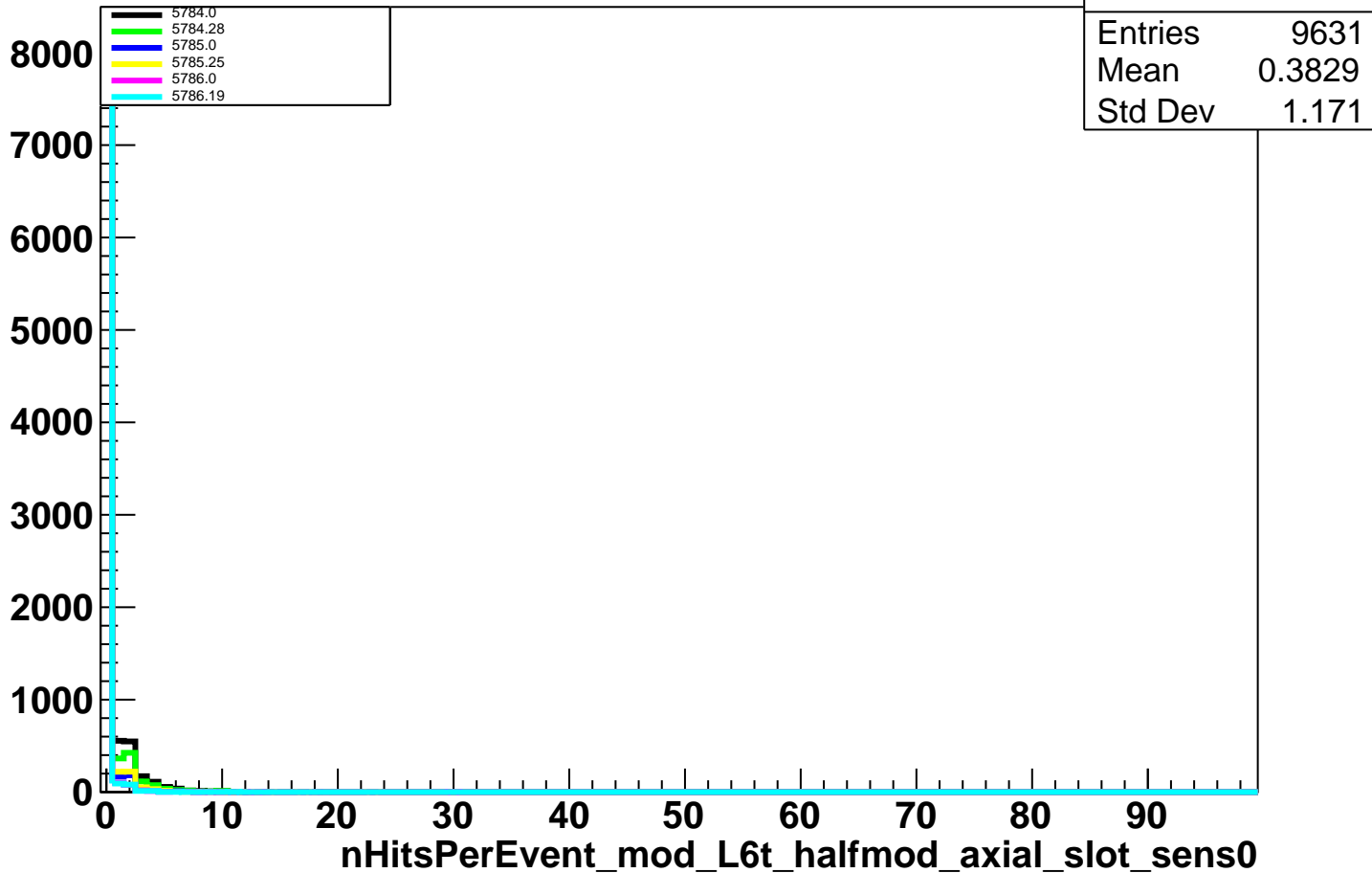


mod_L6t_halfmod_axial_hole_sens0

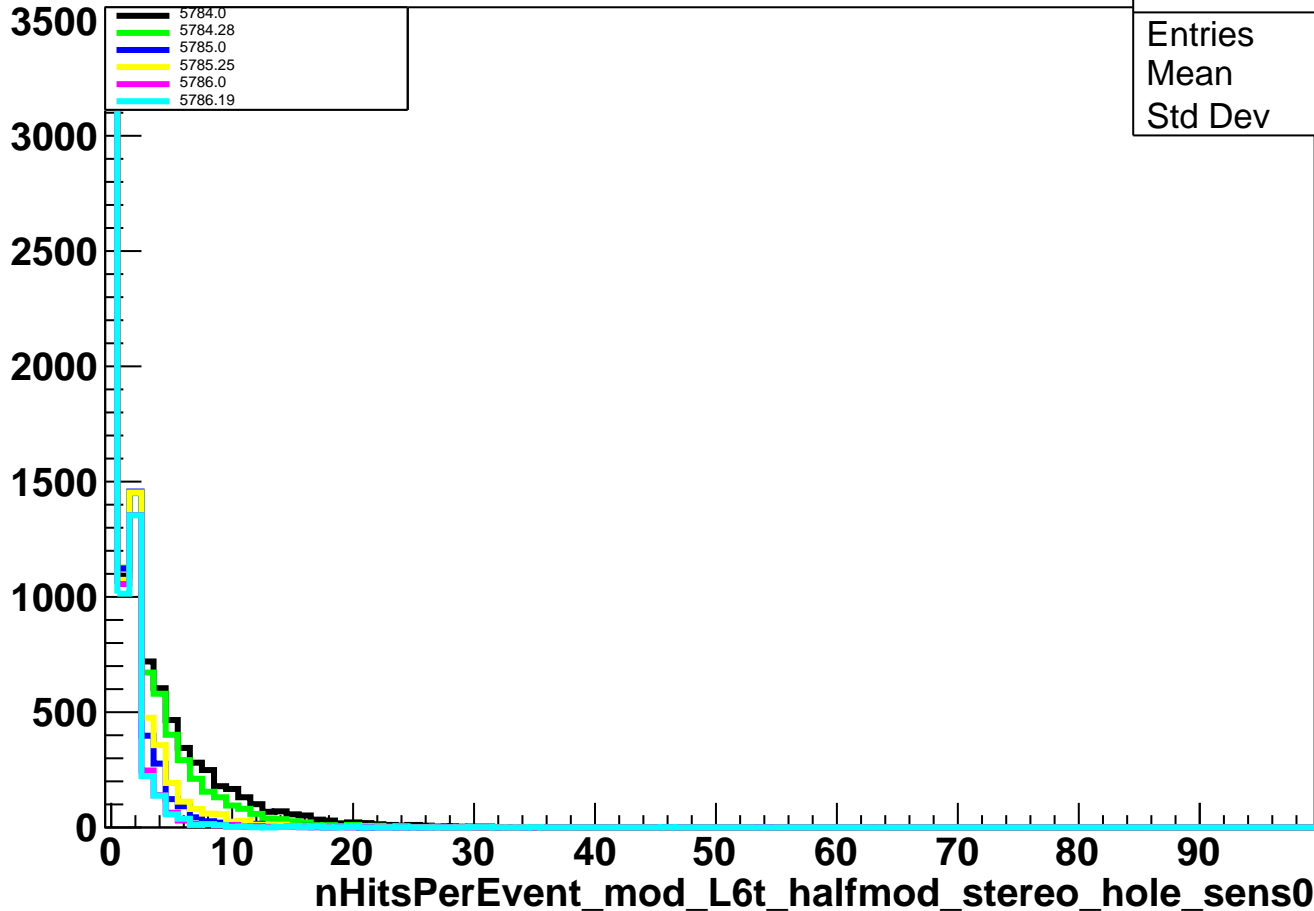


hHitsPerEvent_mod_L6t_halfmod_axial_hole_sens0	
Entries	9631
Mean	4.097
Std Dev	5.381

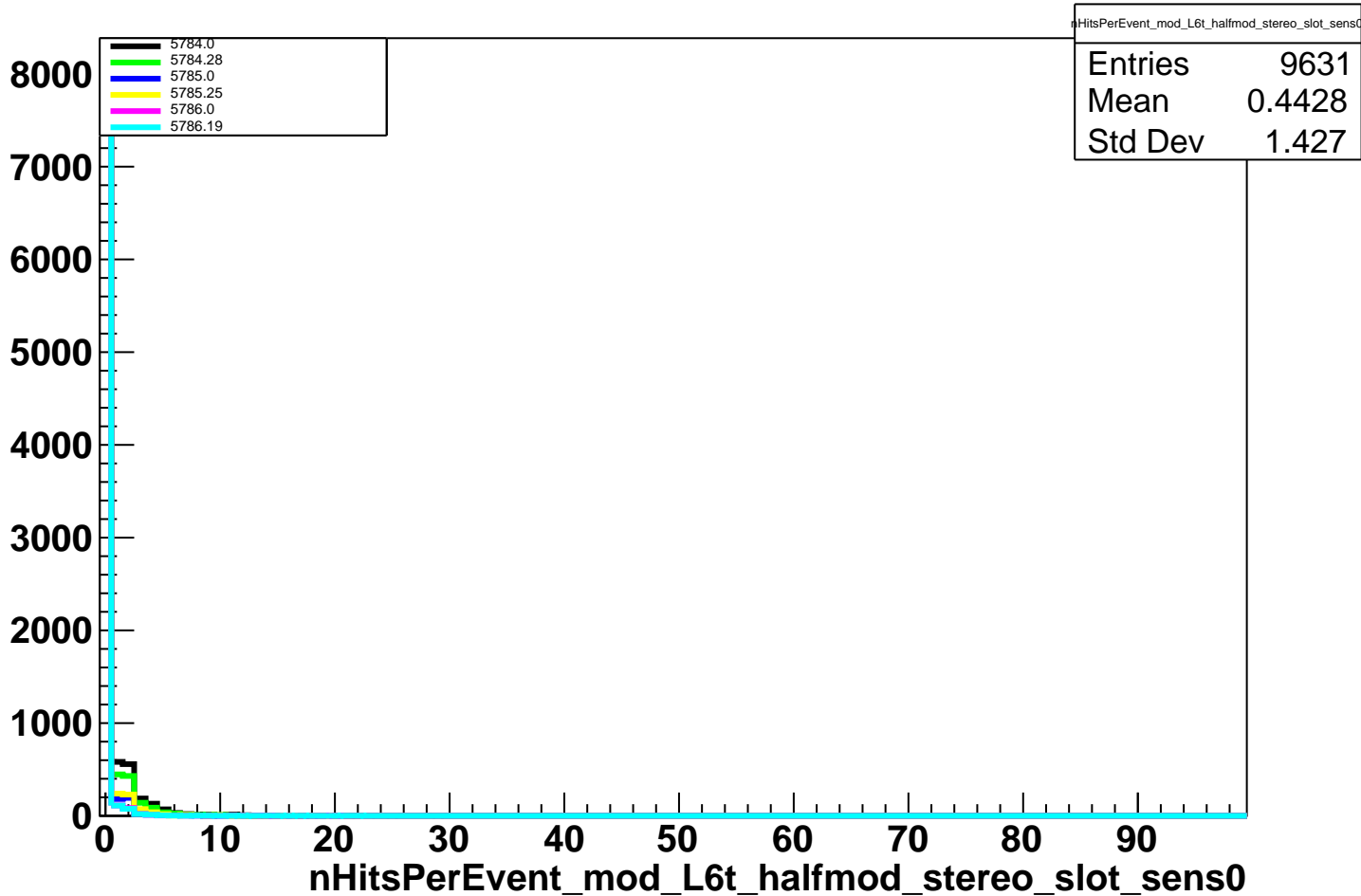
mod_L6t_halfmod_axial_slot_sens0



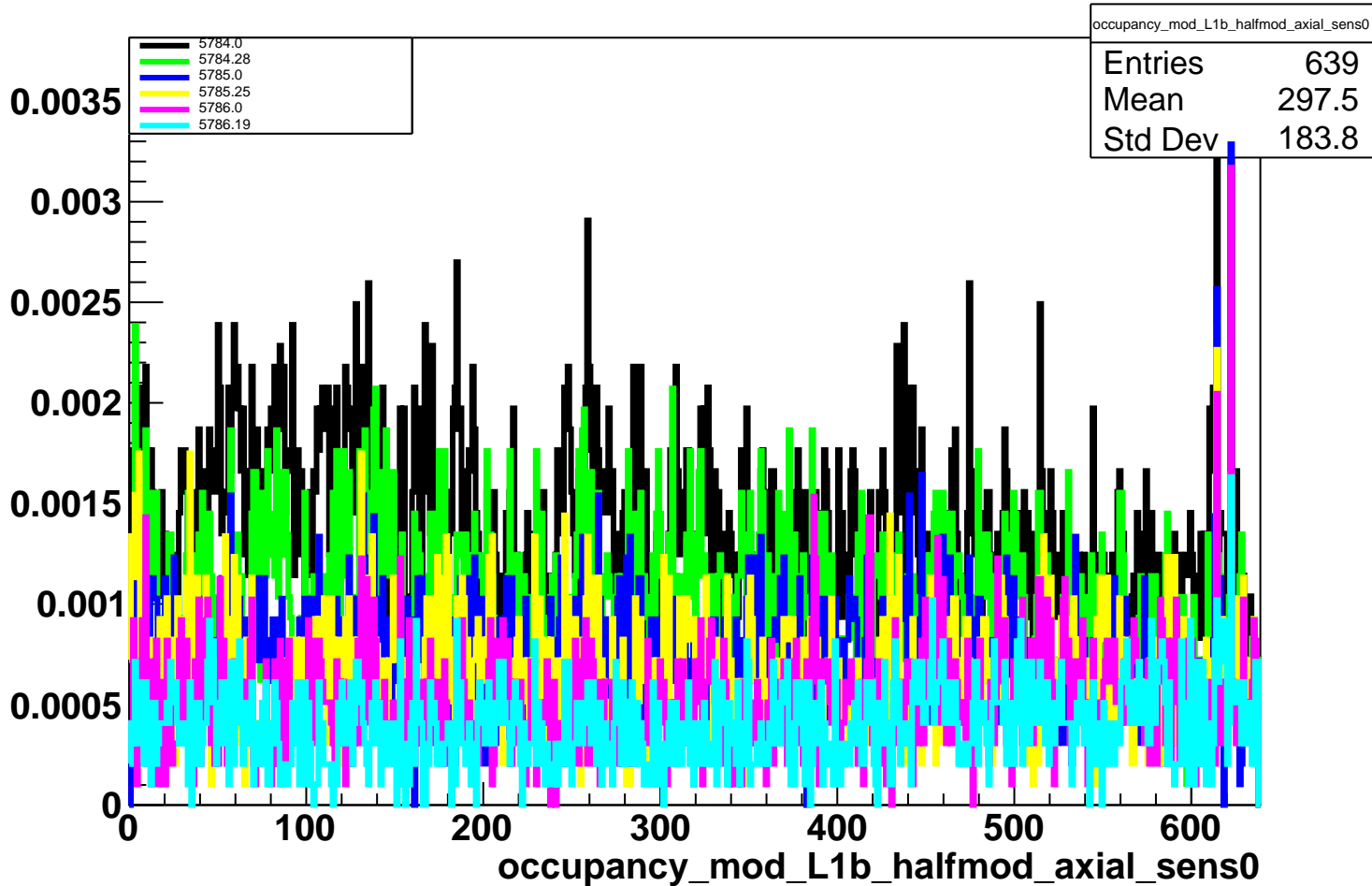
mod_L6t_halfmod_stereo_hole_sens0



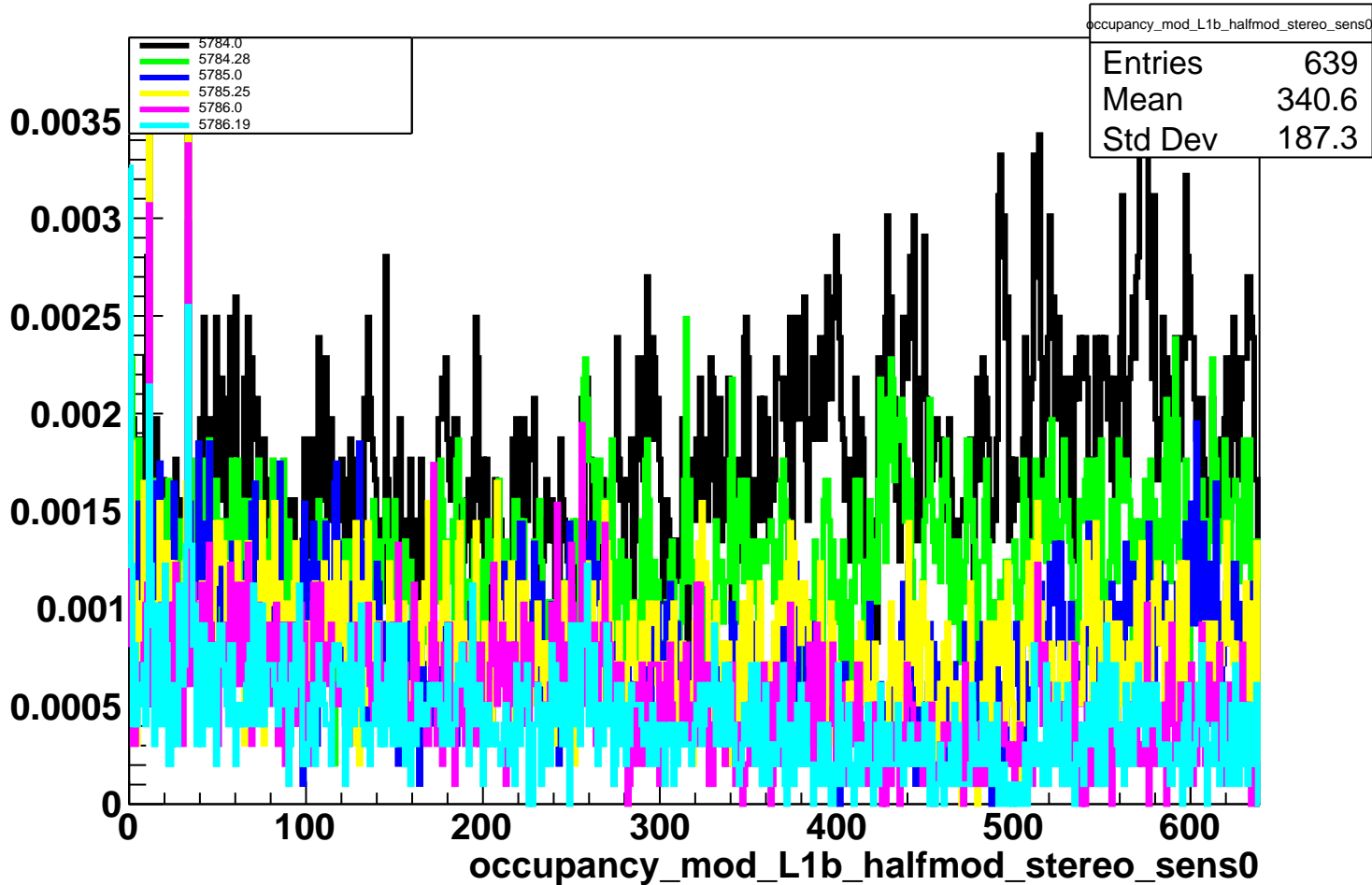
mod_L6t_halfmod_stereo_slot_sens0



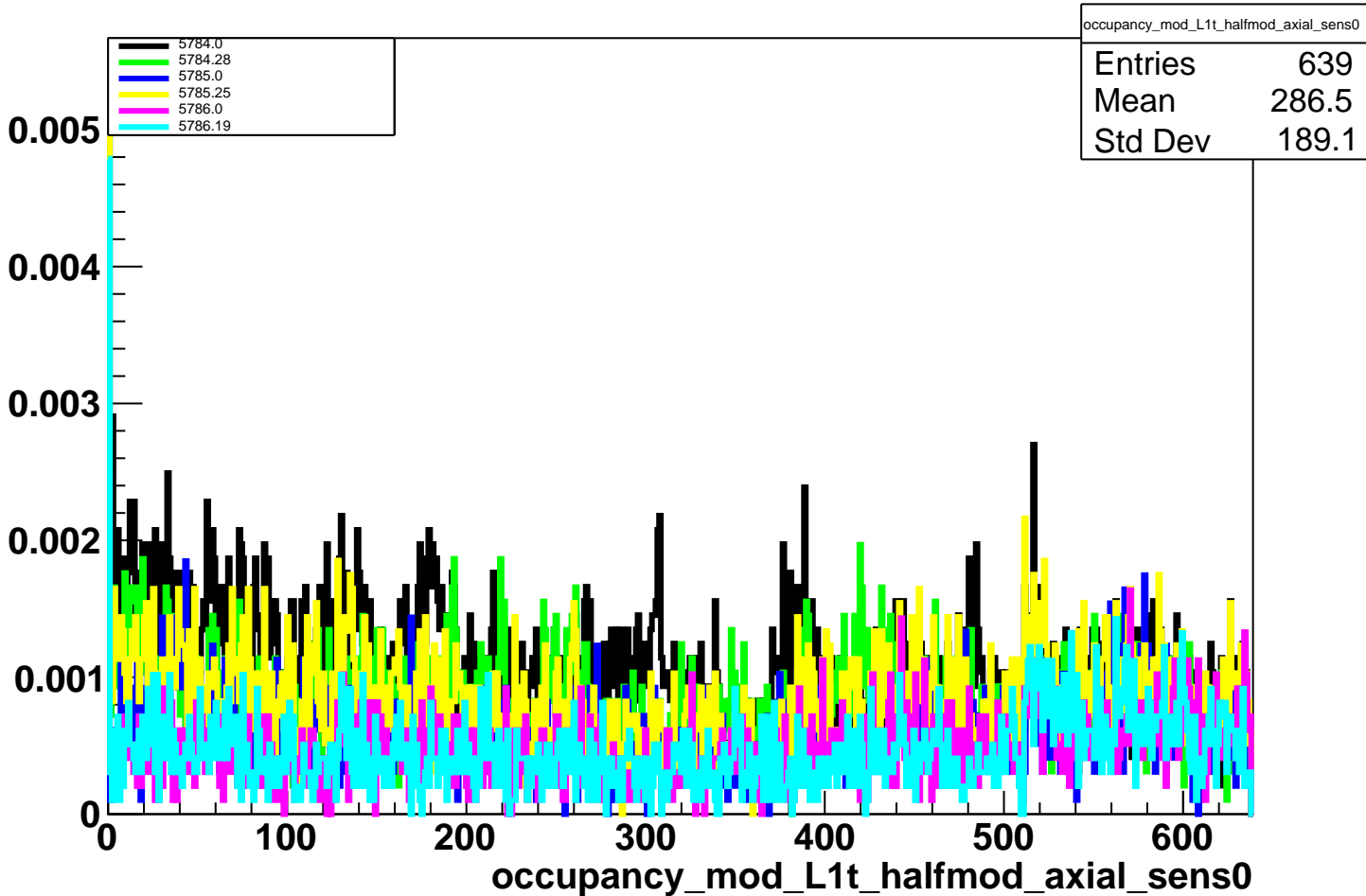
mod_L1b_halfmod_axial_sens0



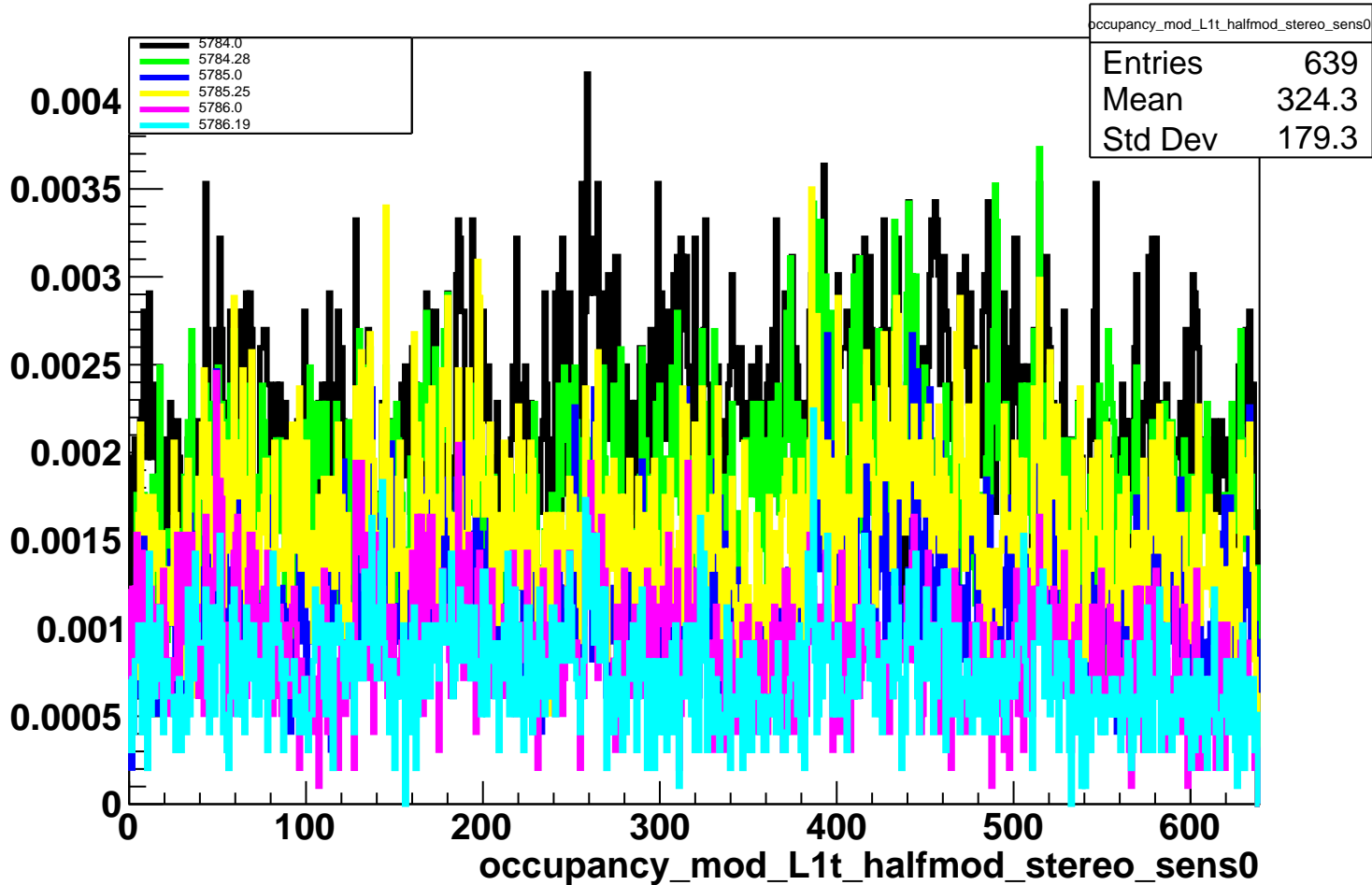
mod_L1b_halfmod_stereo_sens0



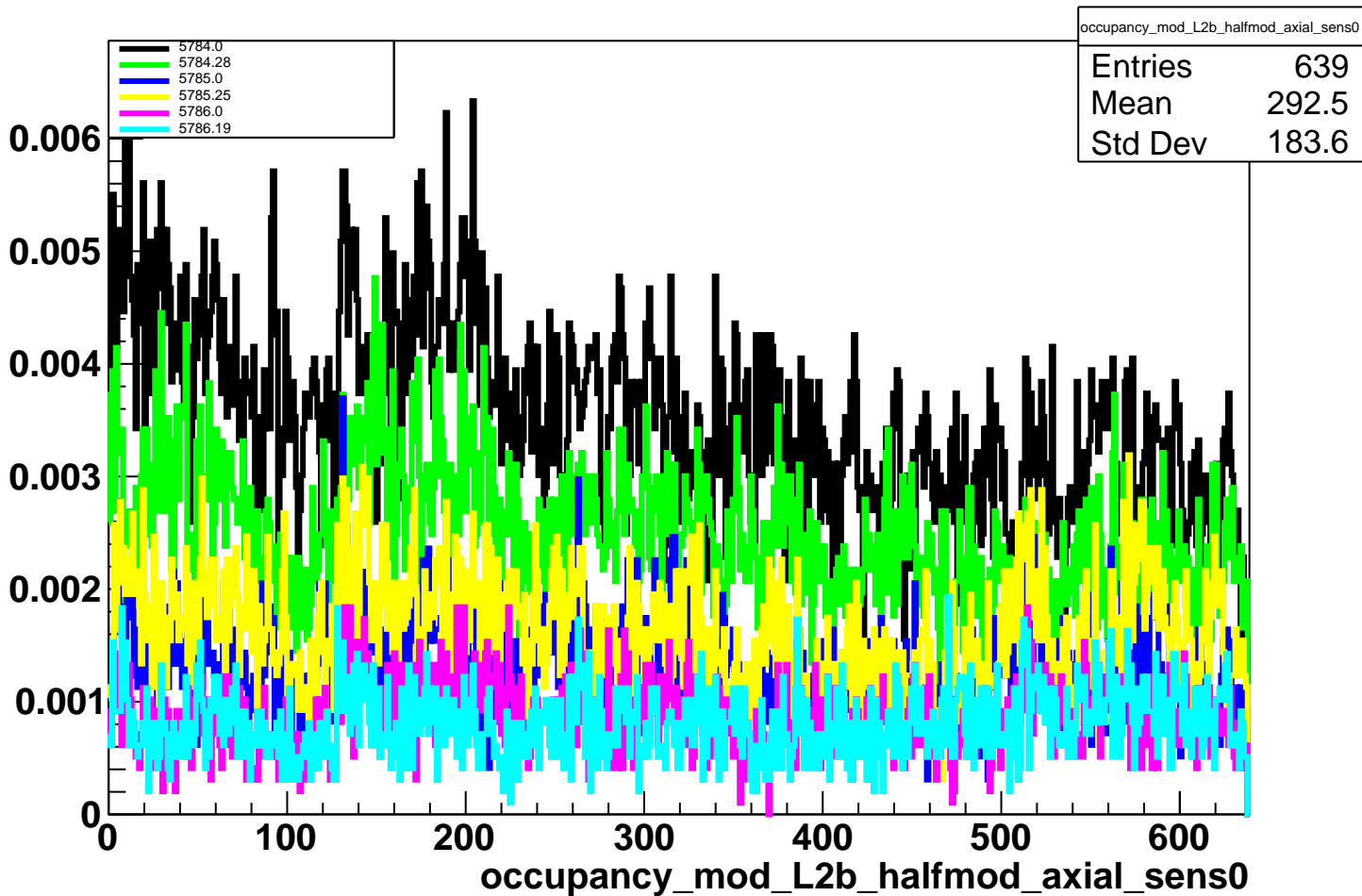
mod_L1t_halfmod_axial_sens0



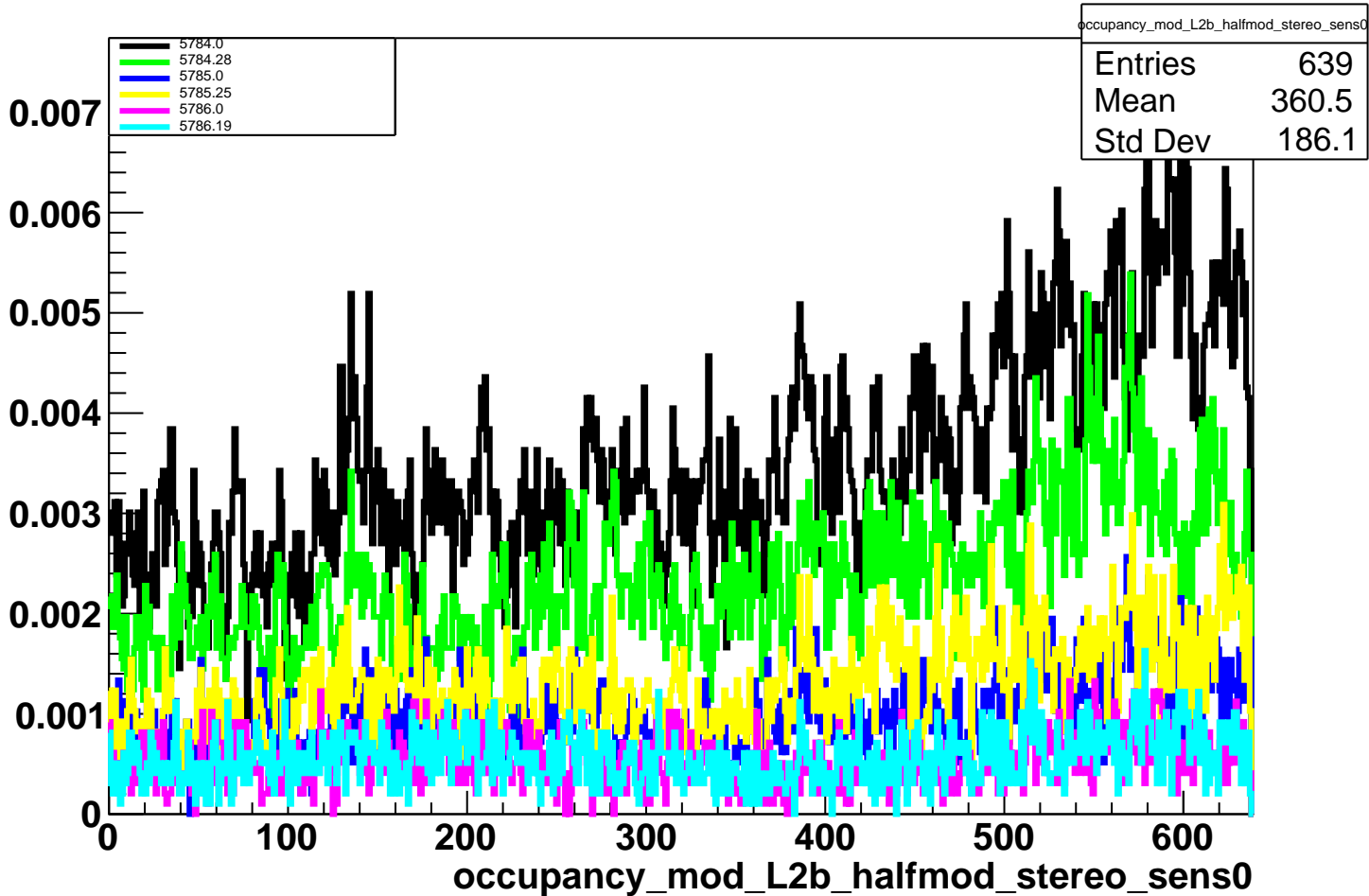
mod_L1t_halfmod_stereo_sens0



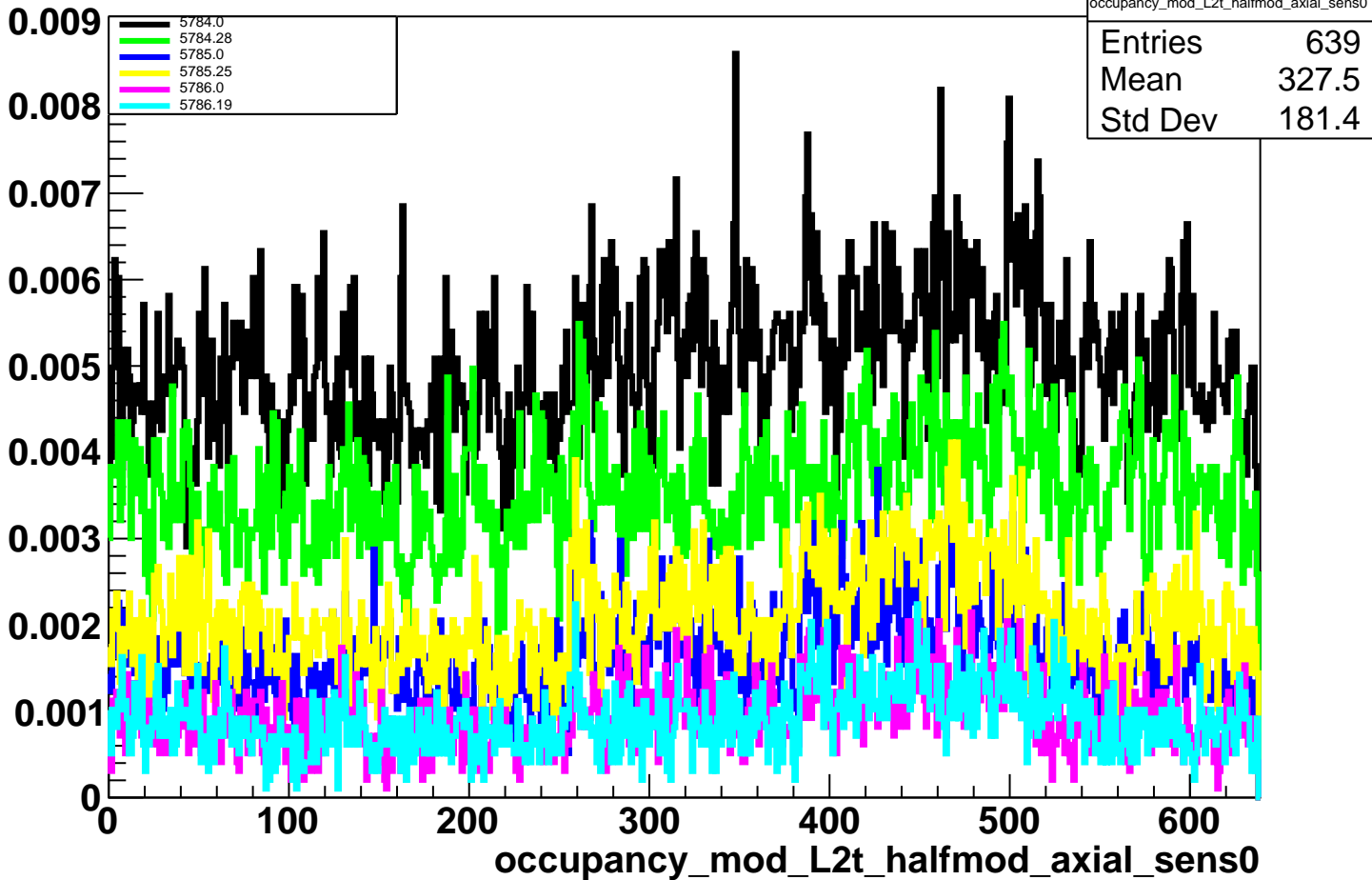
mod_L2b_halfmod_axial_sens0



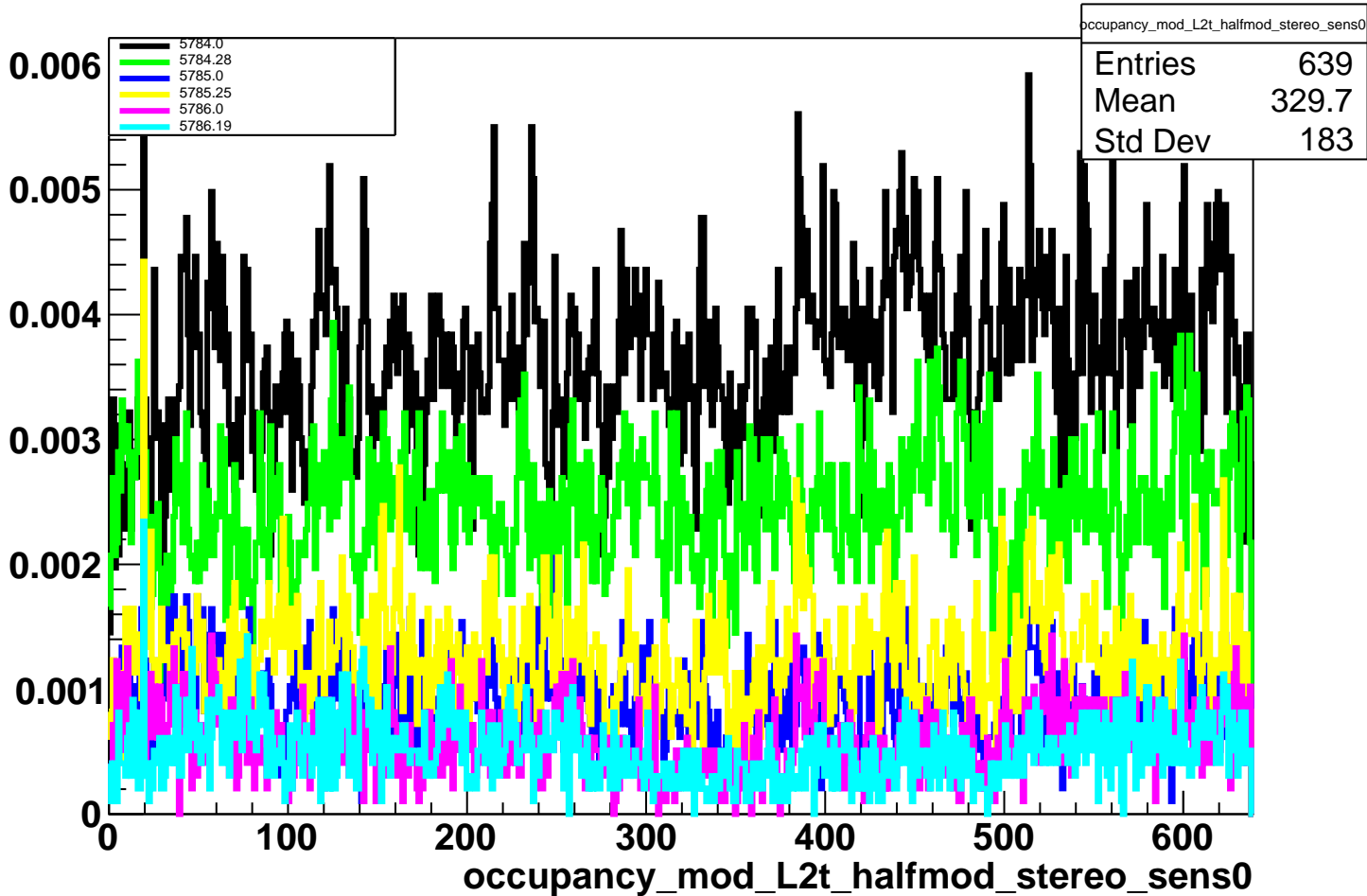
mod_L2b_halfmod_stereo_sens0



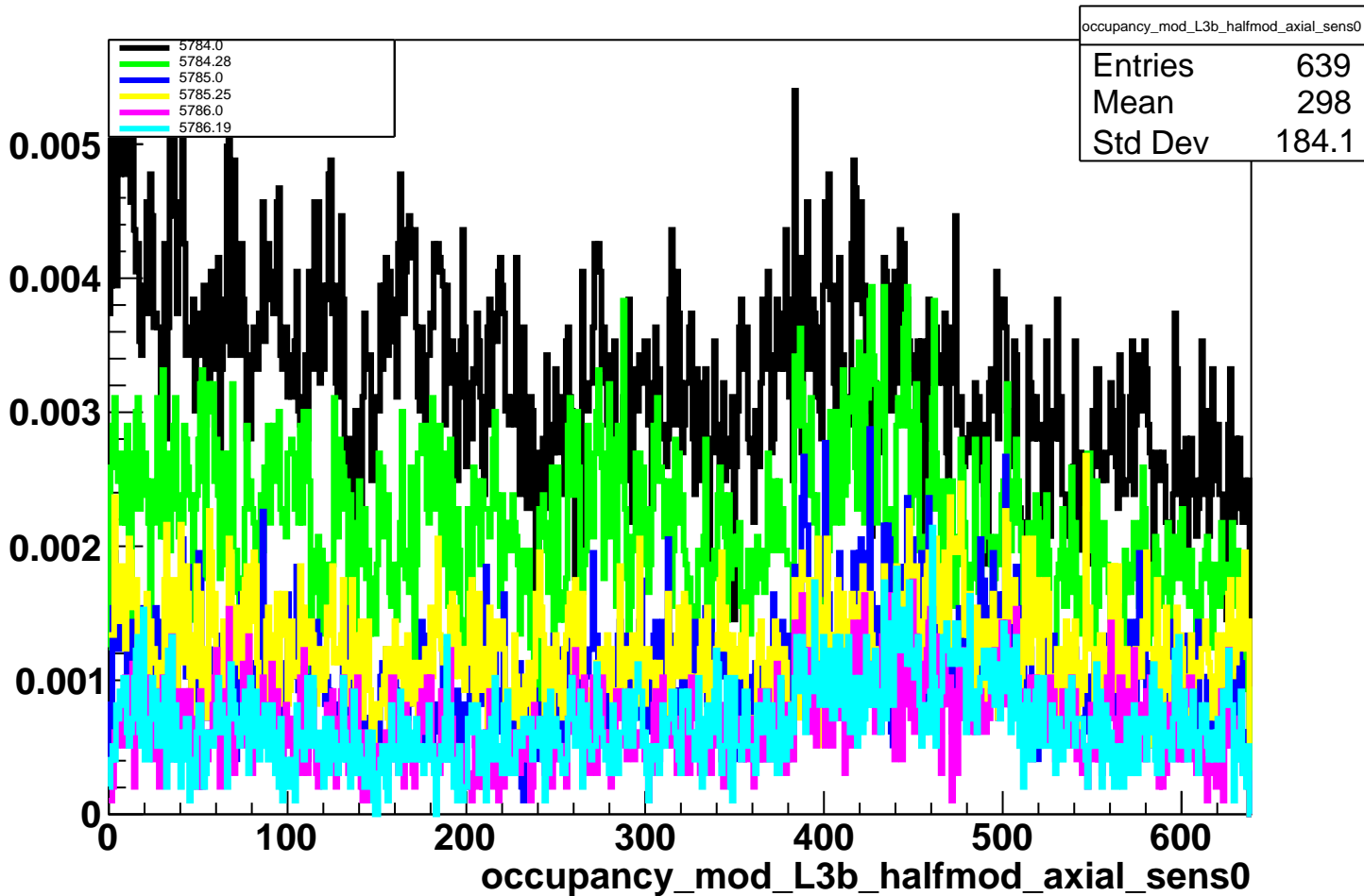
mod_L2t_halfmod_axial_sens0



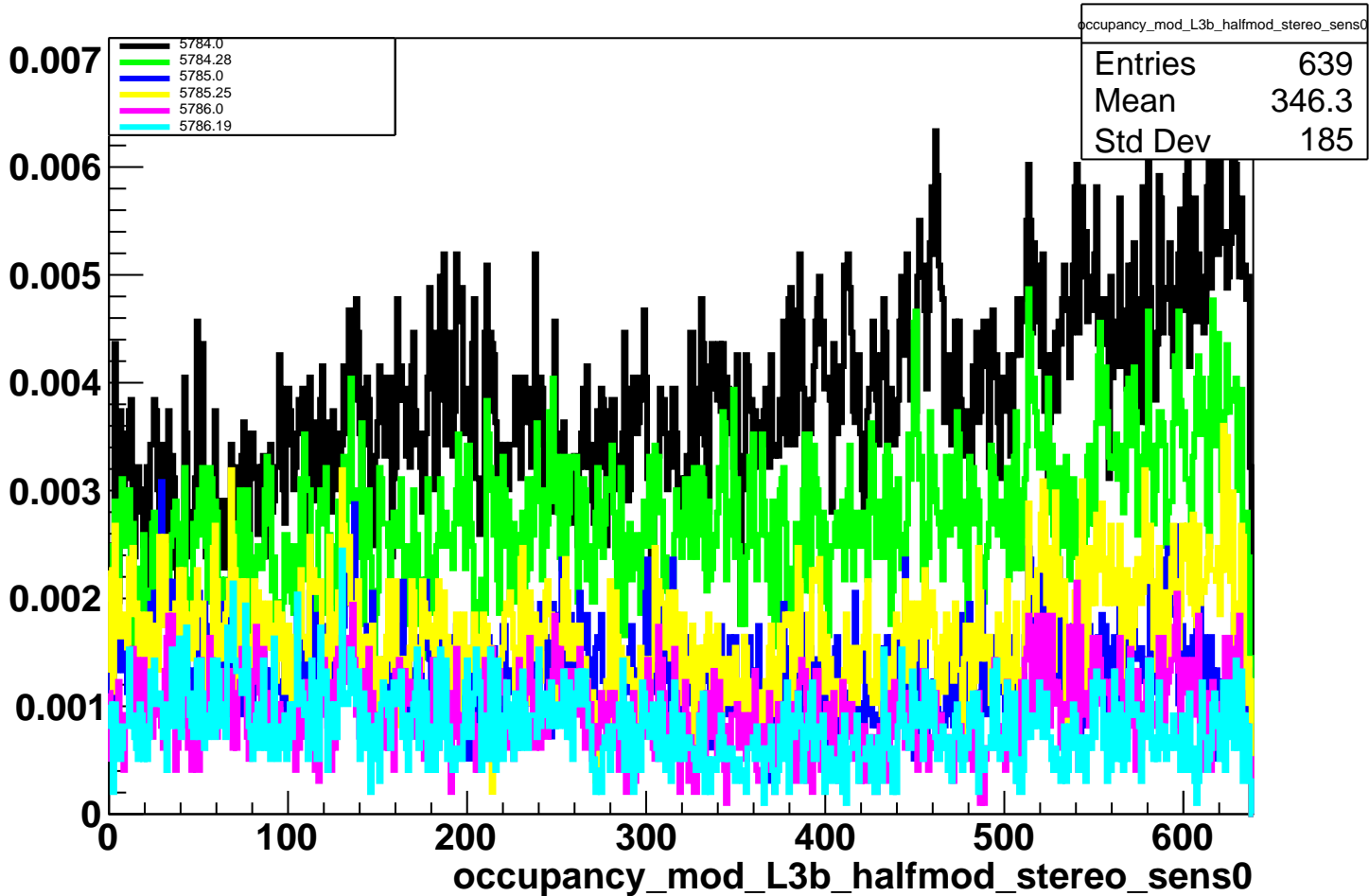
mod_L2t_halfmod_stereo_sens0



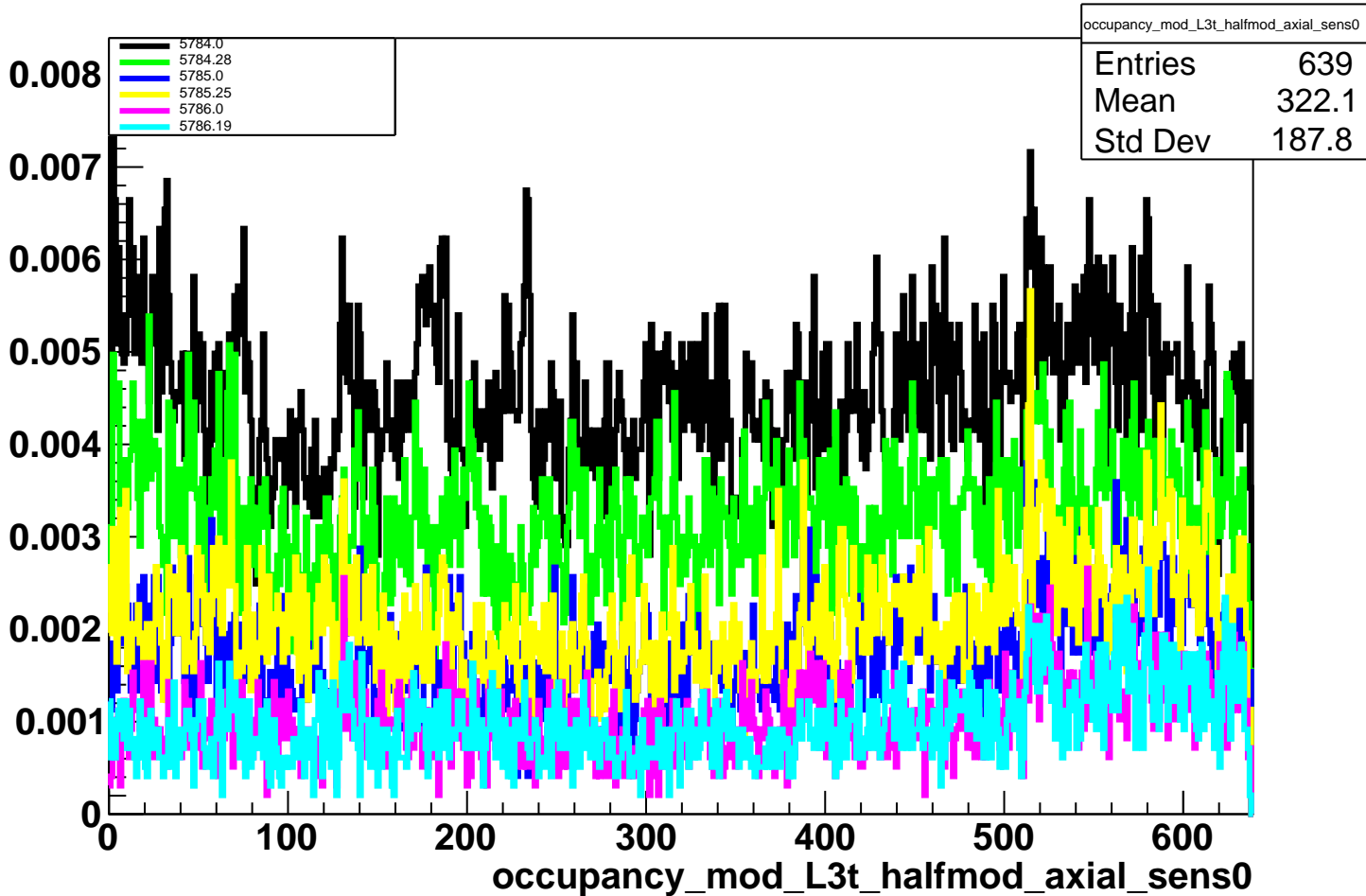
mod_L3b_halfmod_axial_sens0



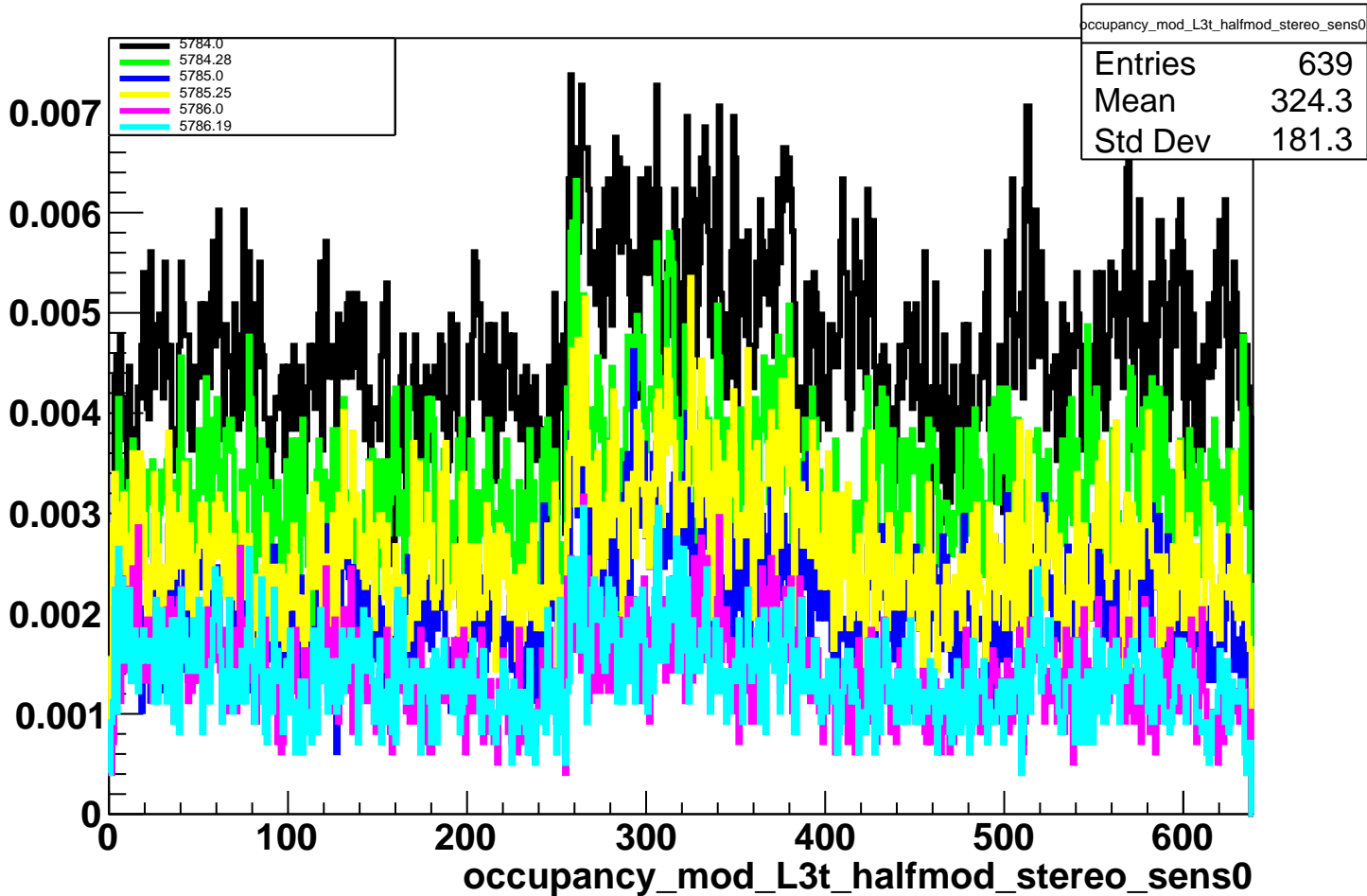
mod_L3b_halfmod_stereo_sens0



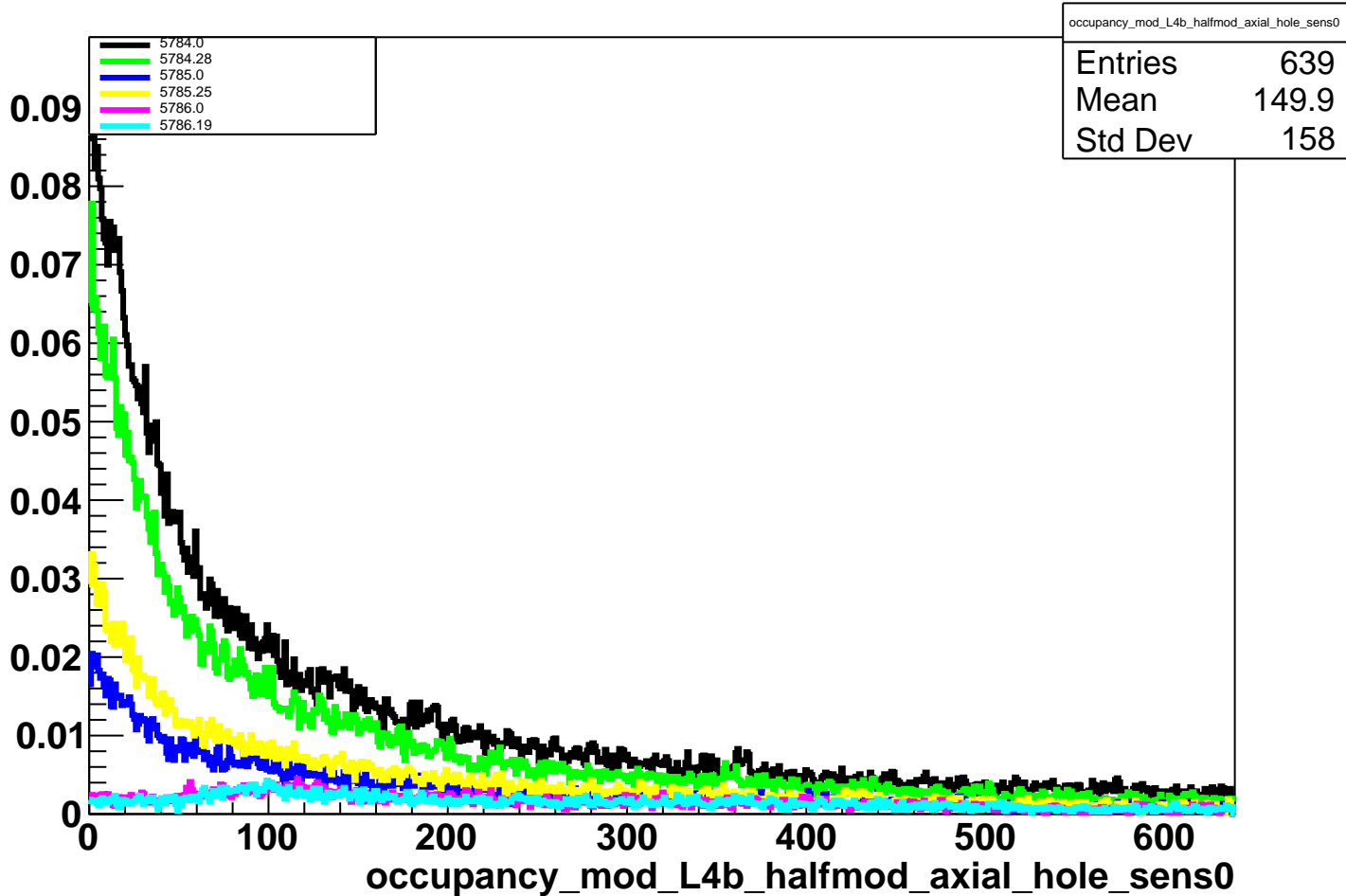
mod_L3t_halfmod_axial_sens0



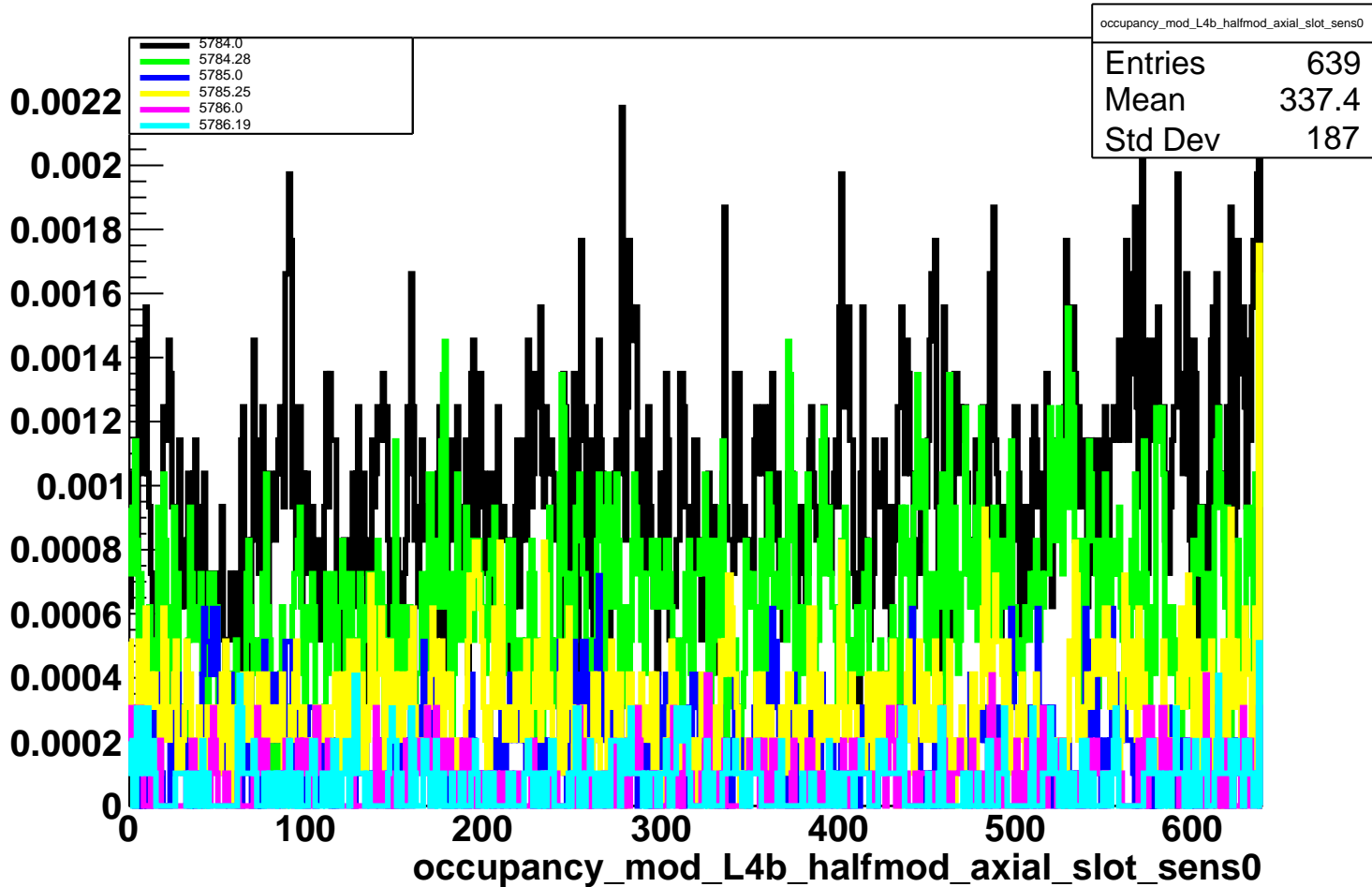
mod_L3t_halfmod_stereo_sens0



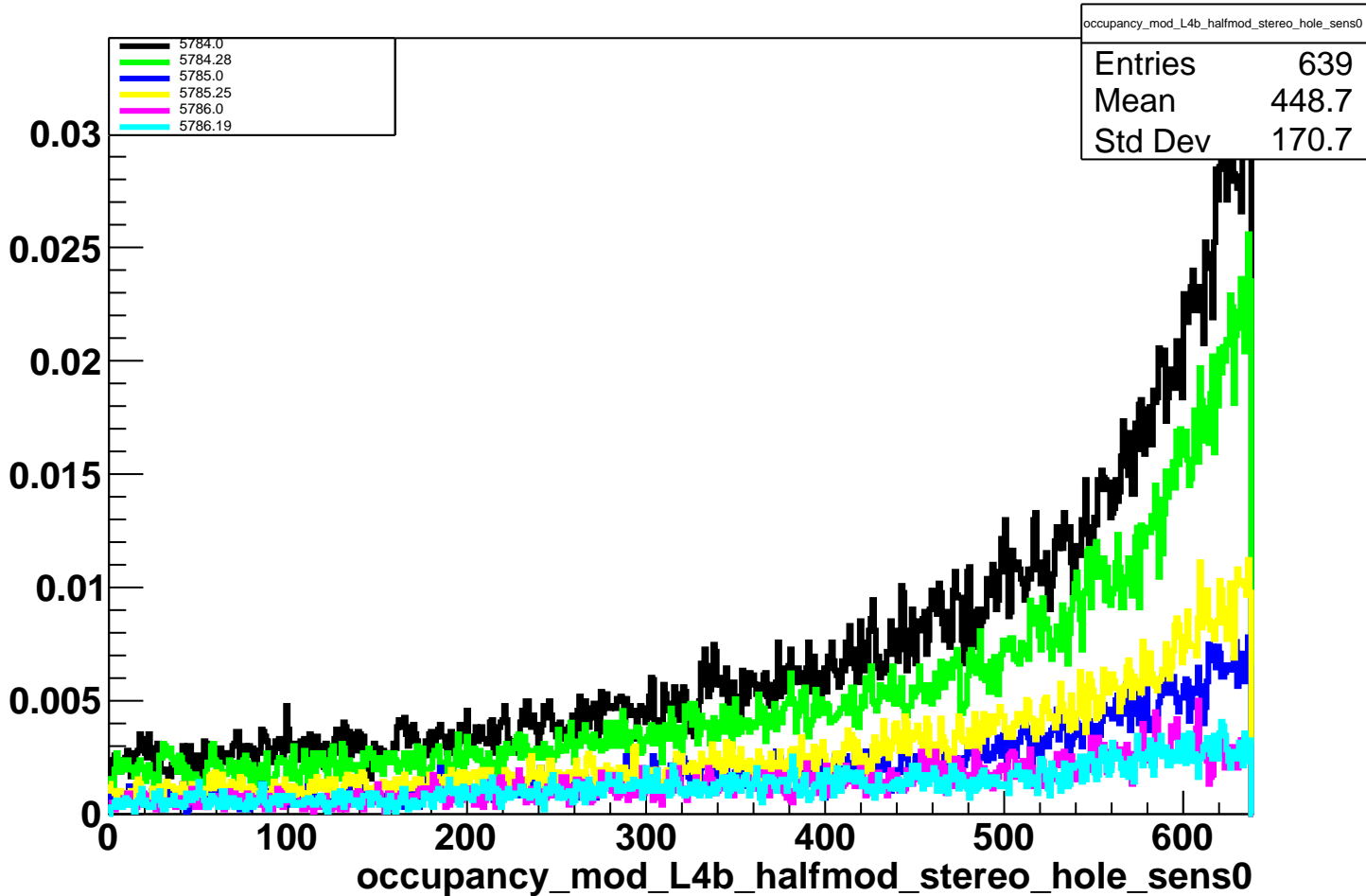
mod_L4b_halfmod_axial_hole_sens0



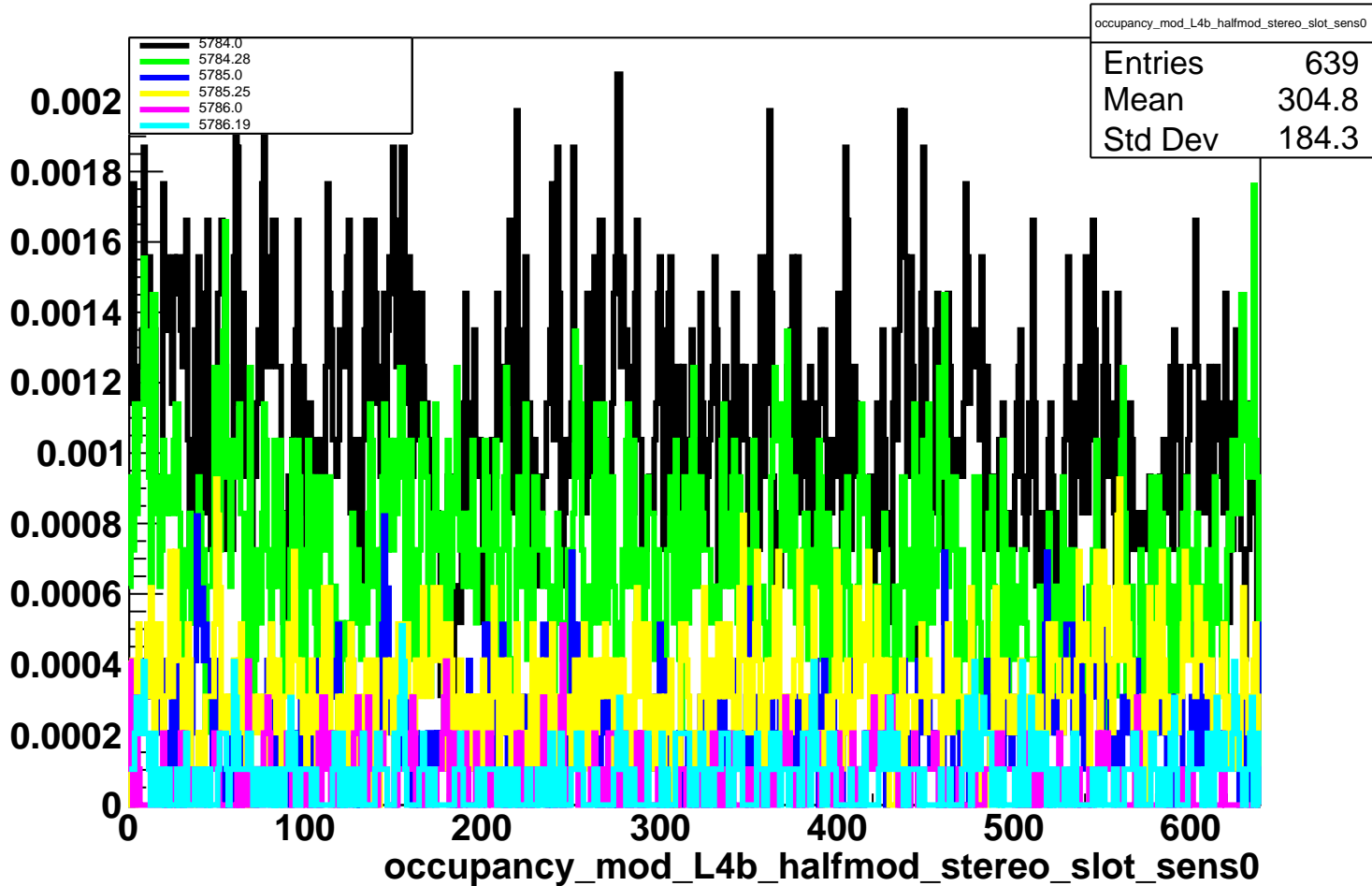
mod_L4b_halfmod_axial_slot_sens0



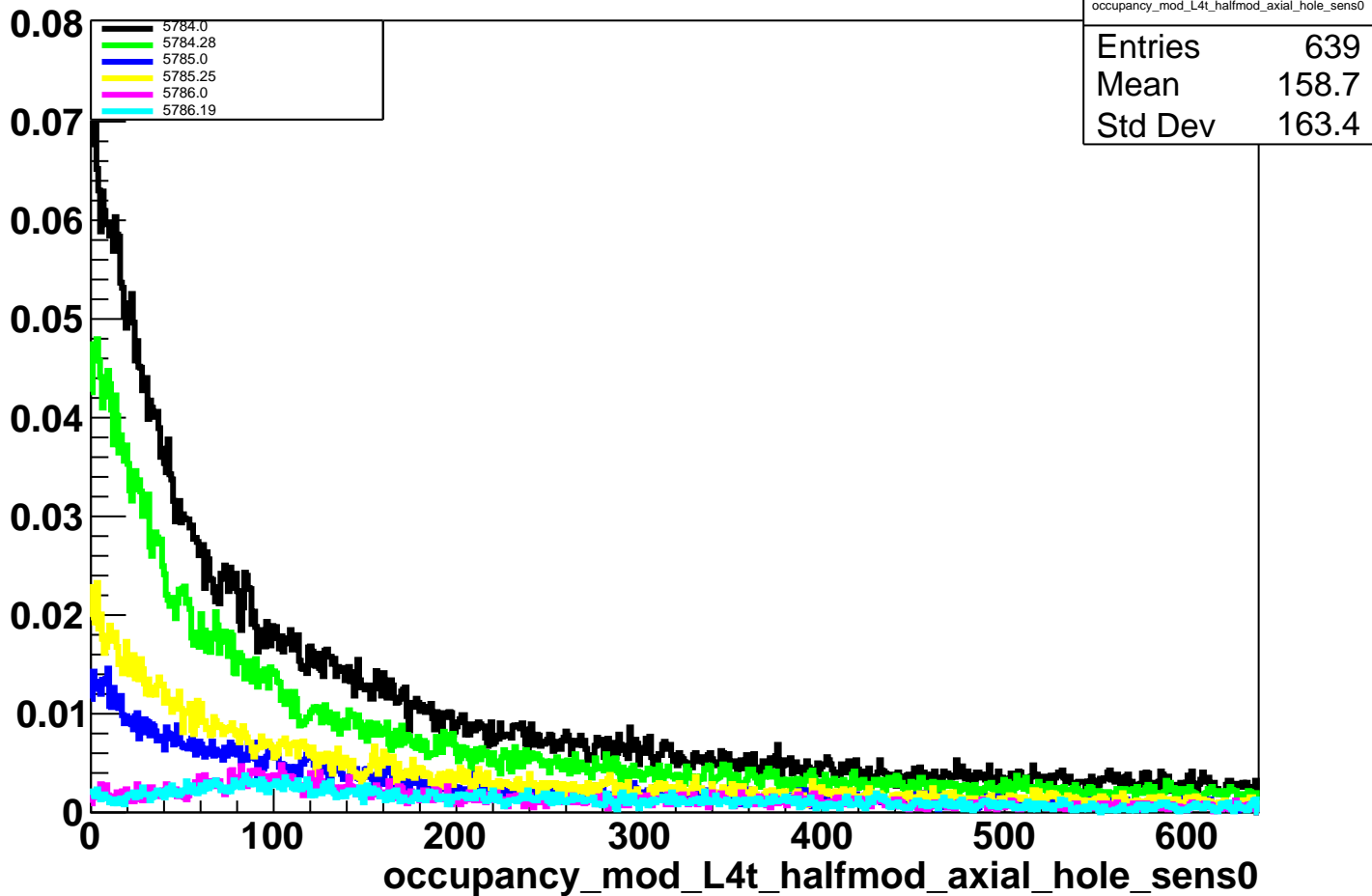
mod_L4b_halfmod_stereo_hole_sens0



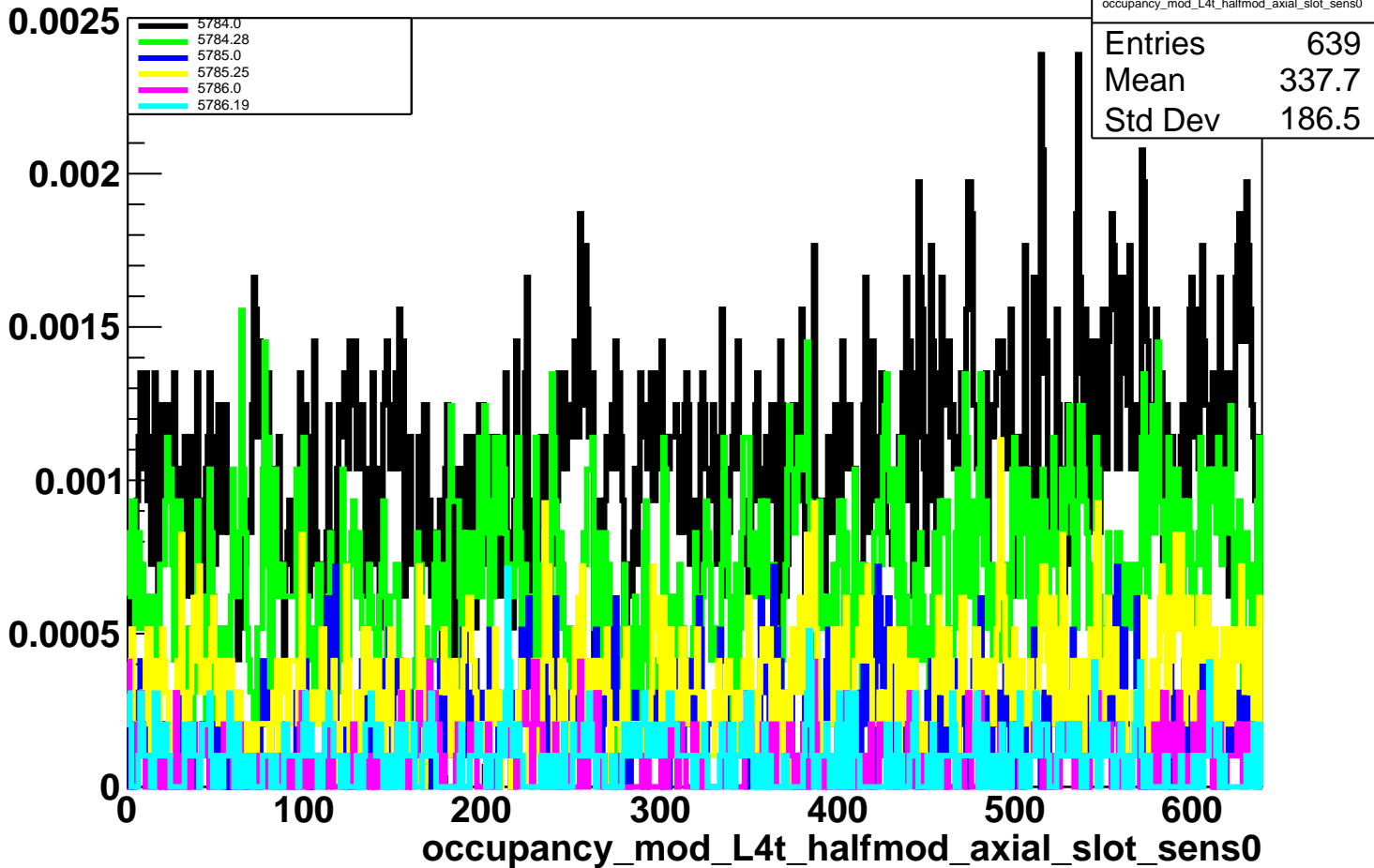
mod_L4b_halfmod_stereo_slot_sens0



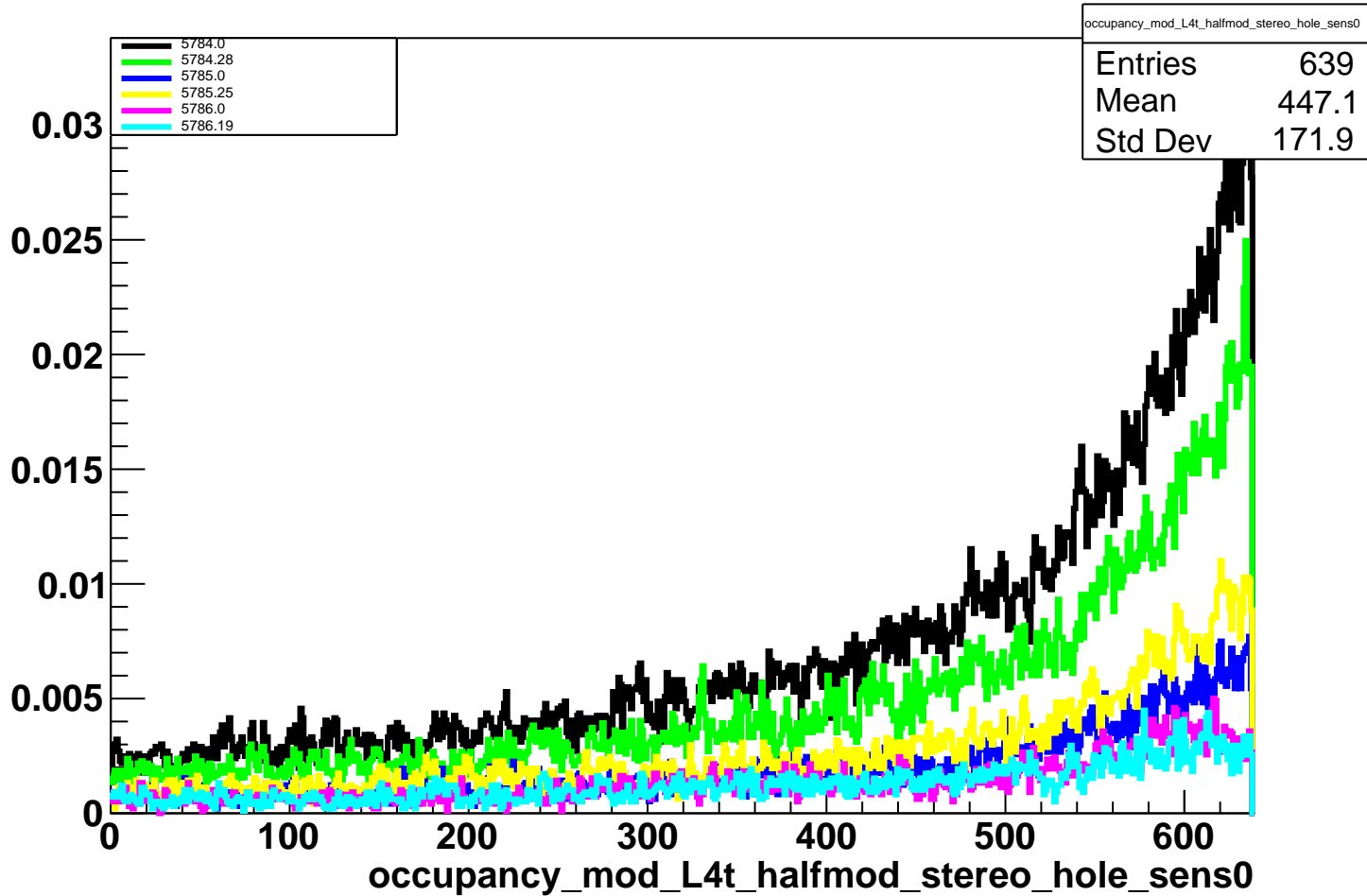
mod_L4t_halfmod_axial_hole_sens0



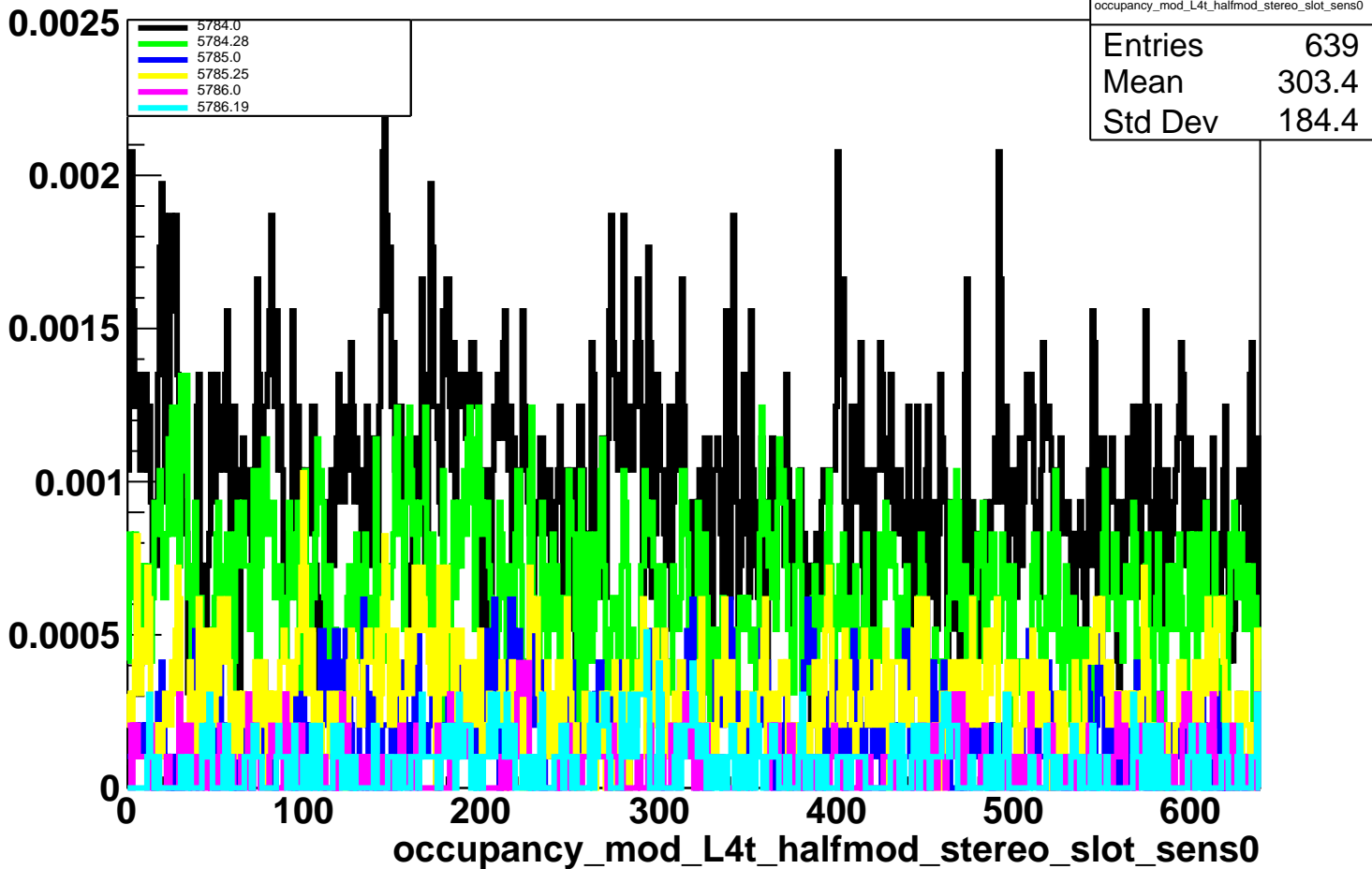
mod_L4t_halfmod_axial_slot_sens0



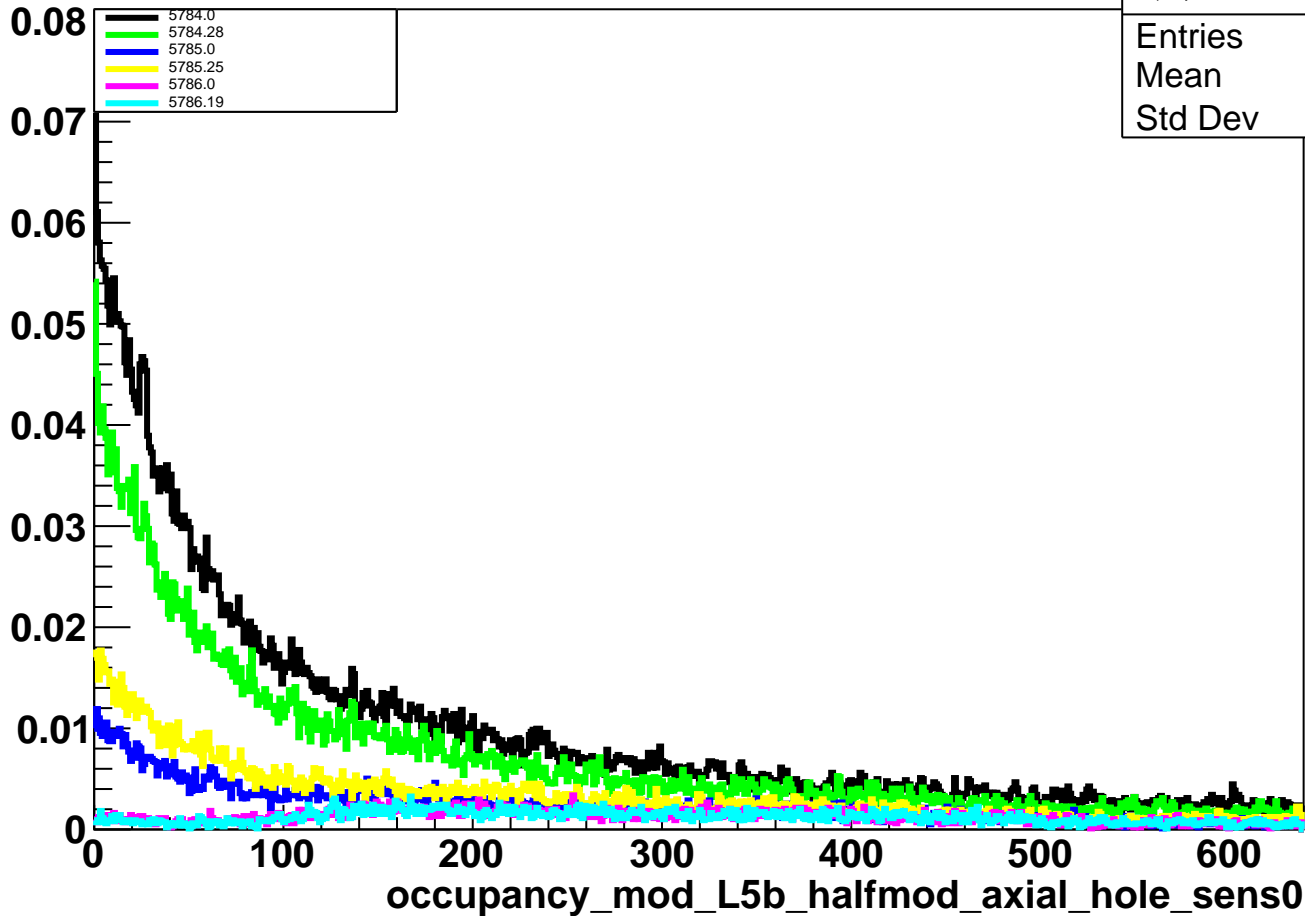
mod_L4t_halfmod_stereo_hole_sens0



mod_L4t_halfmod_stereo_slot_sens0



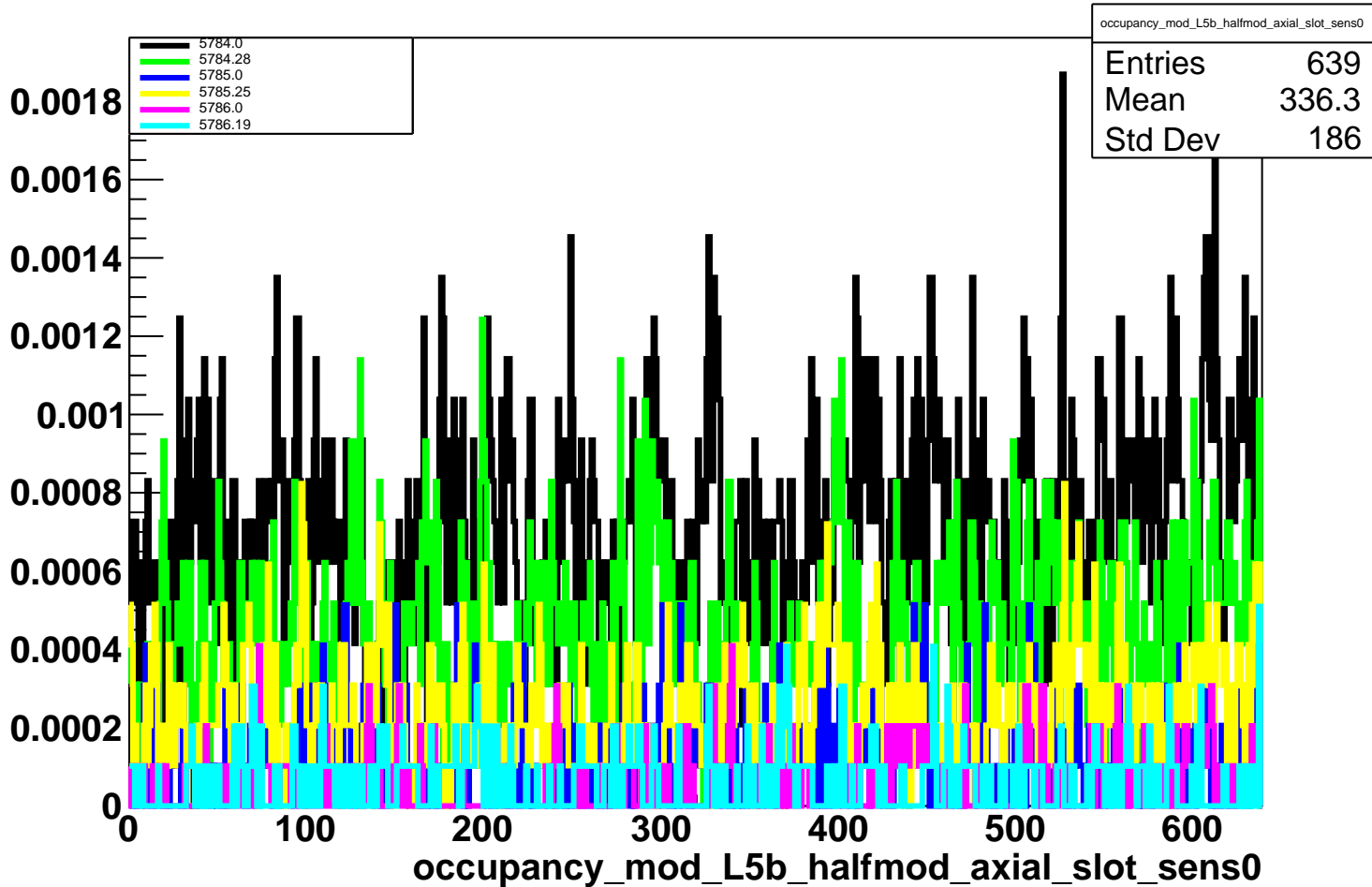
mod_L5b_halfmod_axial_hole_sens0



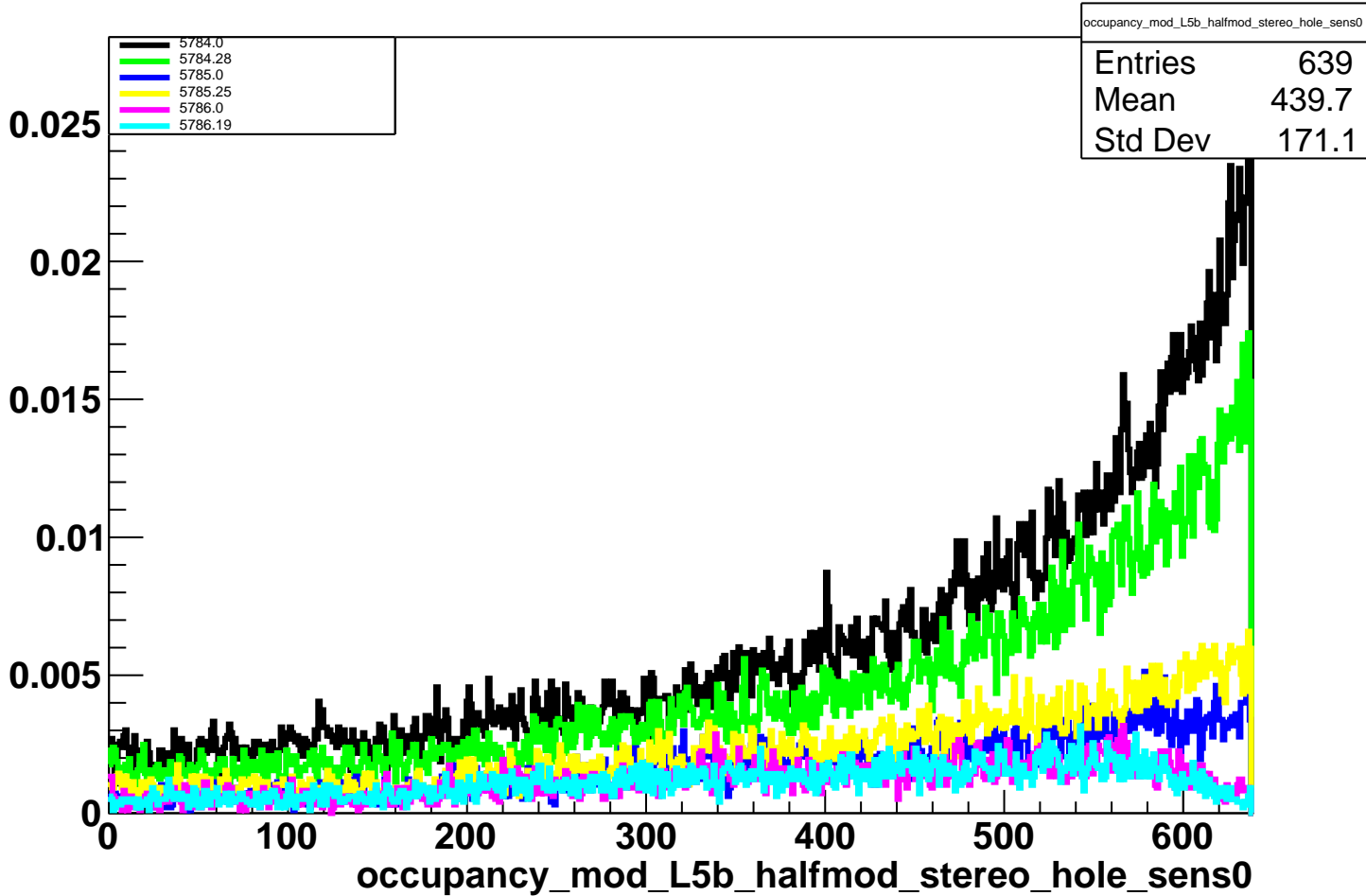
occupancy_mod_L5b_halfmod_axial_hole_sens0

Entries	639
Mean	161.1
Std Dev	161.1

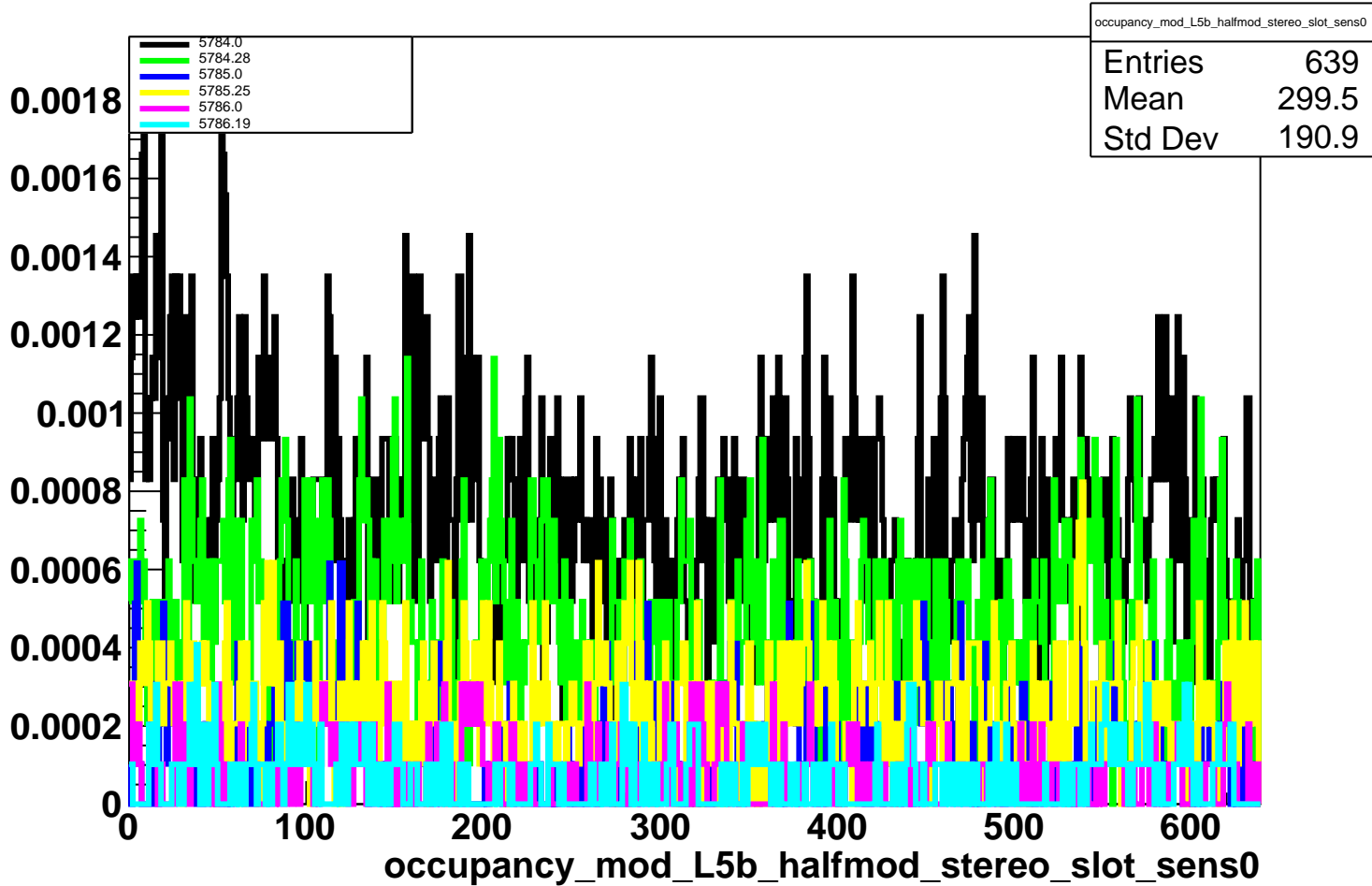
mod_L5b_halfmod_axial_slot_sens0



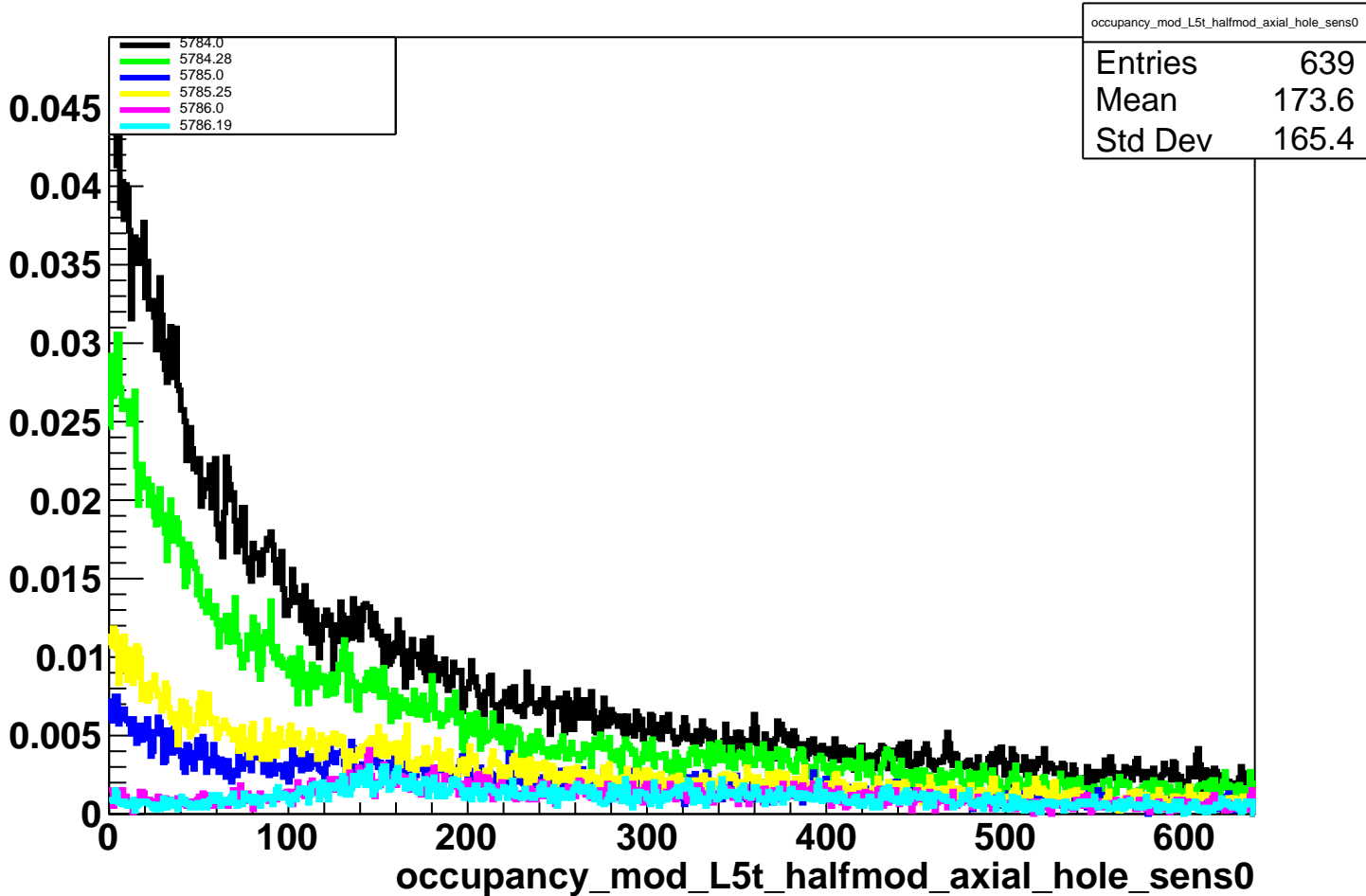
mod_L5b_halfmod_stereo_hole_sens0



mod_L5b_halfmod_stereo_slot_sens0

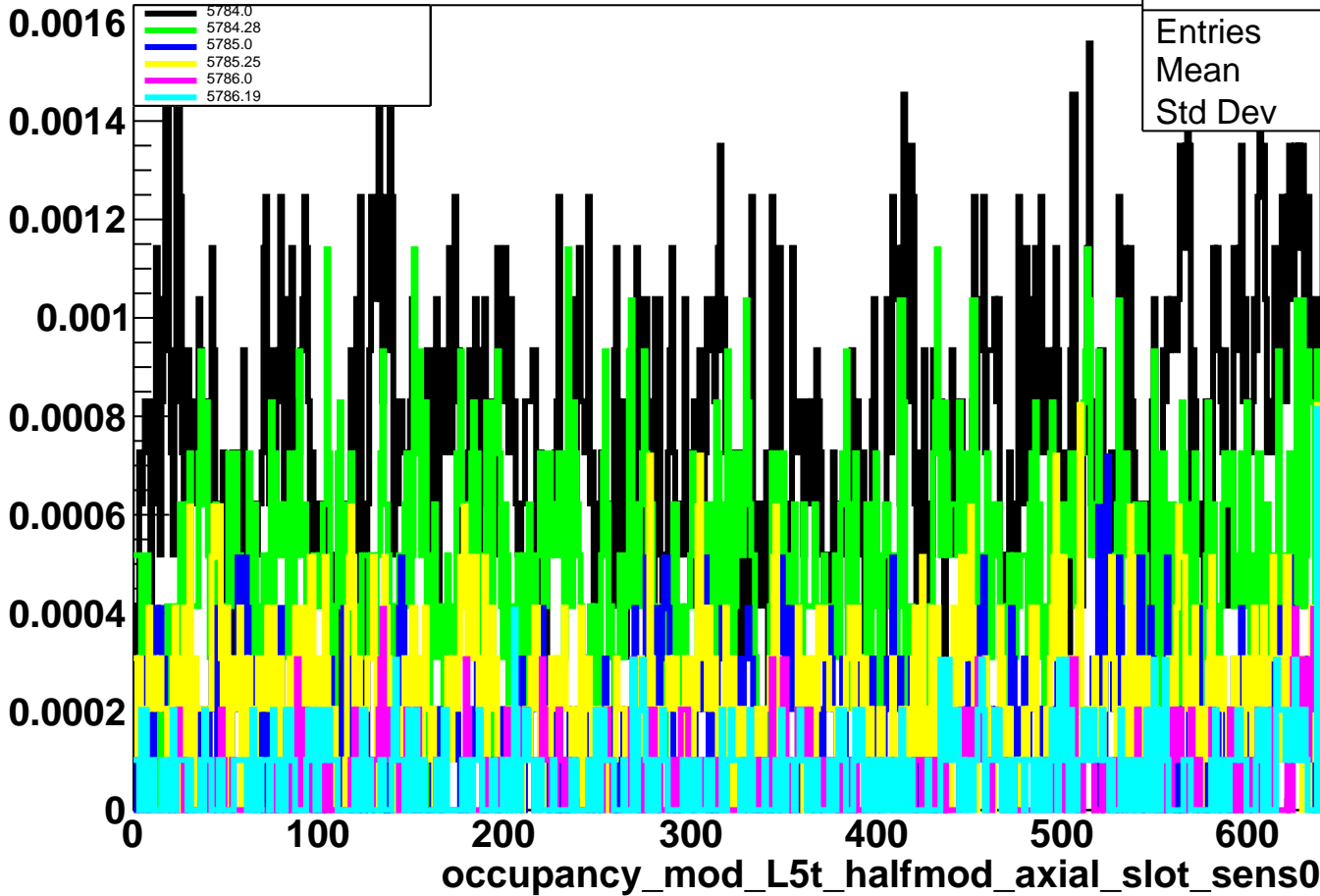


mod_L5t_halfmod_axial_hole_sens0

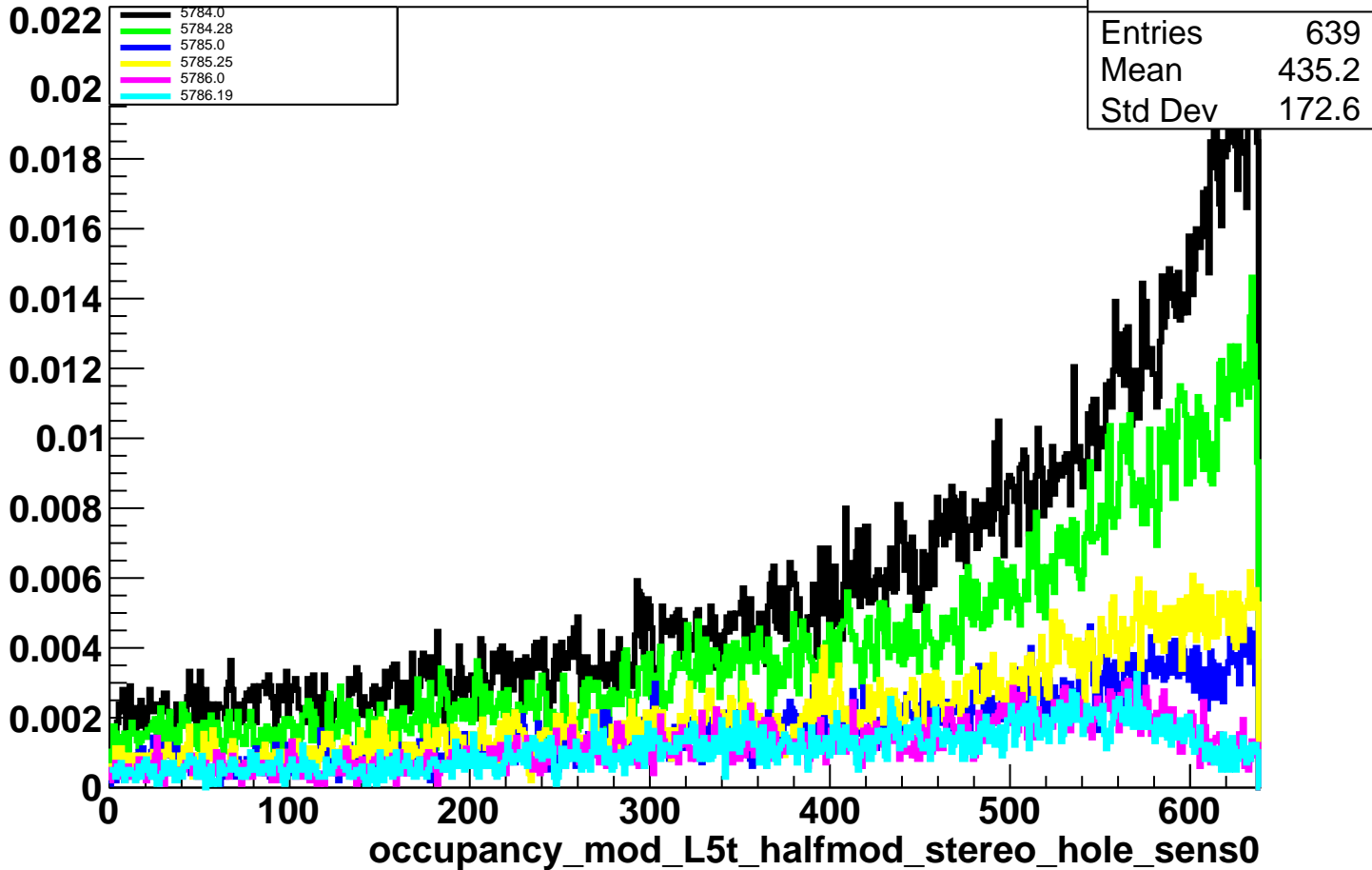


mod_L5t_halfmod_axial_slot_sens0

occupancy_mod_L5t_halfmod_axial_slot_sens0	
Entries	639
Mean	329.6
Std Dev	190



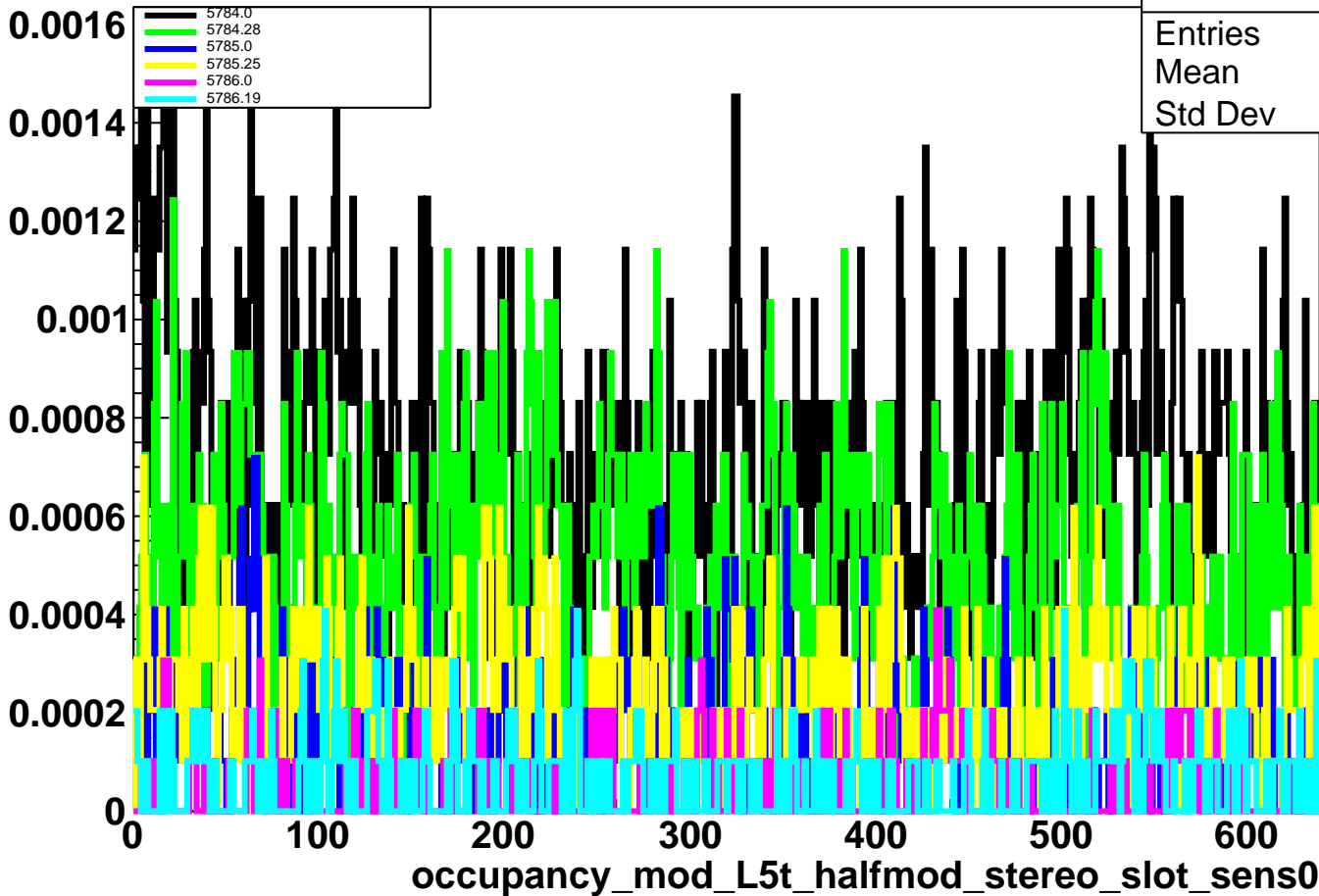
mod_L5t_halfmod_stereo_hole_sens0



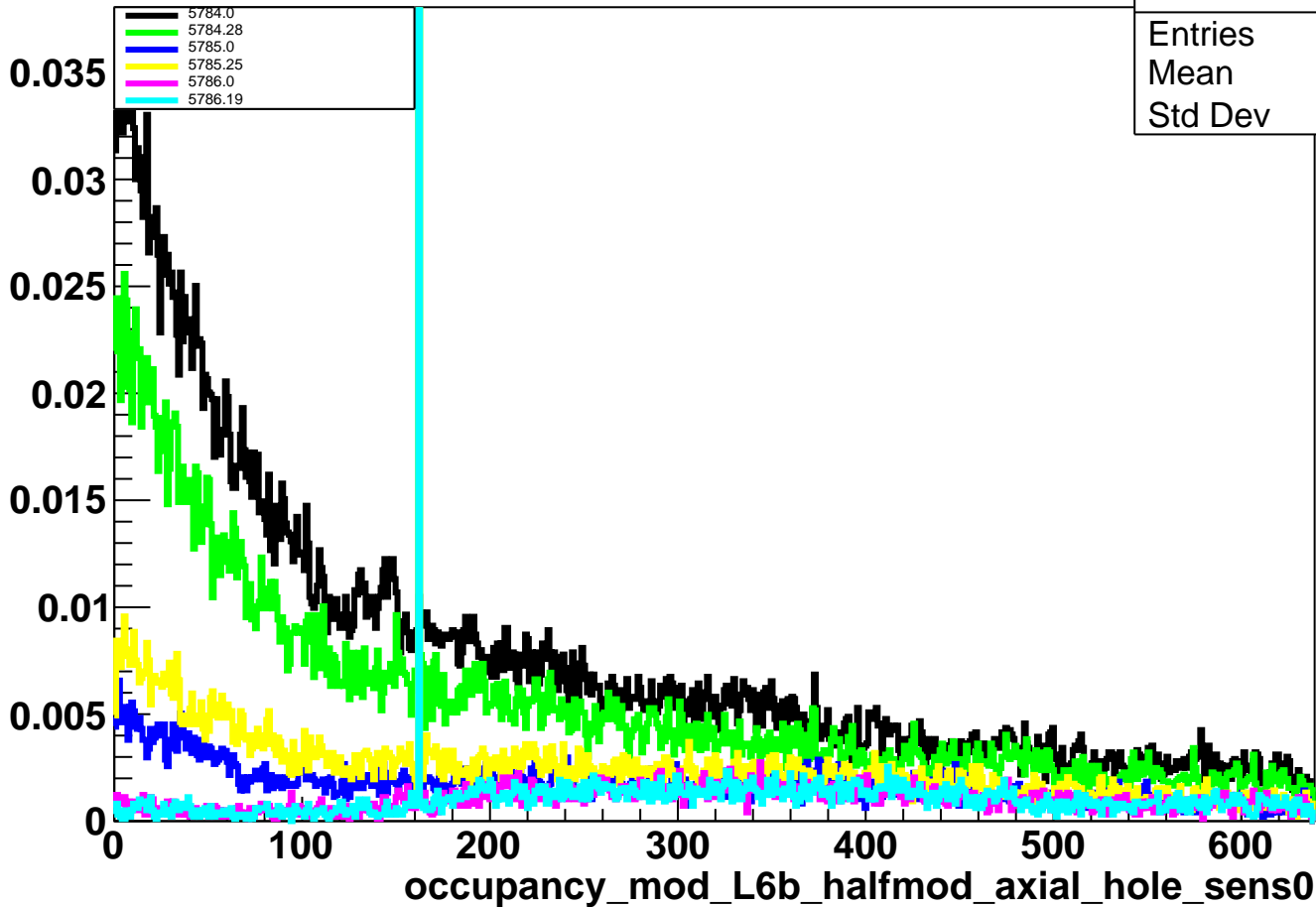
mod_L5t_halfmod_stereo_slot_sens0

occupancy_mod_L5t_halfmod_stereo_slot_sens0

Entries	639
Mean	308.9
Std Dev	191.2

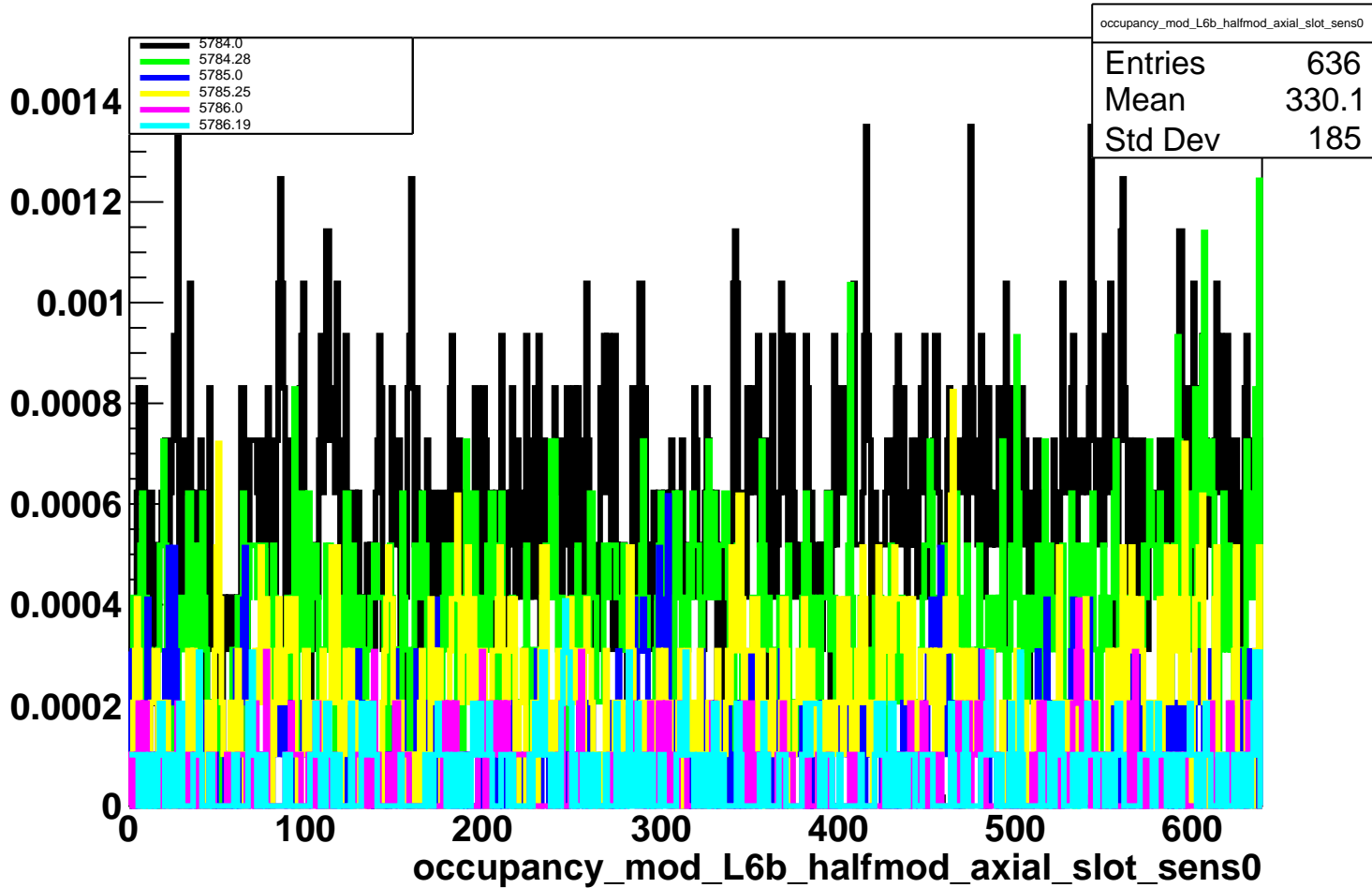


mod_L6b_halfmod_axial_hole_sens0

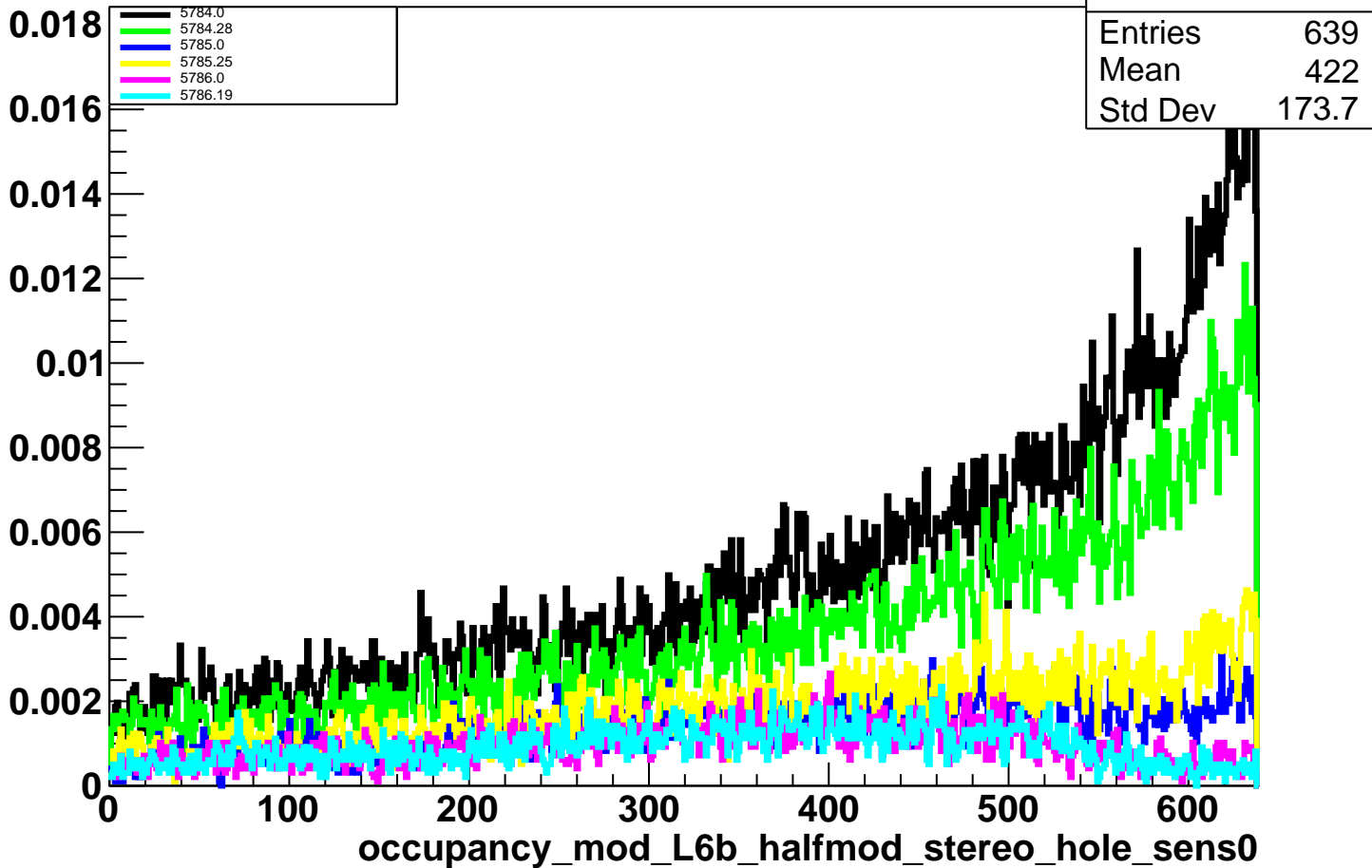


occupancy_mod_L6b_halfmod_axial_hole_sens0	
Entries	639
Mean	186.7
Std Dev	169.3

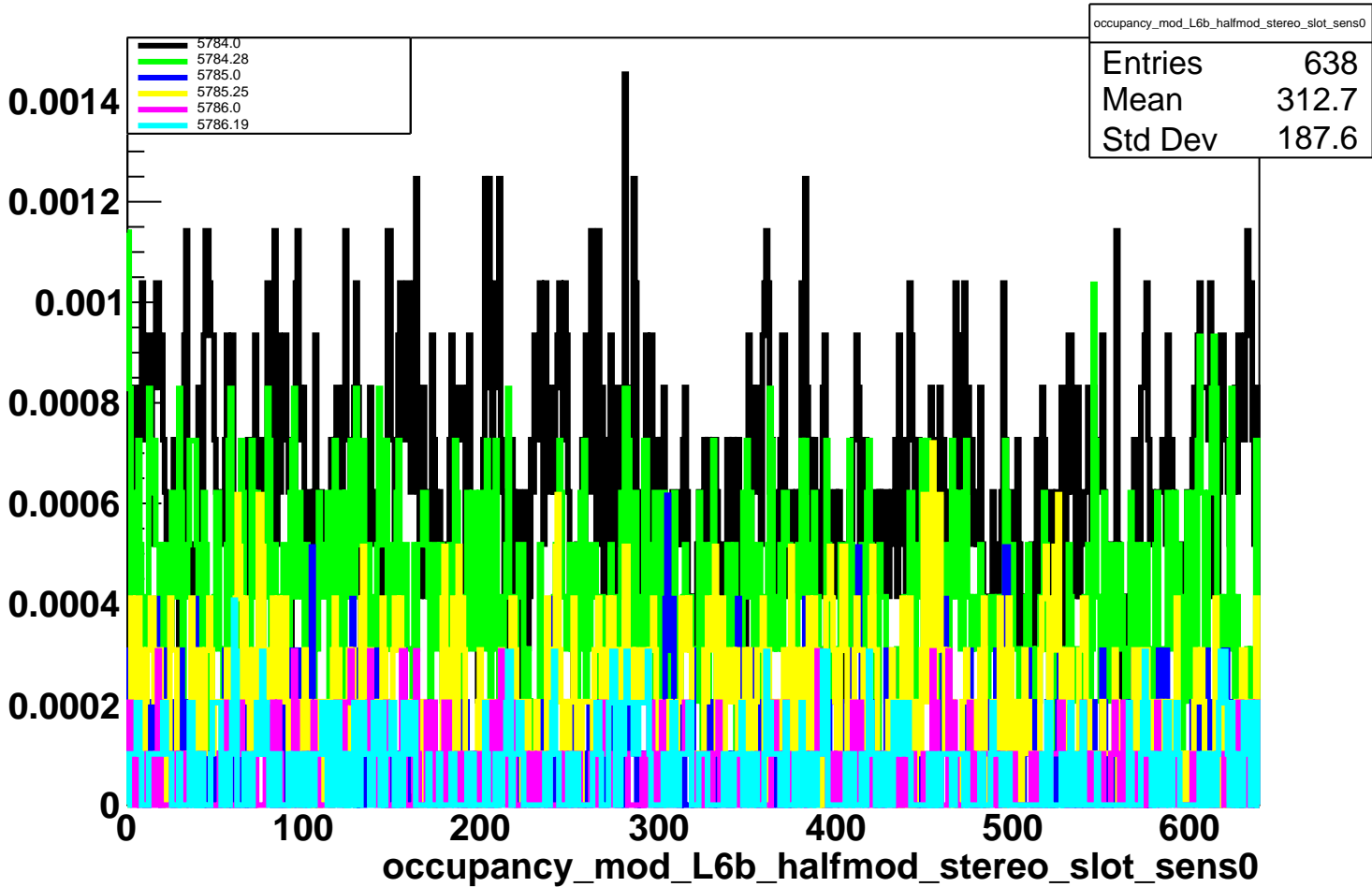
mod_L6b_halfmod_axial_slot_sens0



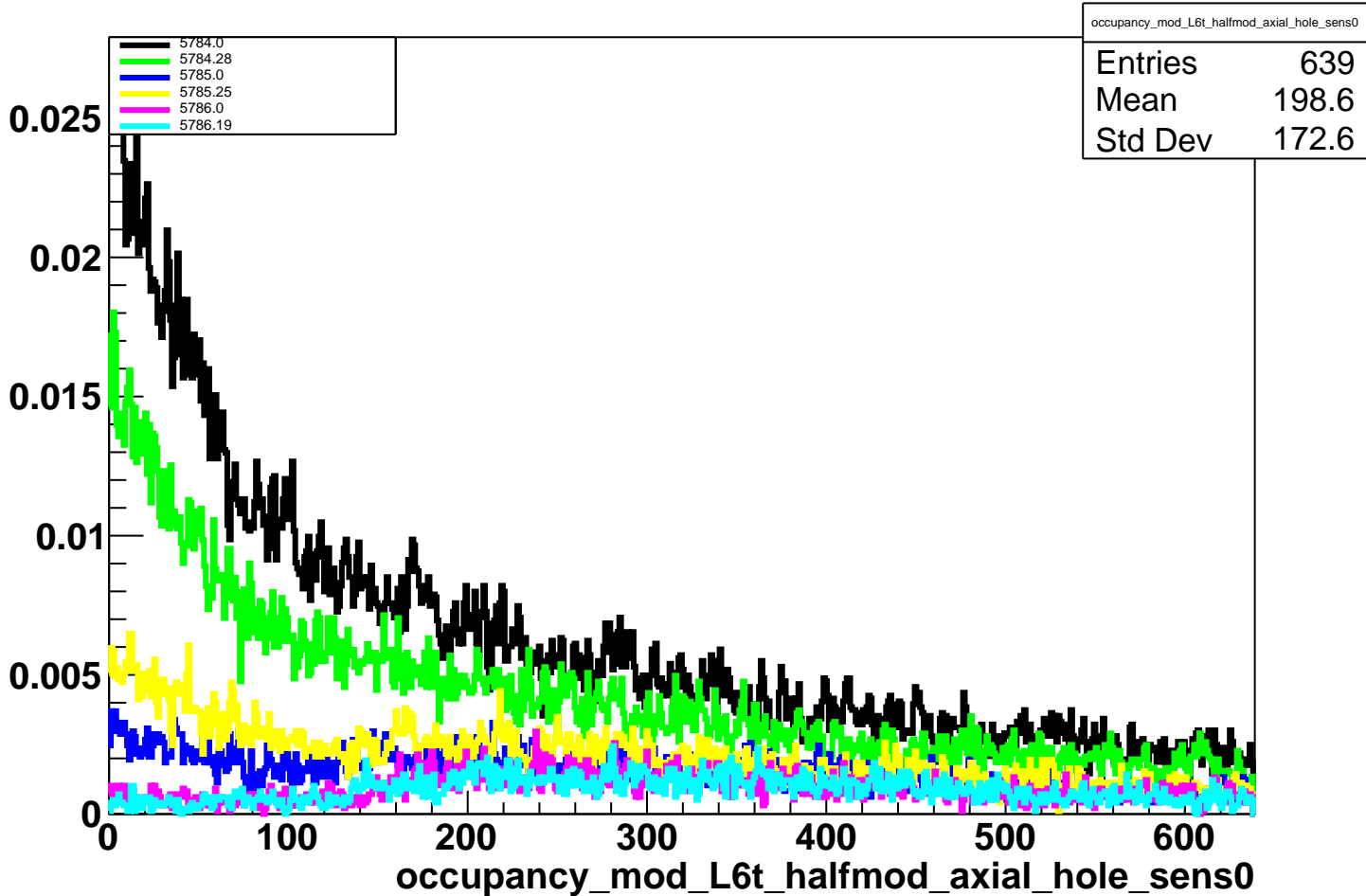
mod_L6b_halfmod_stereo_hole_sens0



mod_L6b_halfmod_stereo_slot_sens0

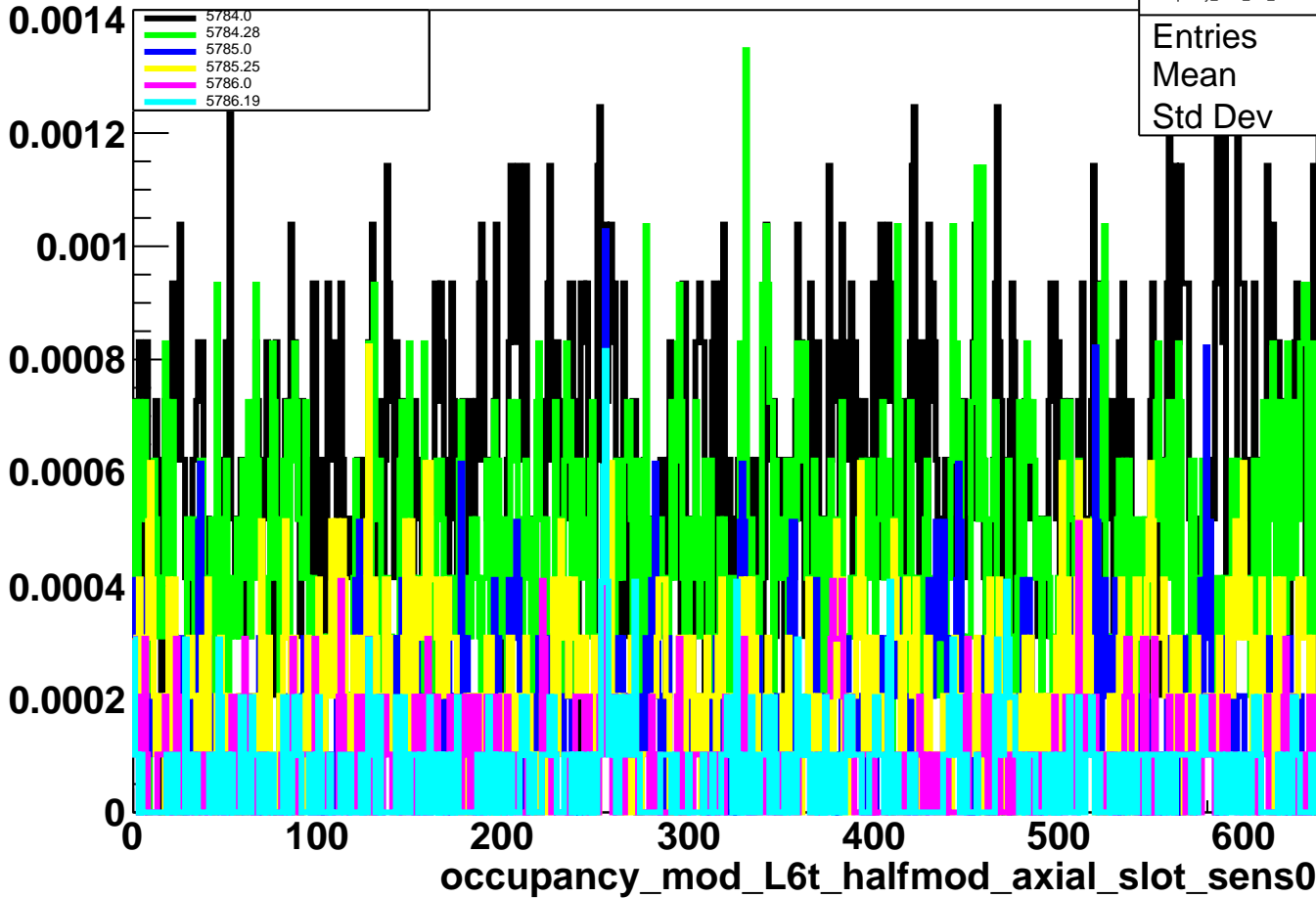


mod_L6t_halfmod_axial_hole_sens0

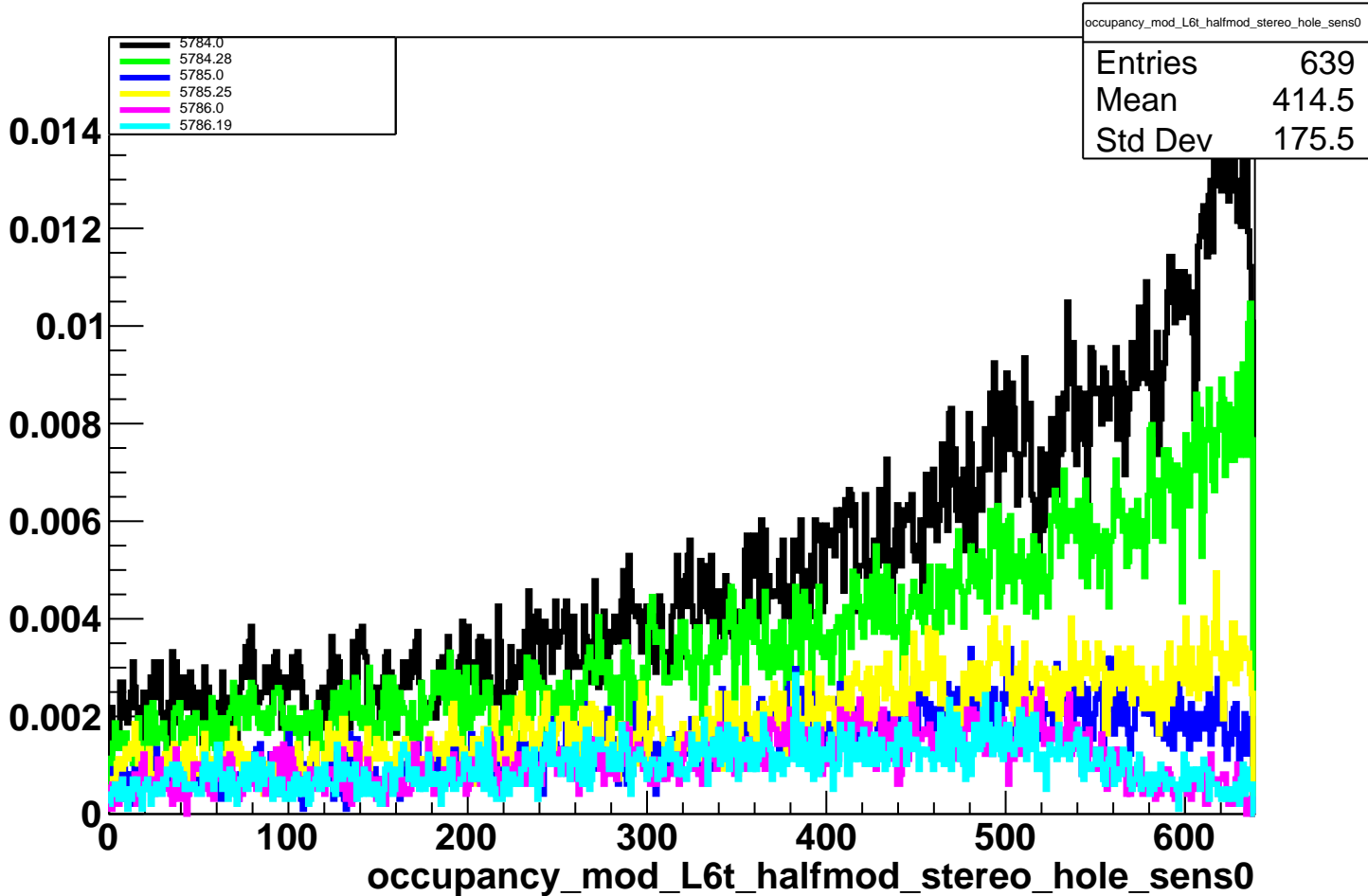


mod_L6t_halfmod_axial_slot_sens0

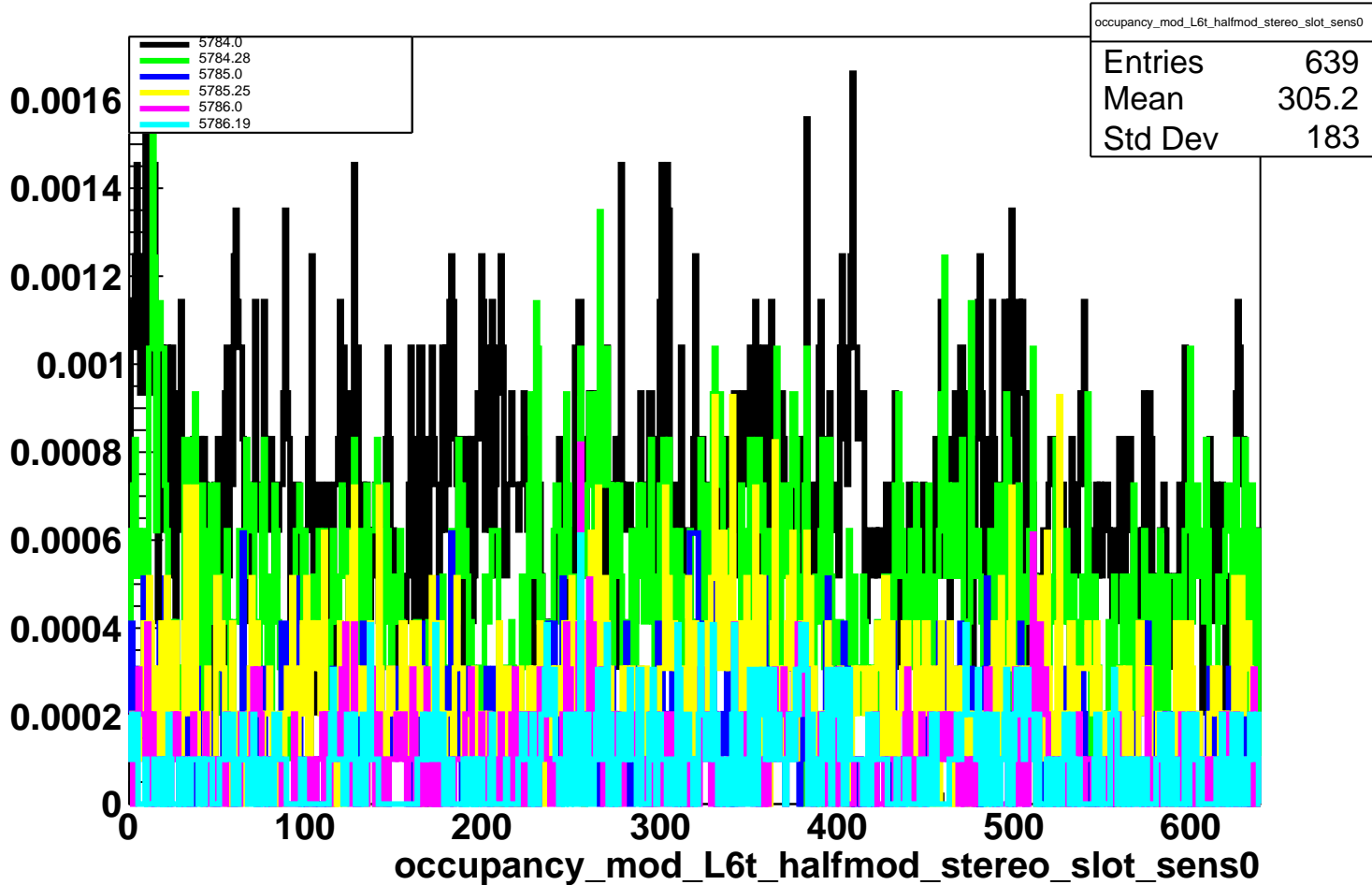
occupancy_mod_L6t_halfmod_axial_slot_sens0	
Entries	635
Mean	324.1
Std Dev	184.8



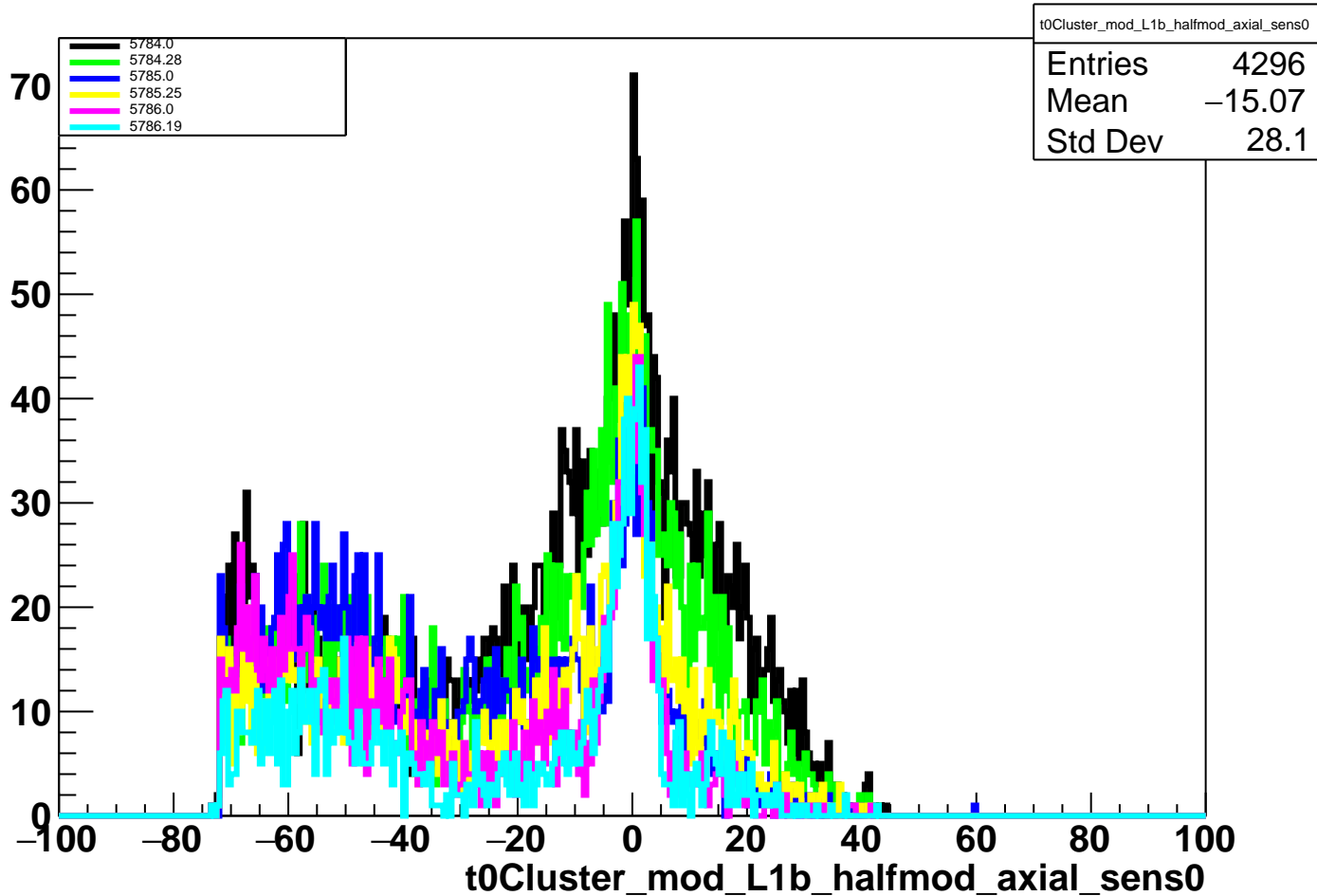
mod_L6t_halfmod_stereo_hole_sens0



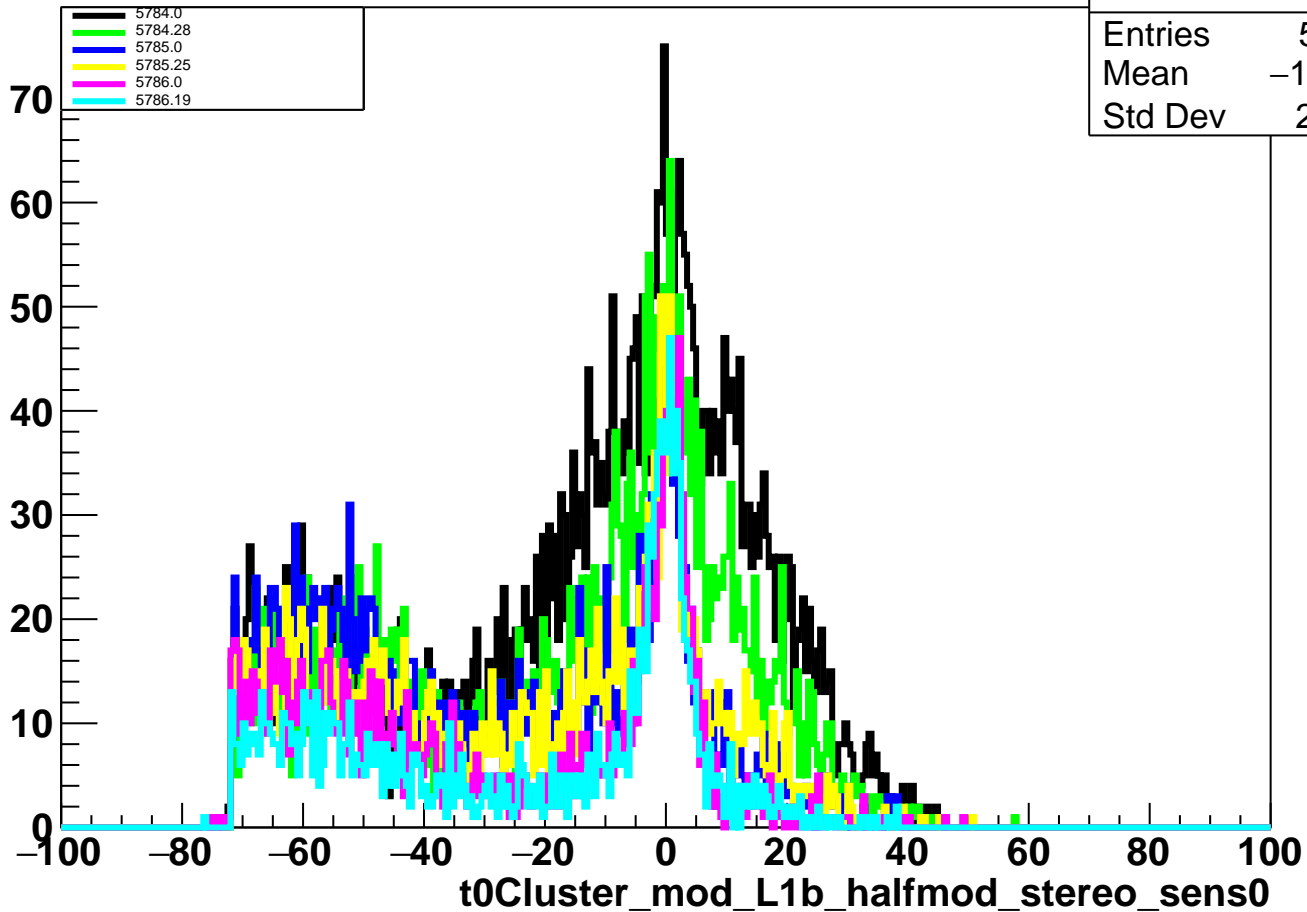
mod_L6t_halfmod_stereo_slot_sens0



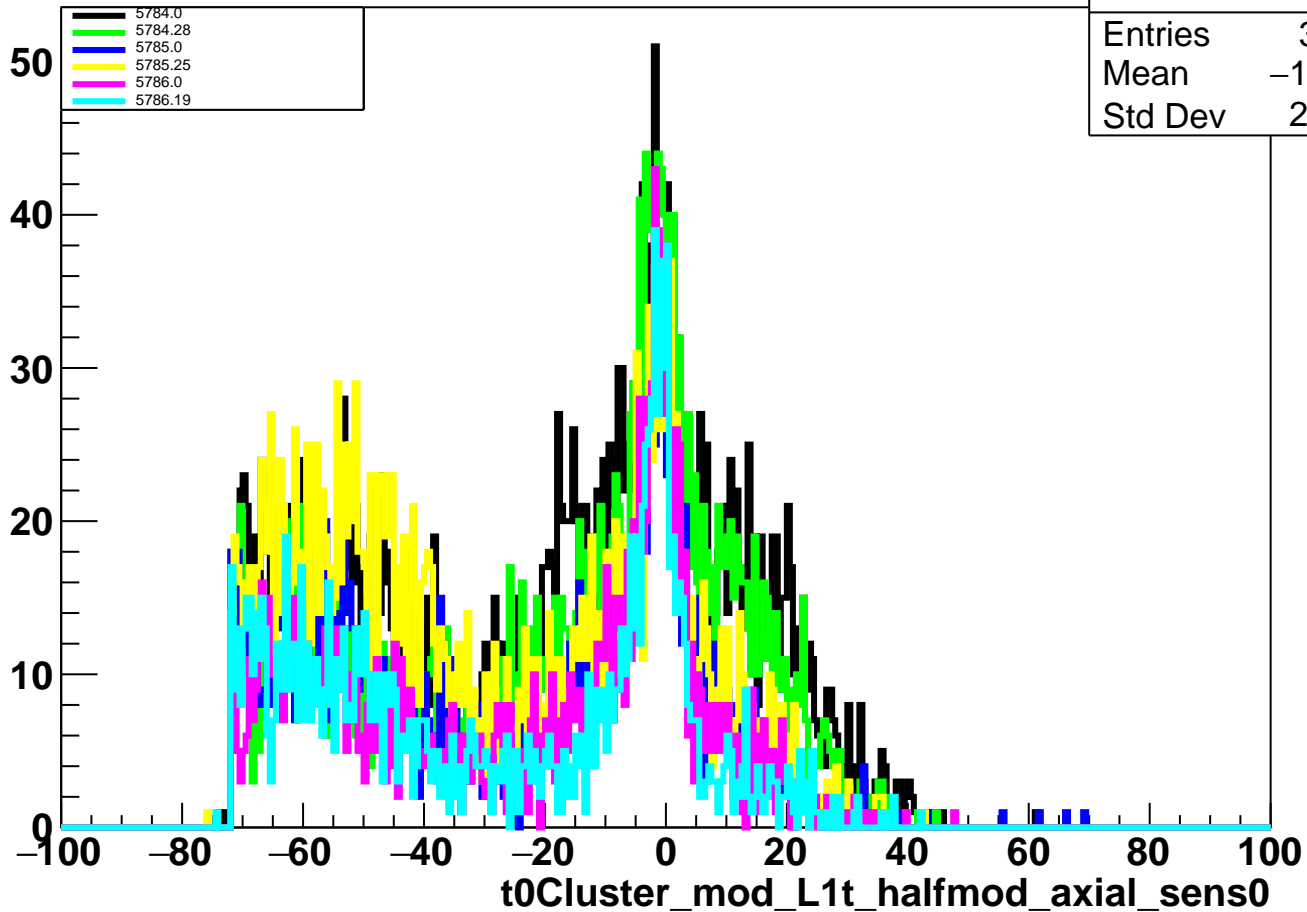
mod_L1b_halfmod_axial_sens0



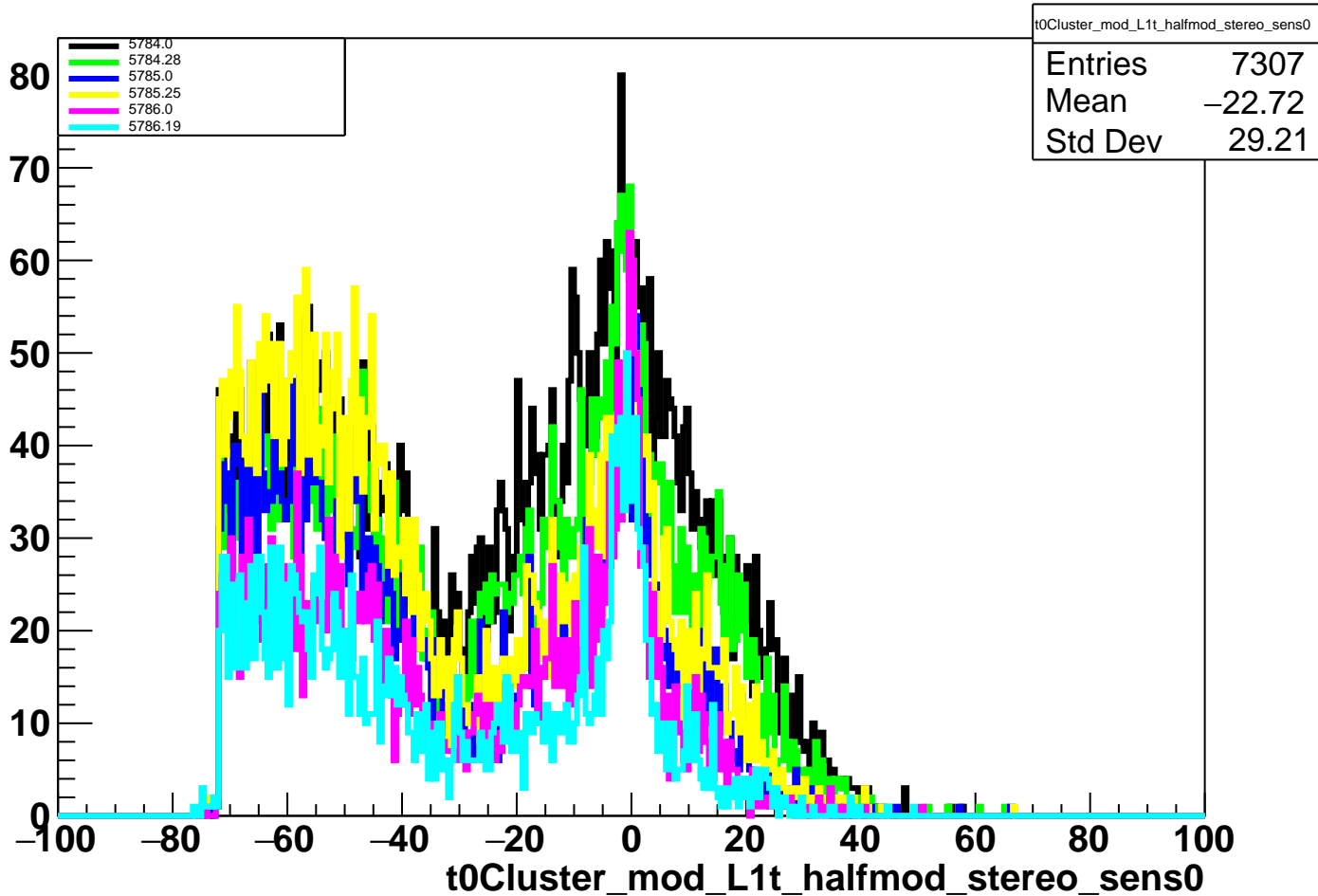
mod_L1b_halfmod_stereo_sens0



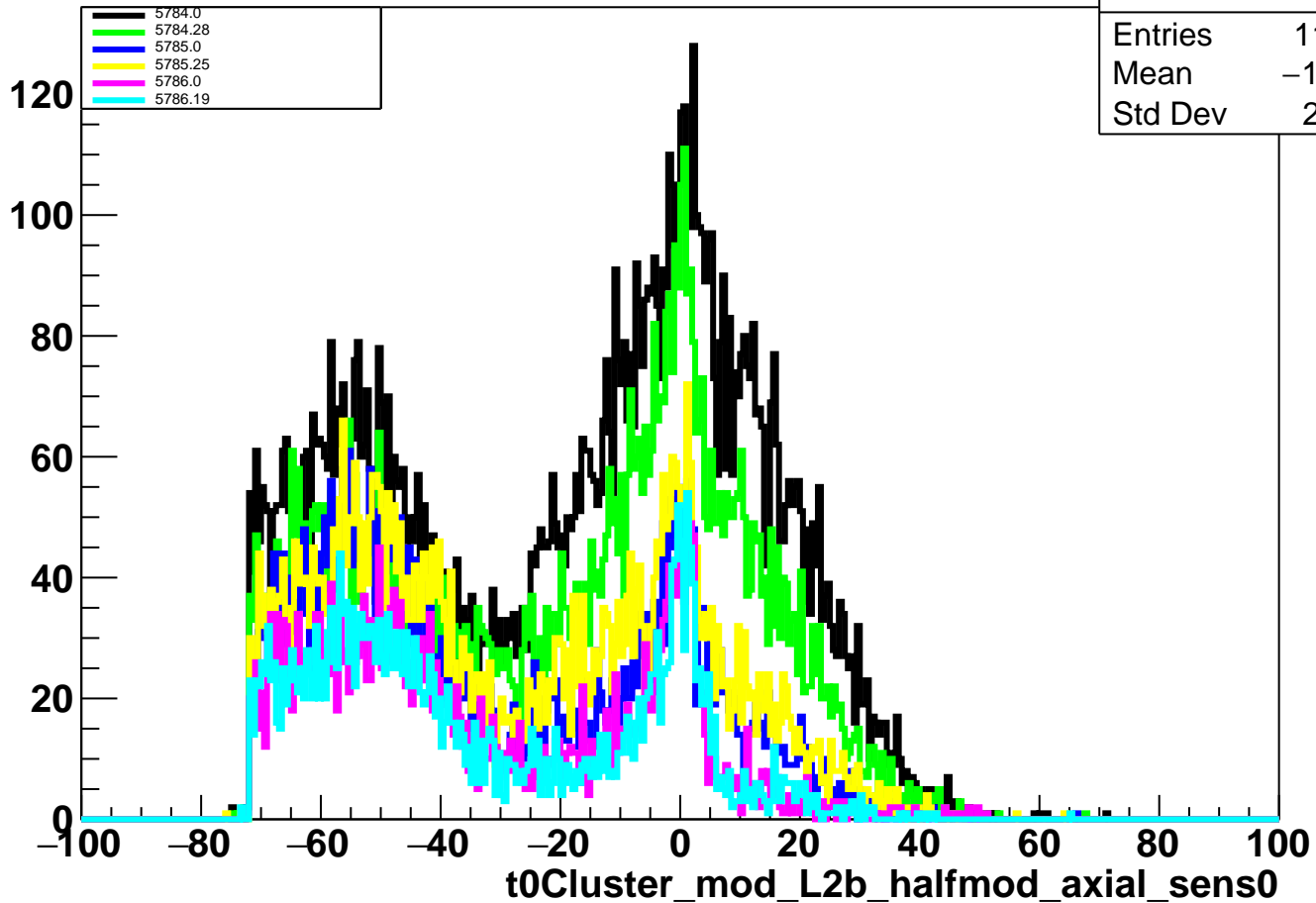
mod_L1t_halfmod_axial_sens0



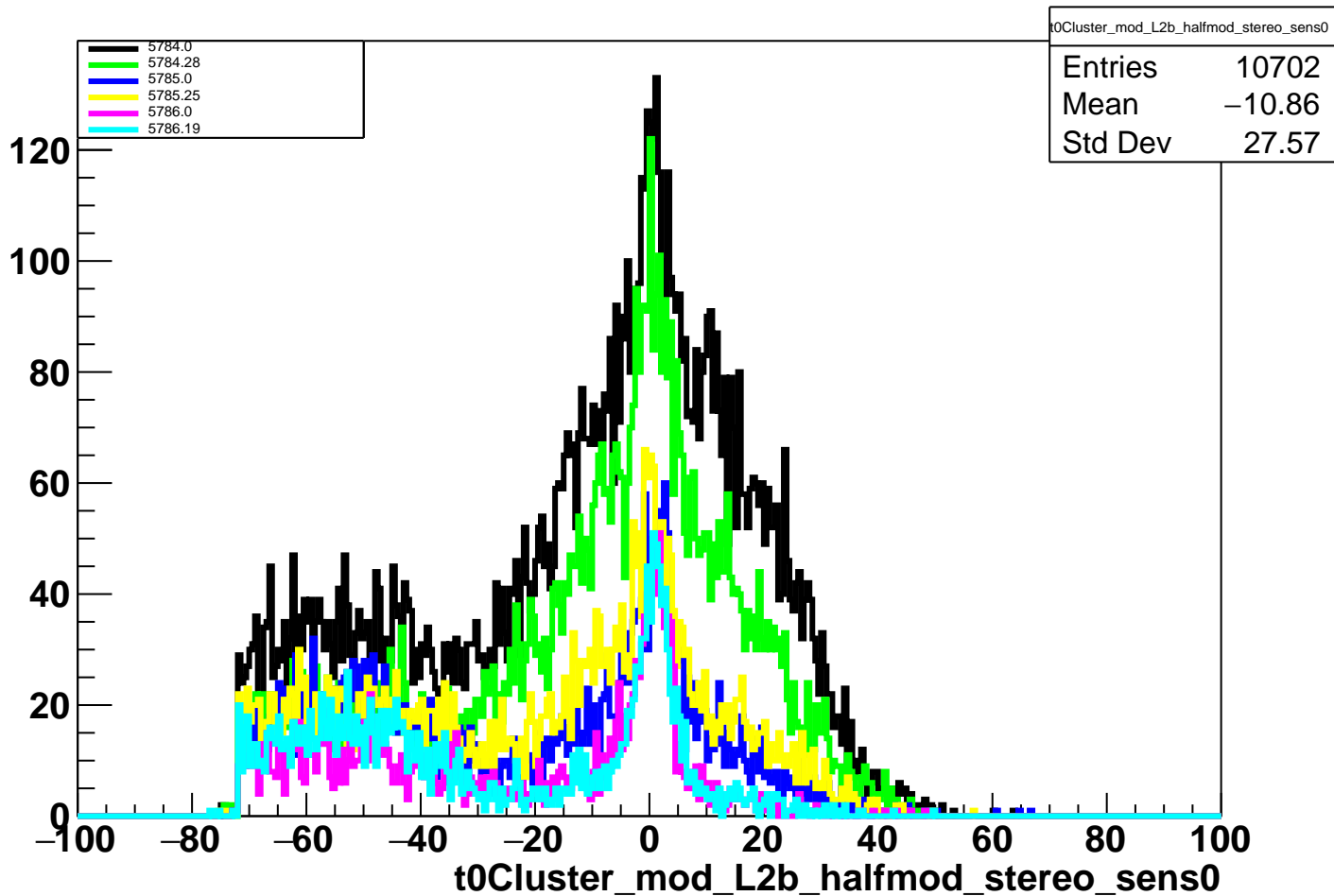
mod_L1t_halfmod_stereo_sens0



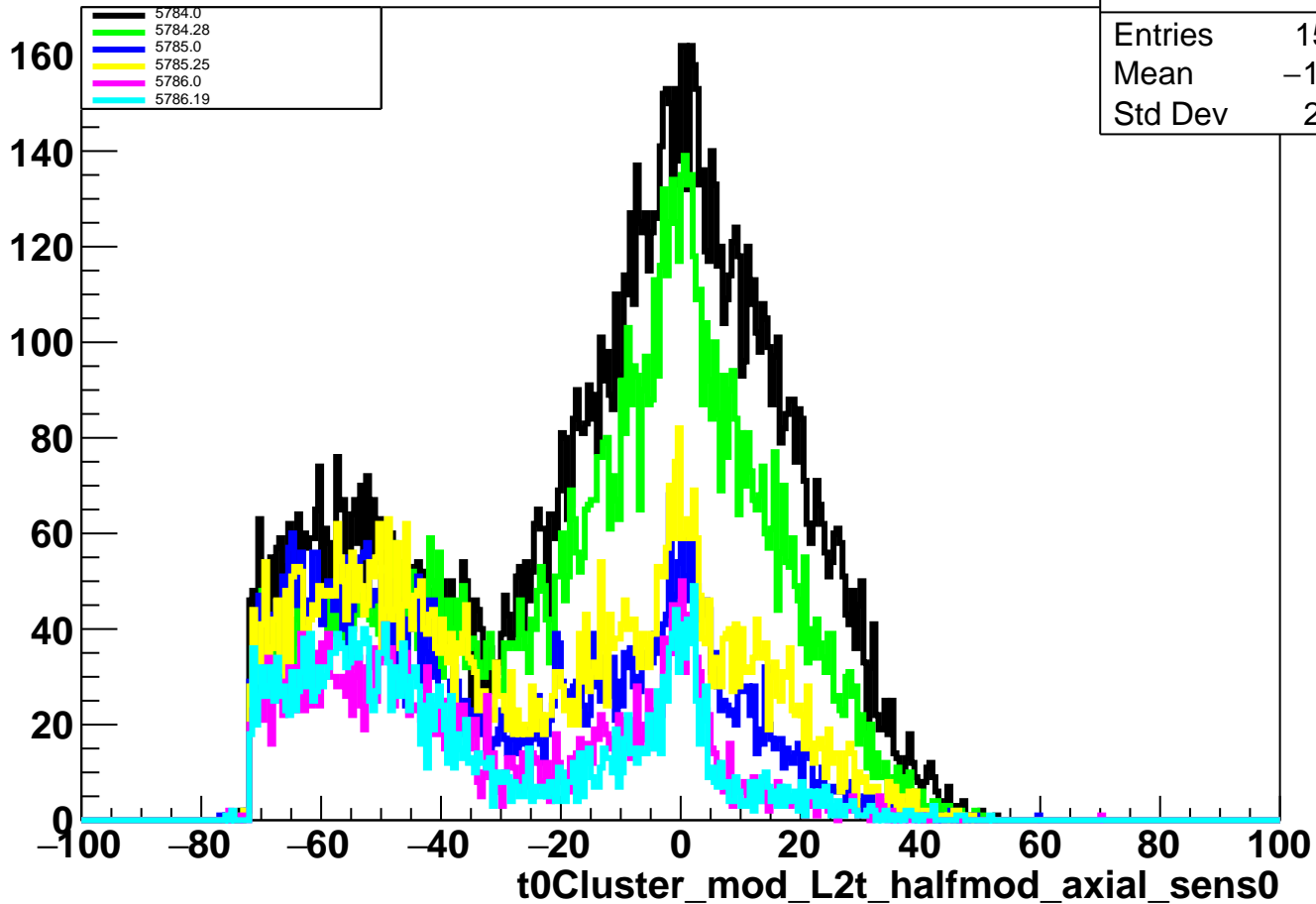
mod_L2b_halfmod_axial_sens0



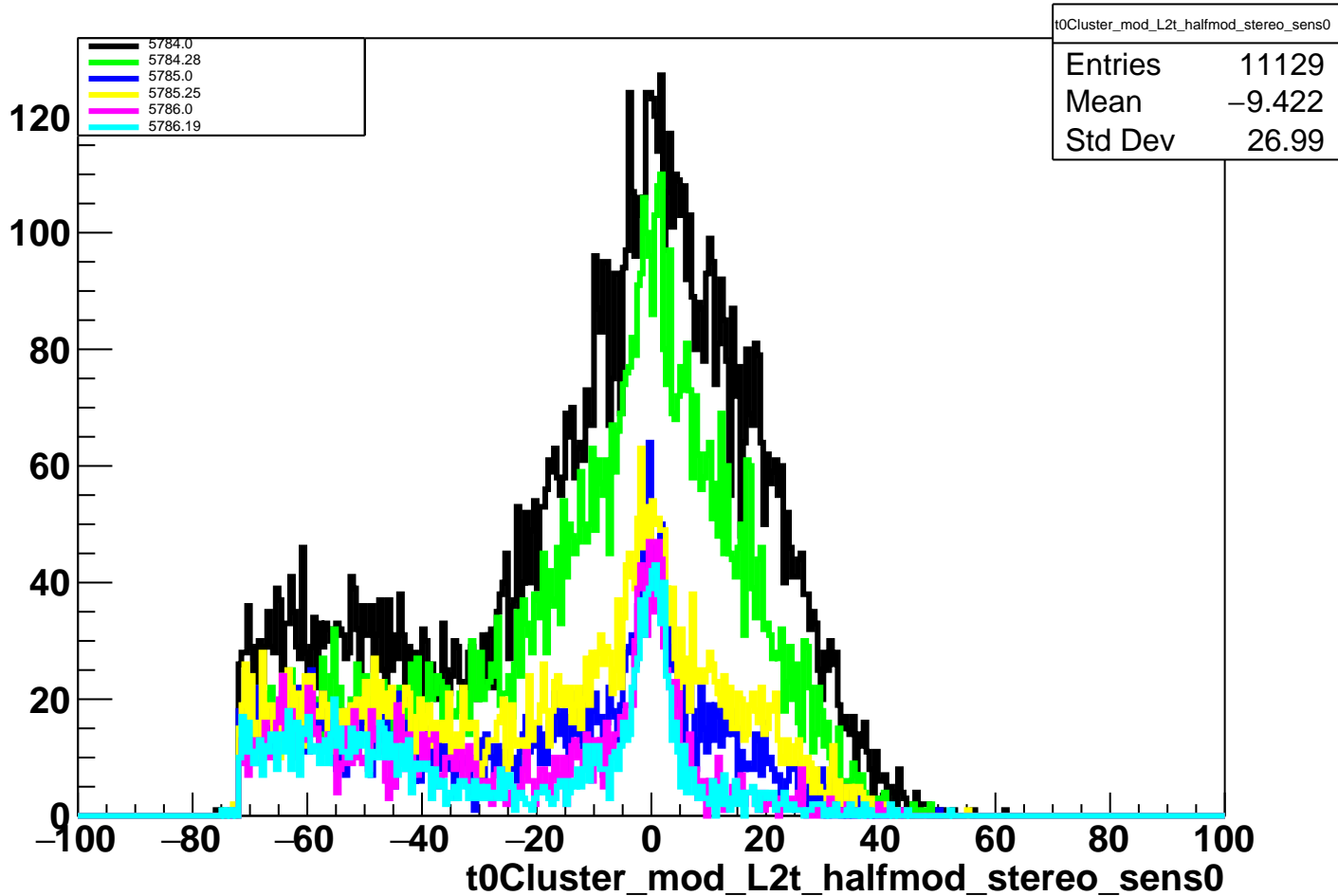
mod_L2b_halfmod_stereo_sens0



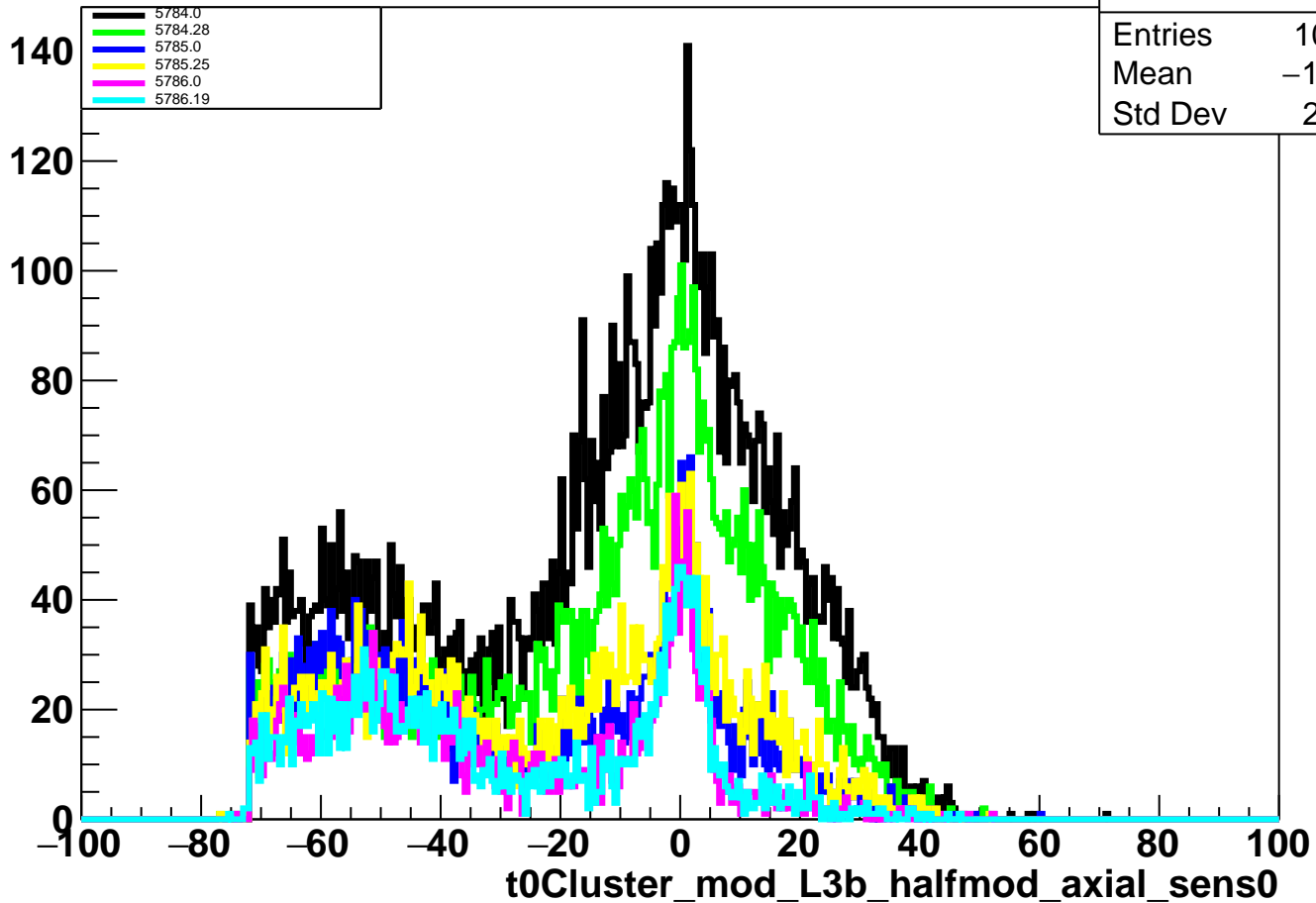
mod_L2t_halfmod_axial_sens0



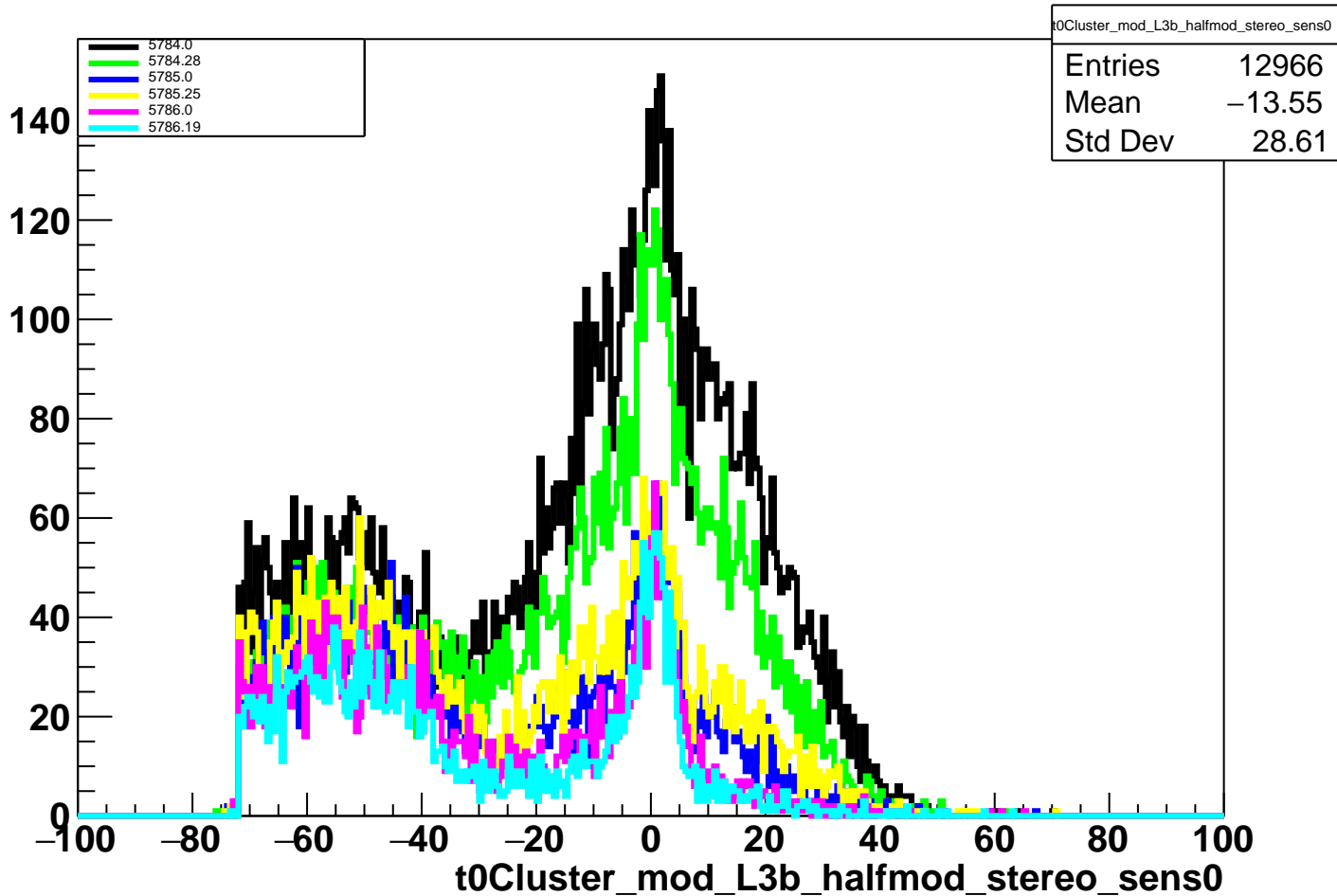
mod_L2t_halfmod_stereo_sens0



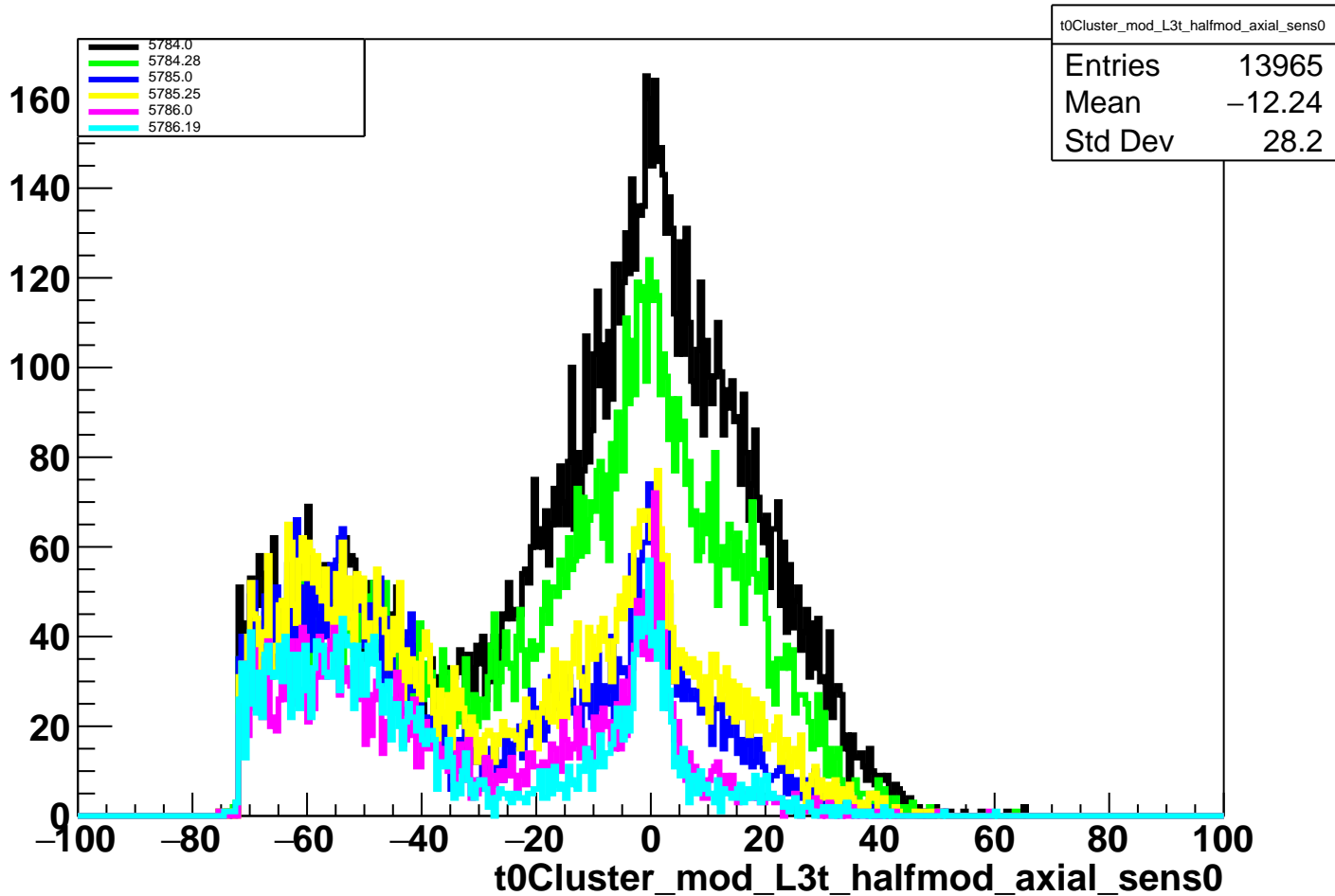
mod_L3b_halfmod_axial_sens0



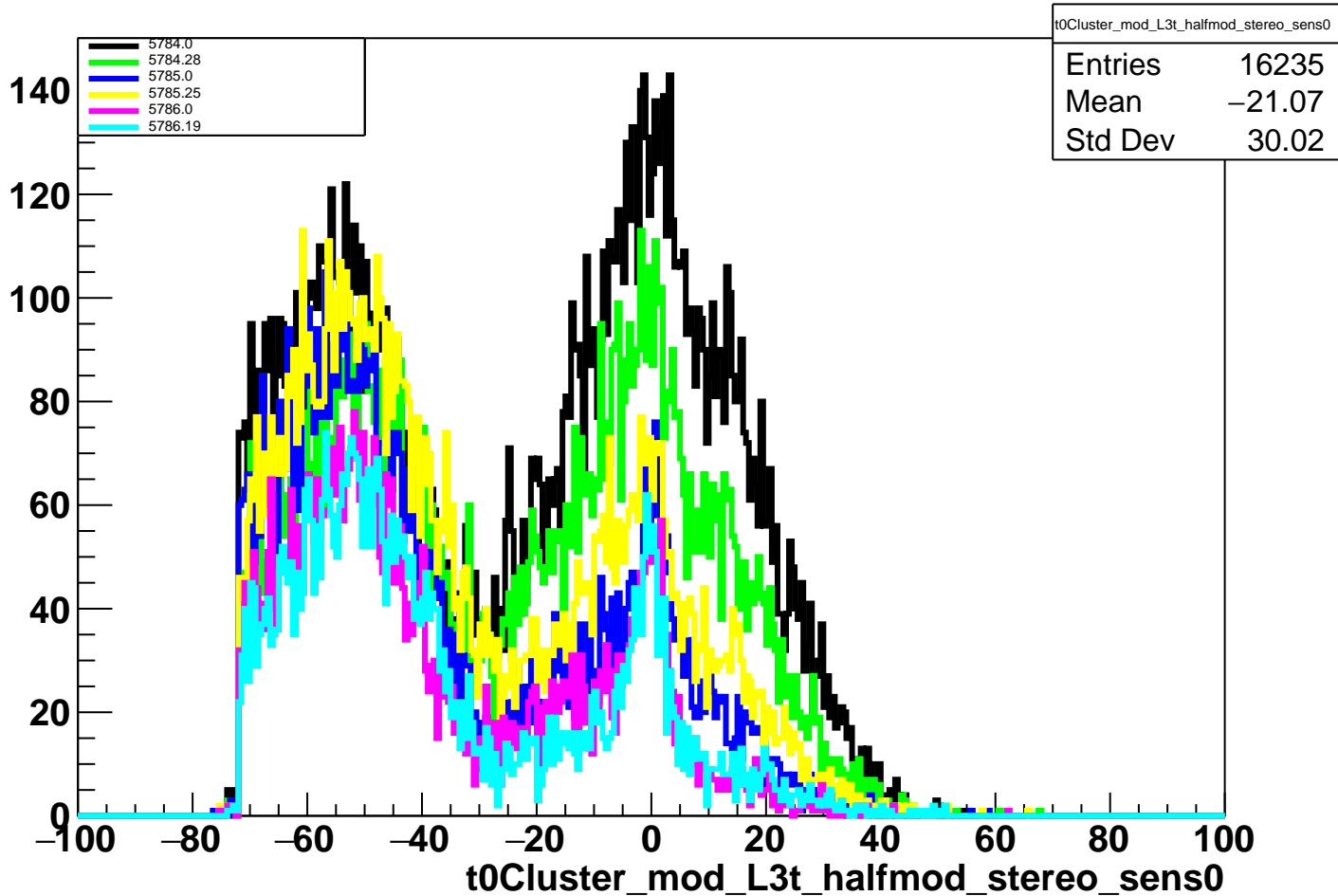
mod_L3b_halfmod_stereo_sens0



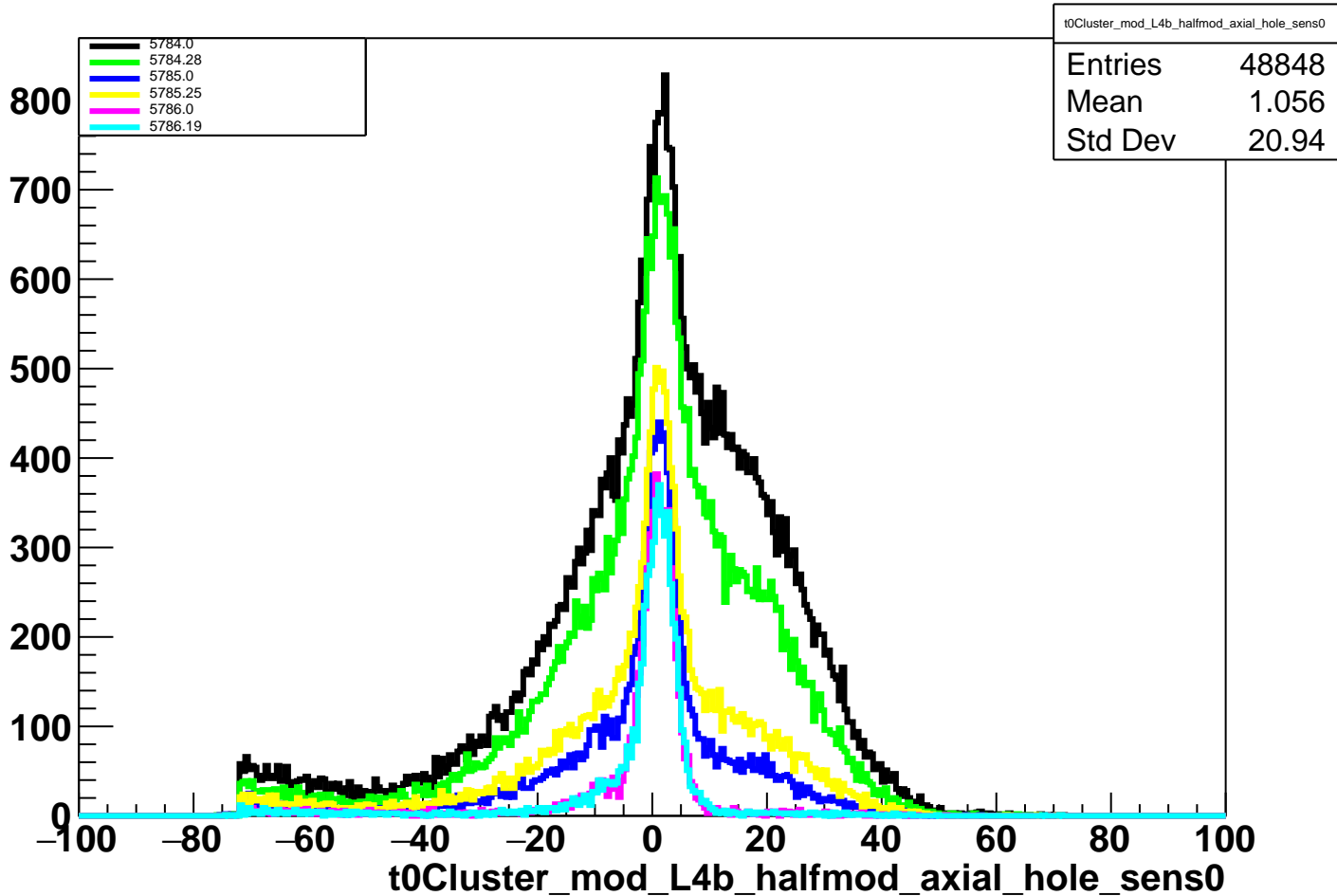
mod_L3t_halfmod_axial_sens0



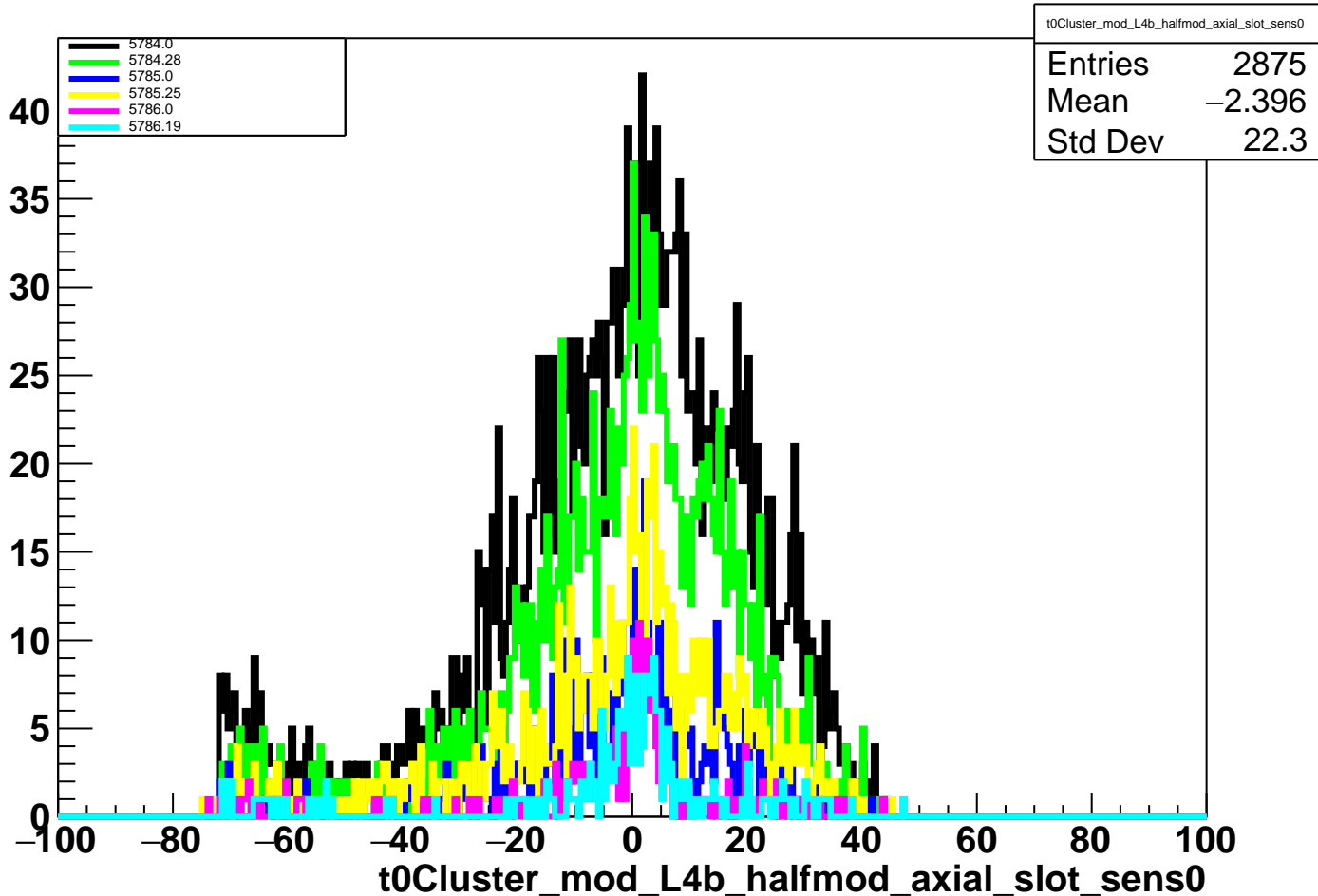
mod_L3t_halfmod_stereo_sens0



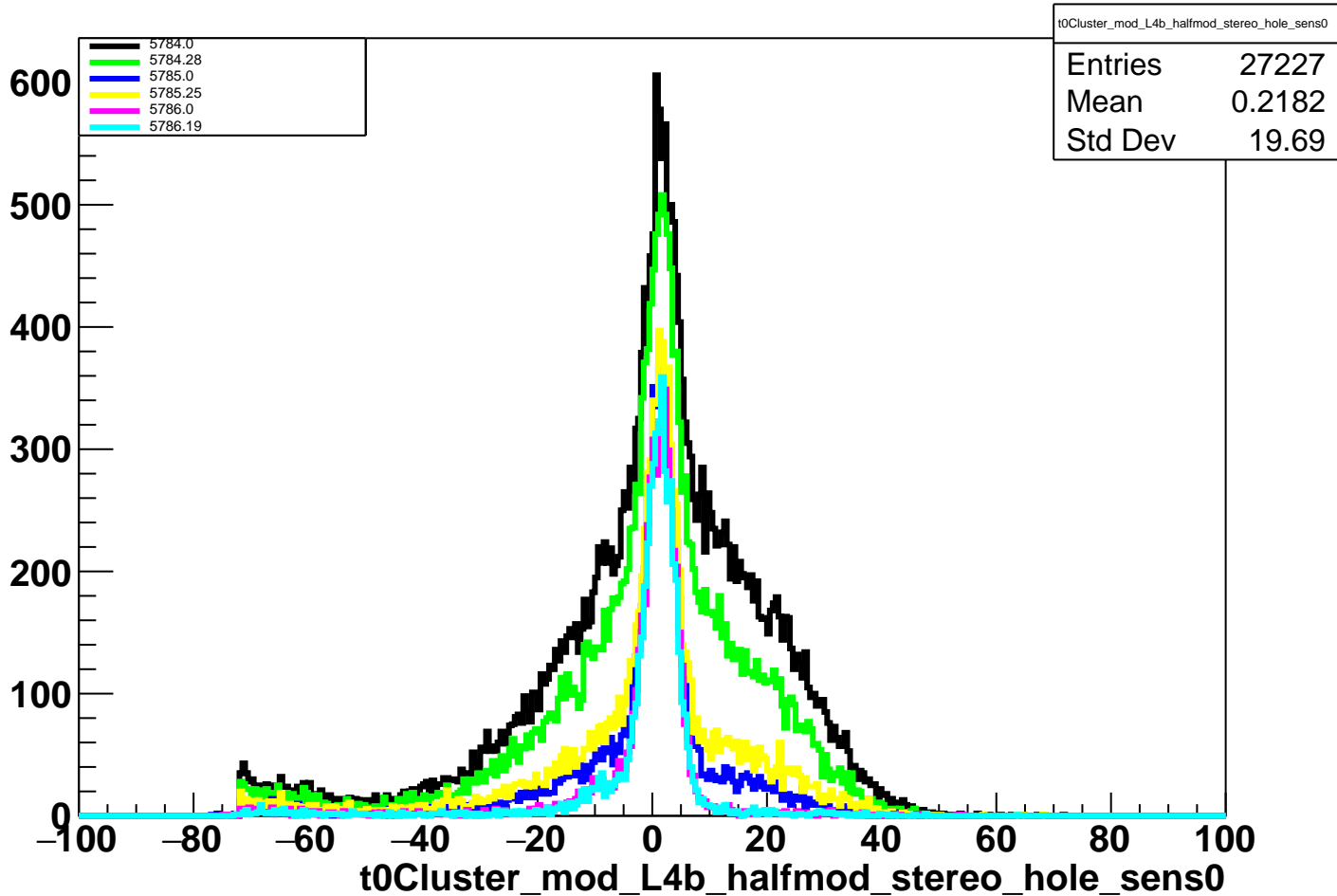
mod_L4b_halfmod_axial_hole_sens0



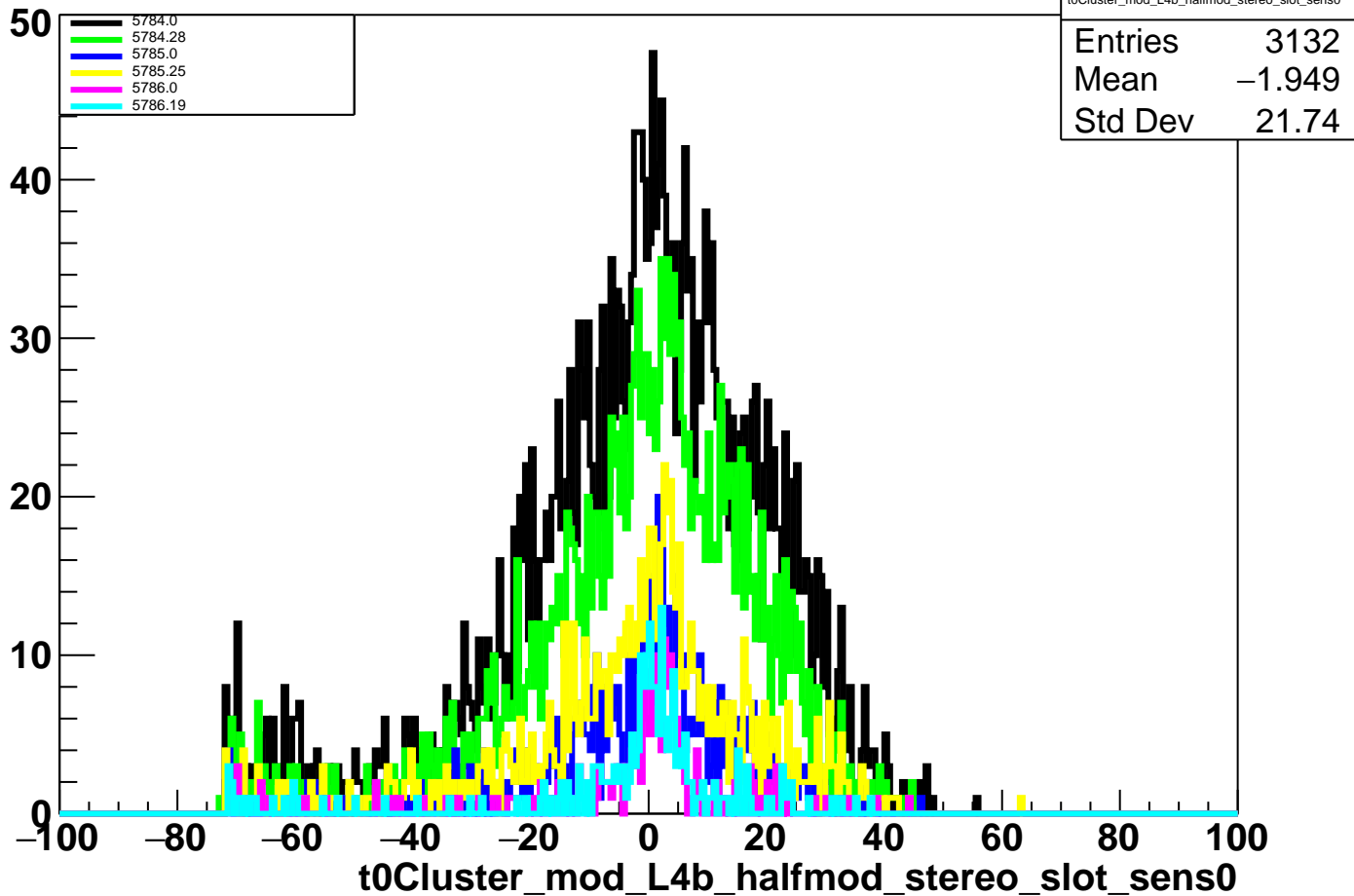
mod_L4b_halfmod_axial_slot_sens0



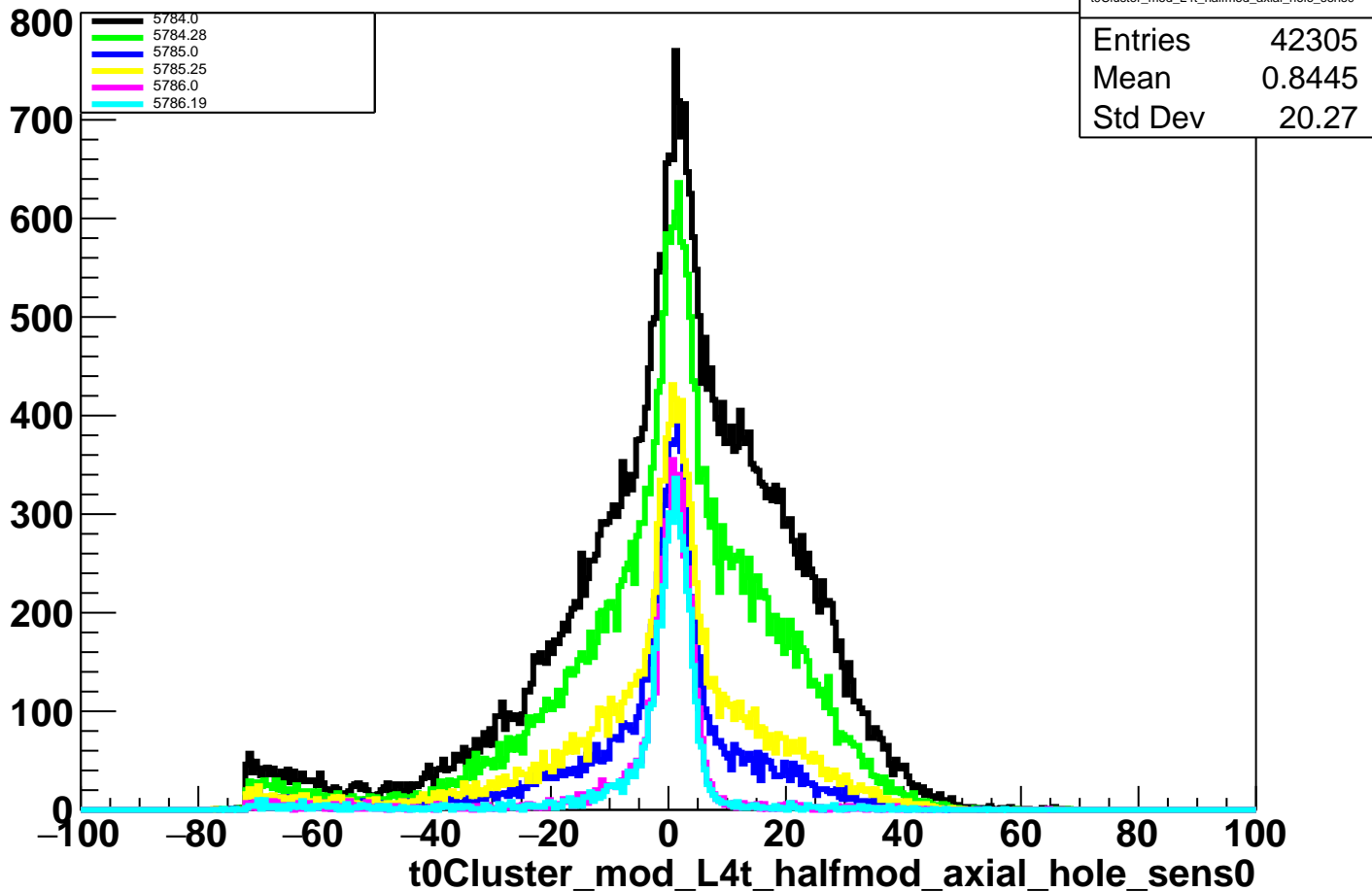
mod_L4b_halfmod_stereo_hole_sens0



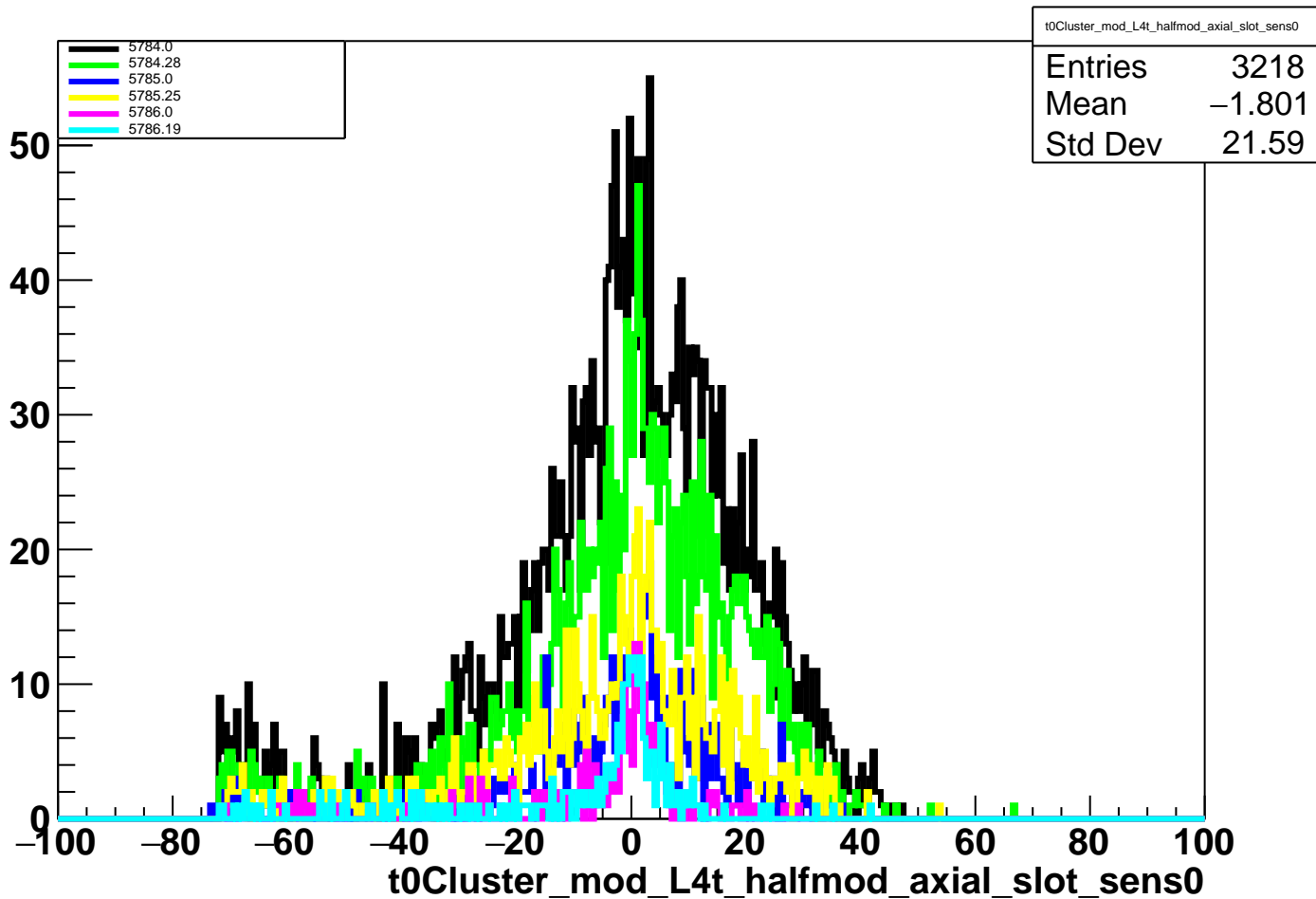
mod_L4b_halfmod_stereo_slot_sens0



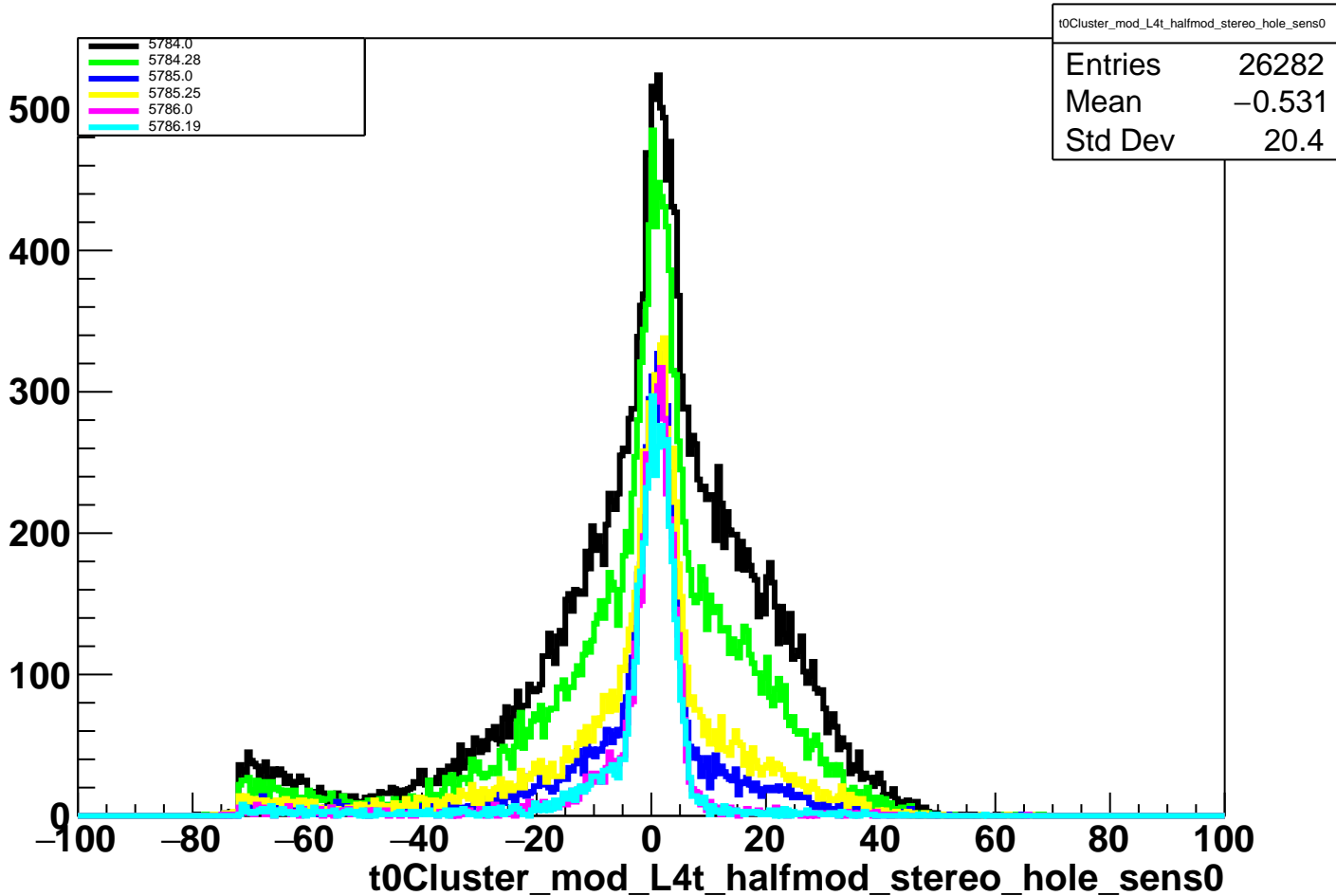
mod_L4t_halfmod_axial_hole_sens0



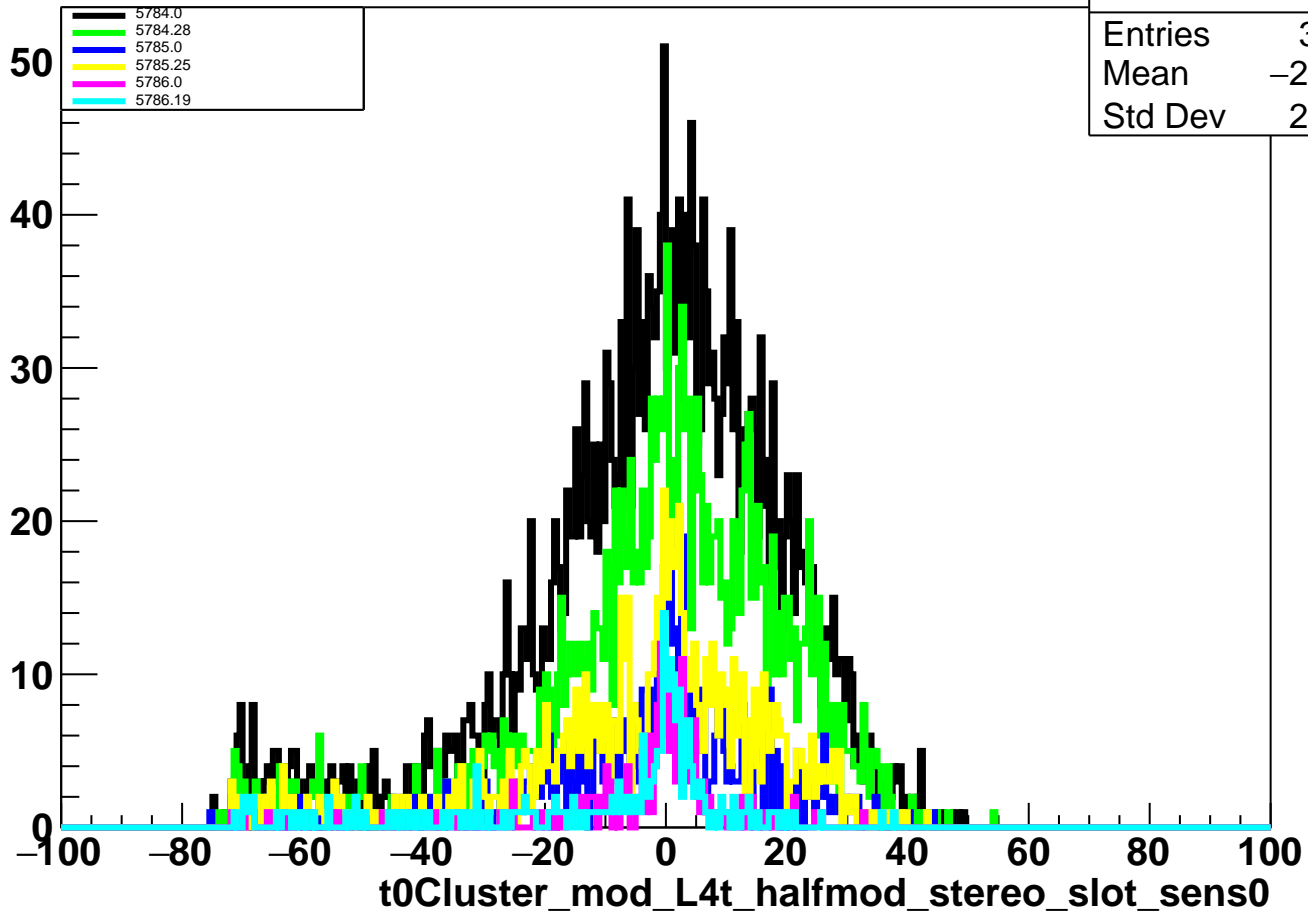
mod_L4t_halfmod_axial_slot_sens0



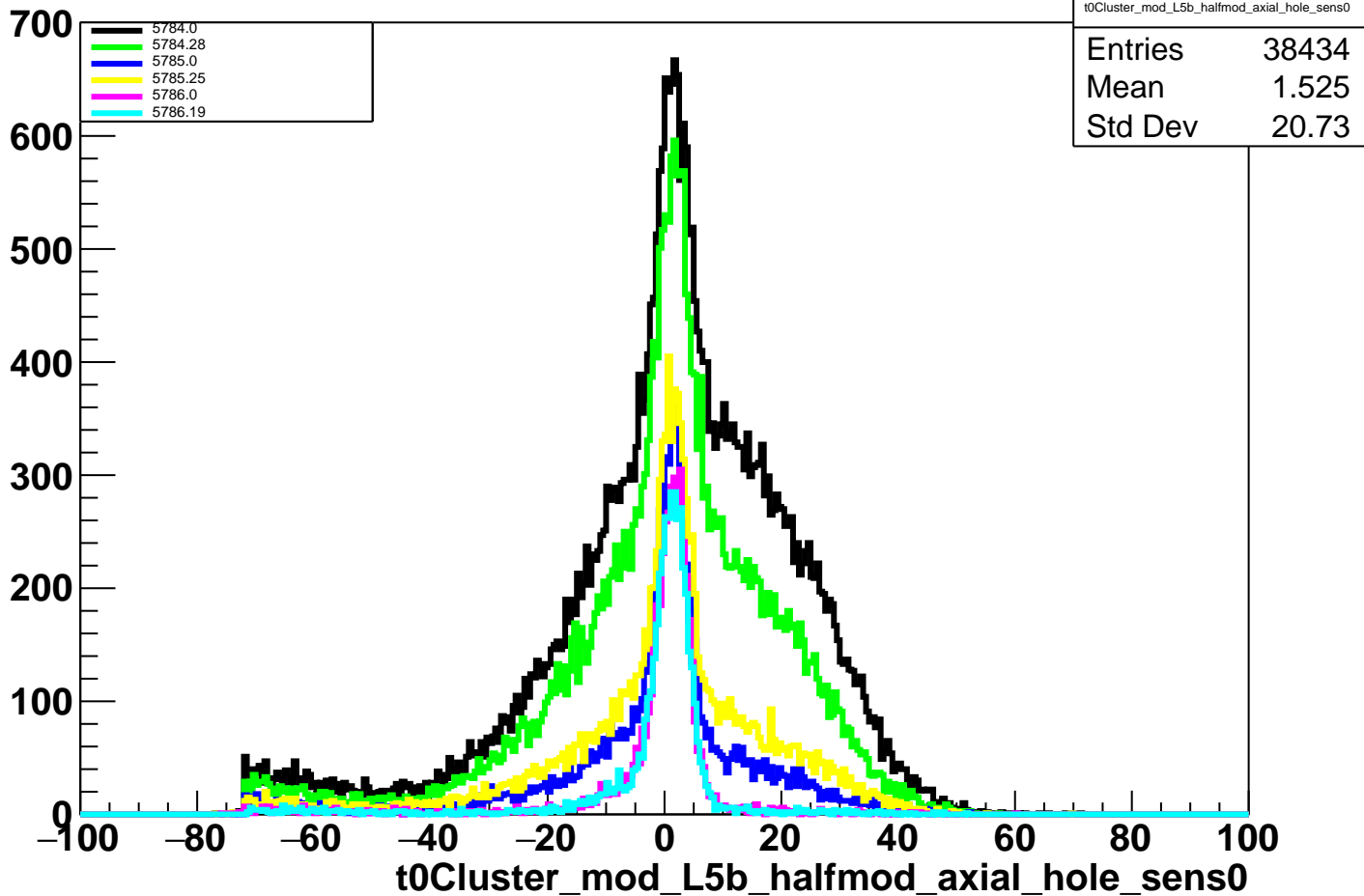
mod_L4t_halfmod_stereo_hole_sens0



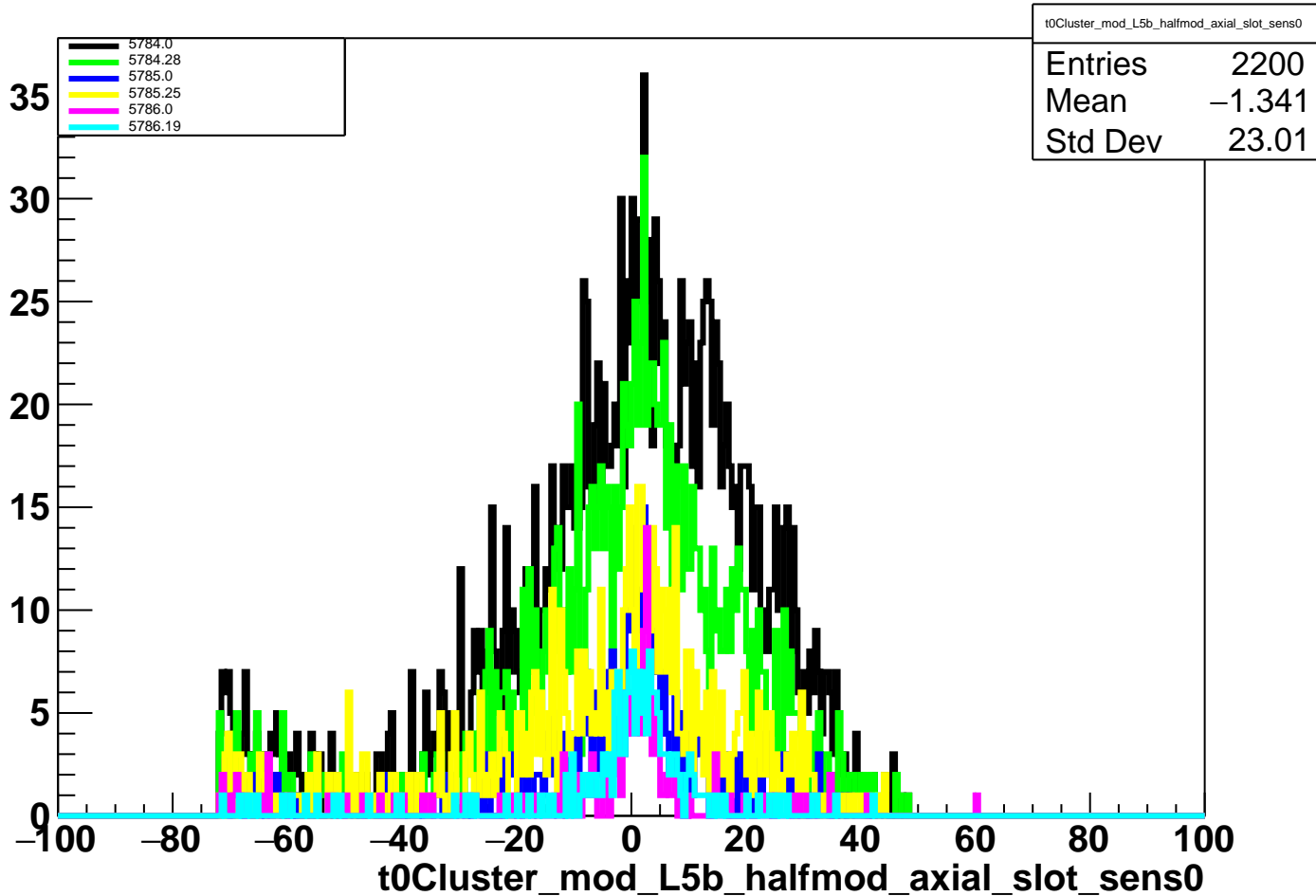
mod_L4t_halfmod_stereo_slot_sens0



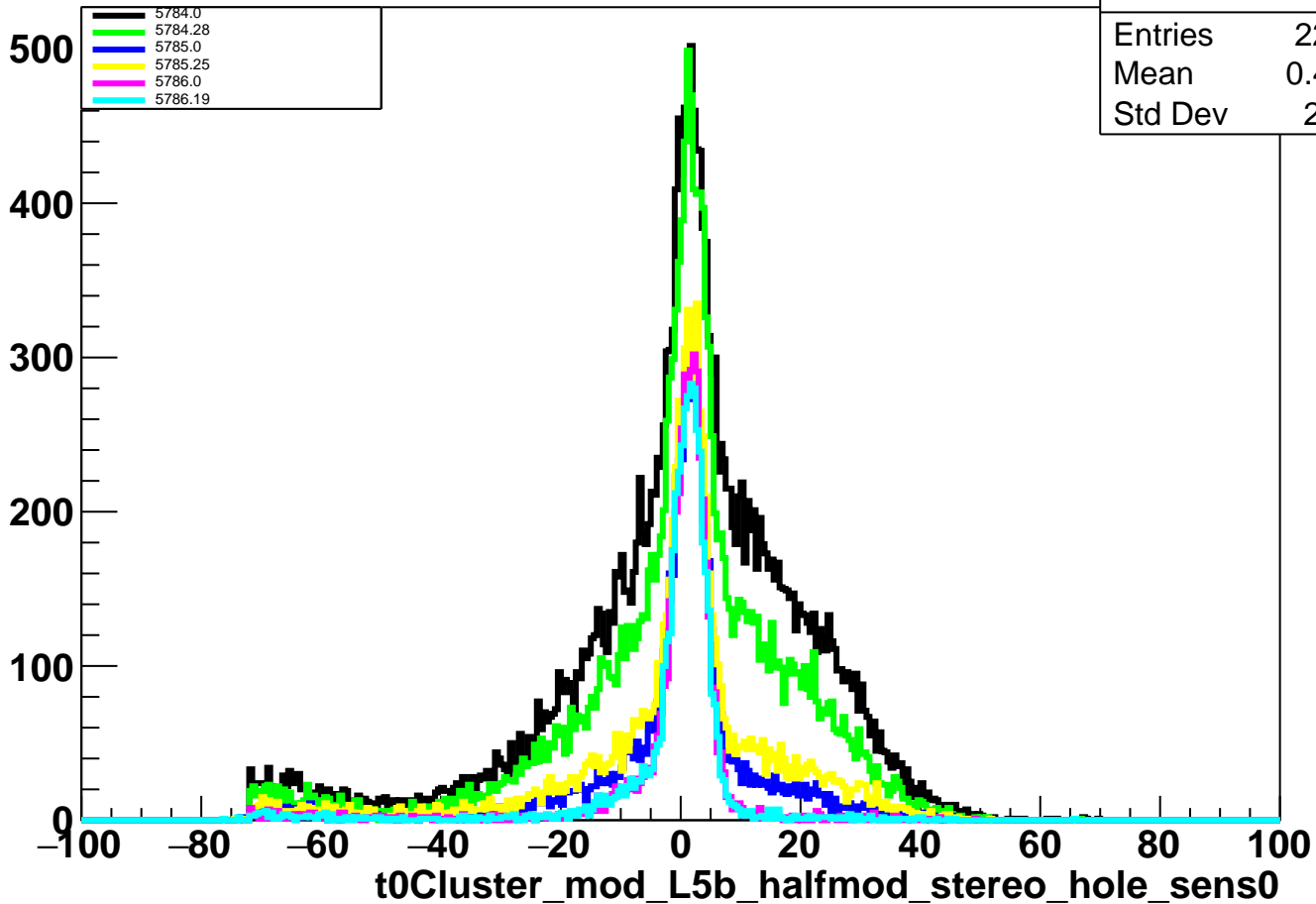
mod_L5b_halfmod_axial_hole_sens0



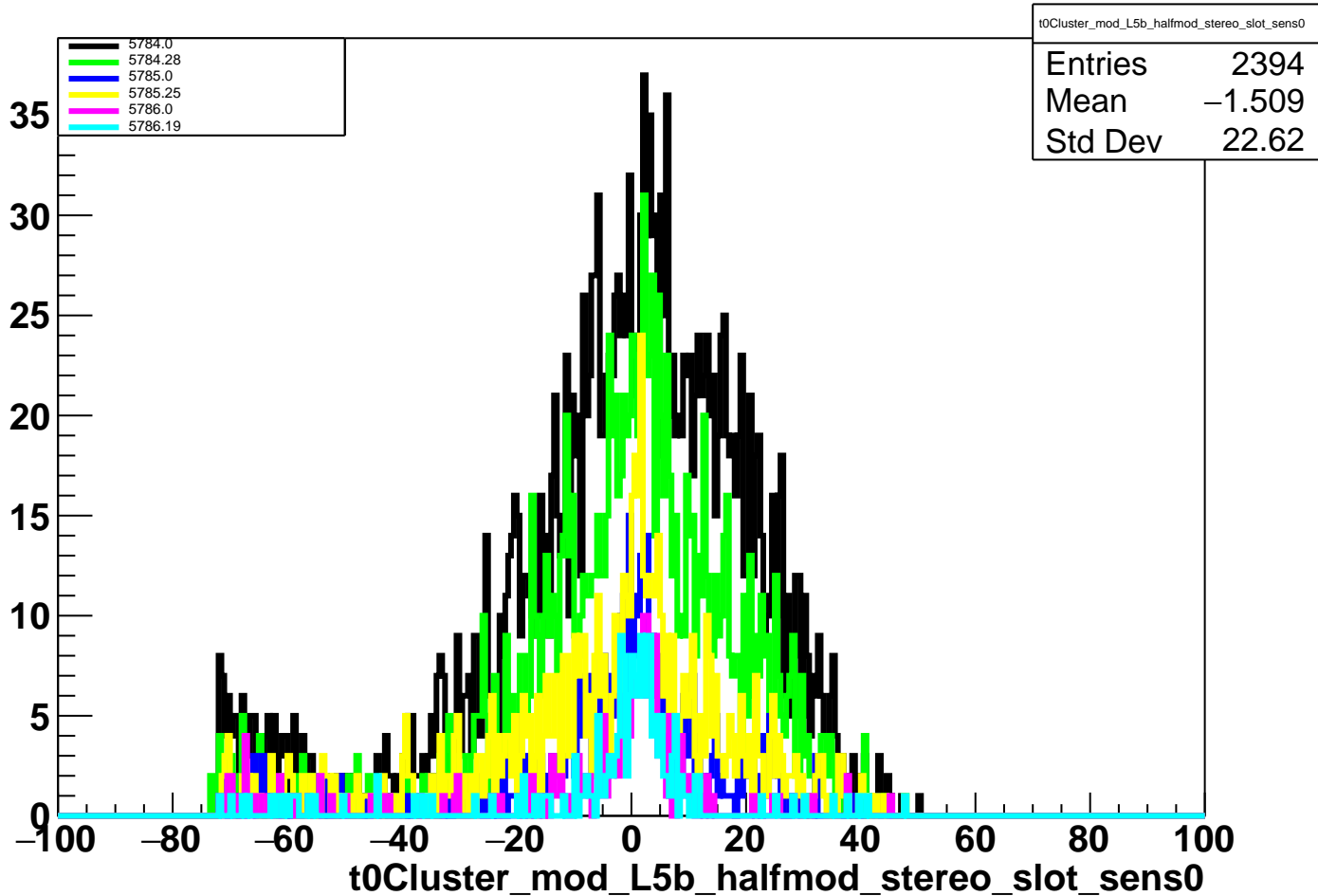
mod_L5b_halfmod_axial_slot_sens0



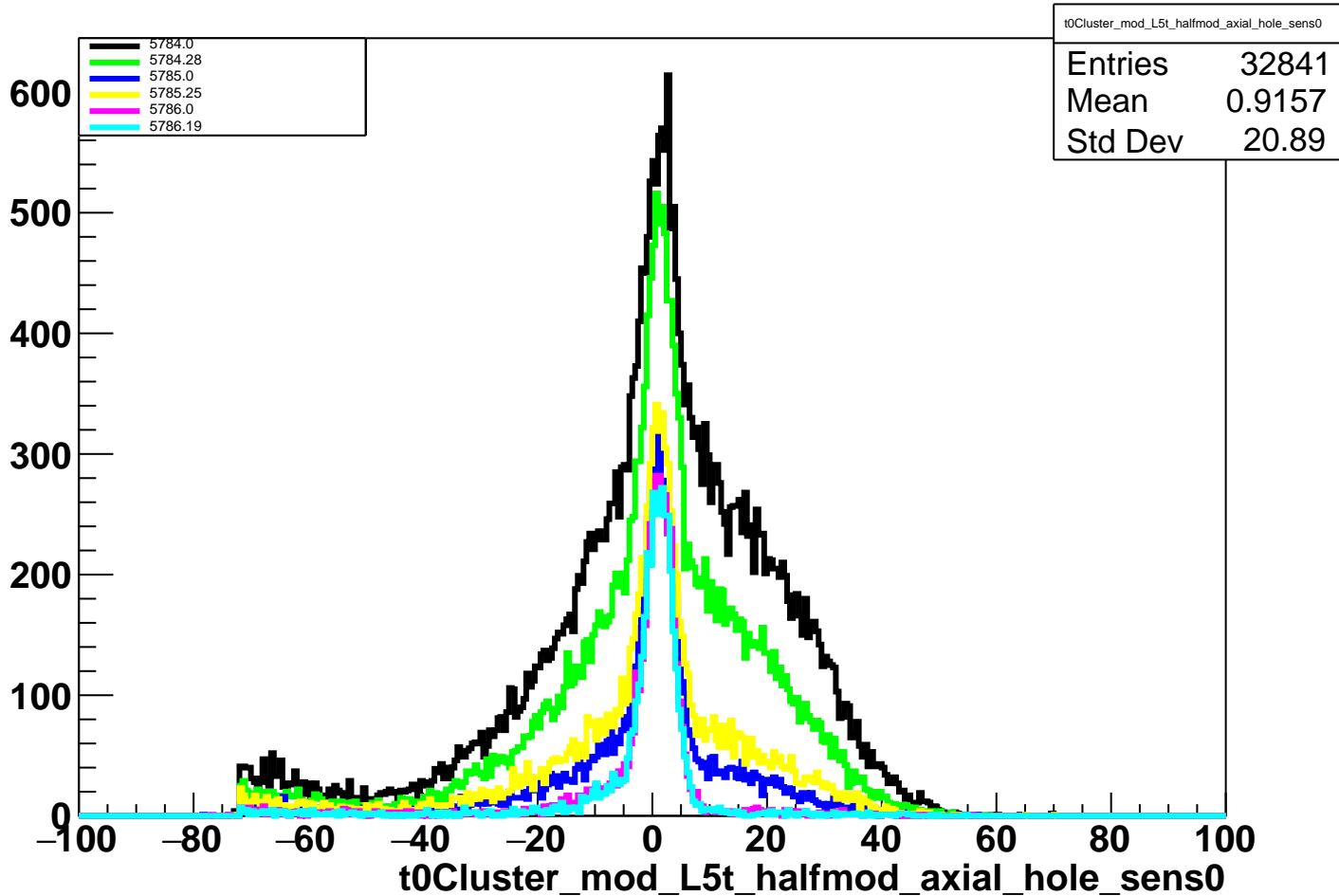
mod_L5b_halfmod_stereo_hole_sens0



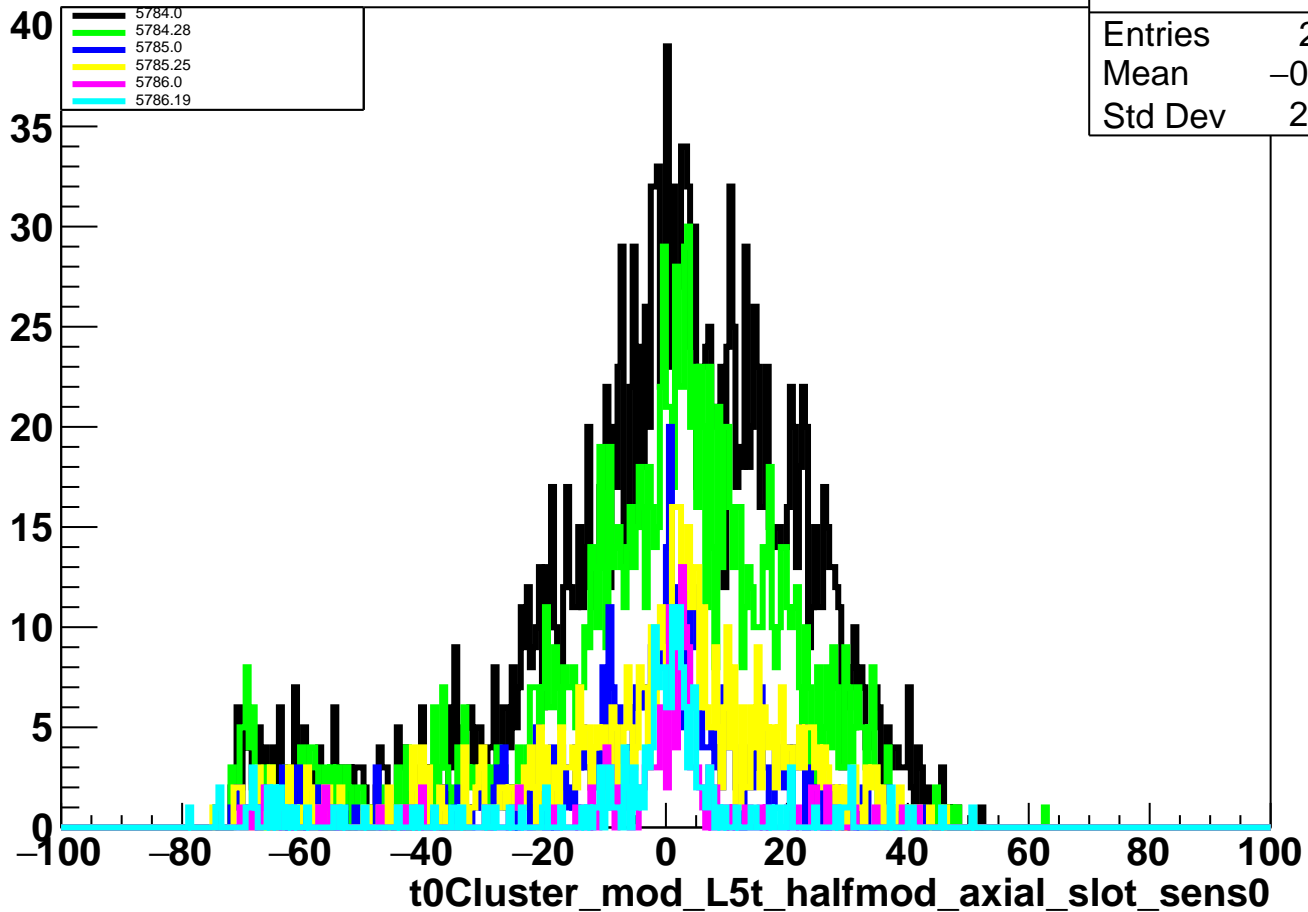
mod_L5b_halfmod_stereo_slot_sens0



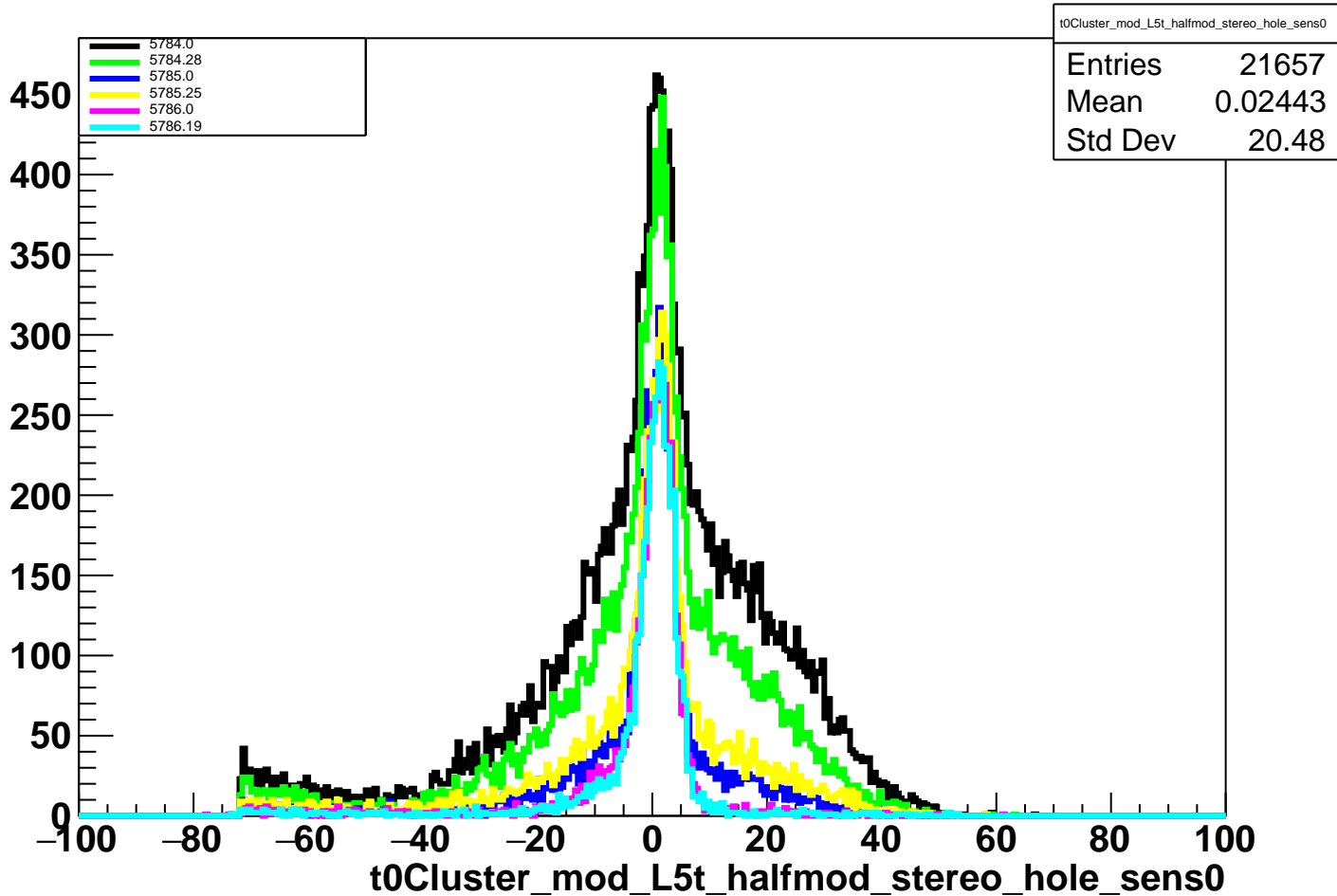
mod_L5t_halfmod_axial_hole_sens0



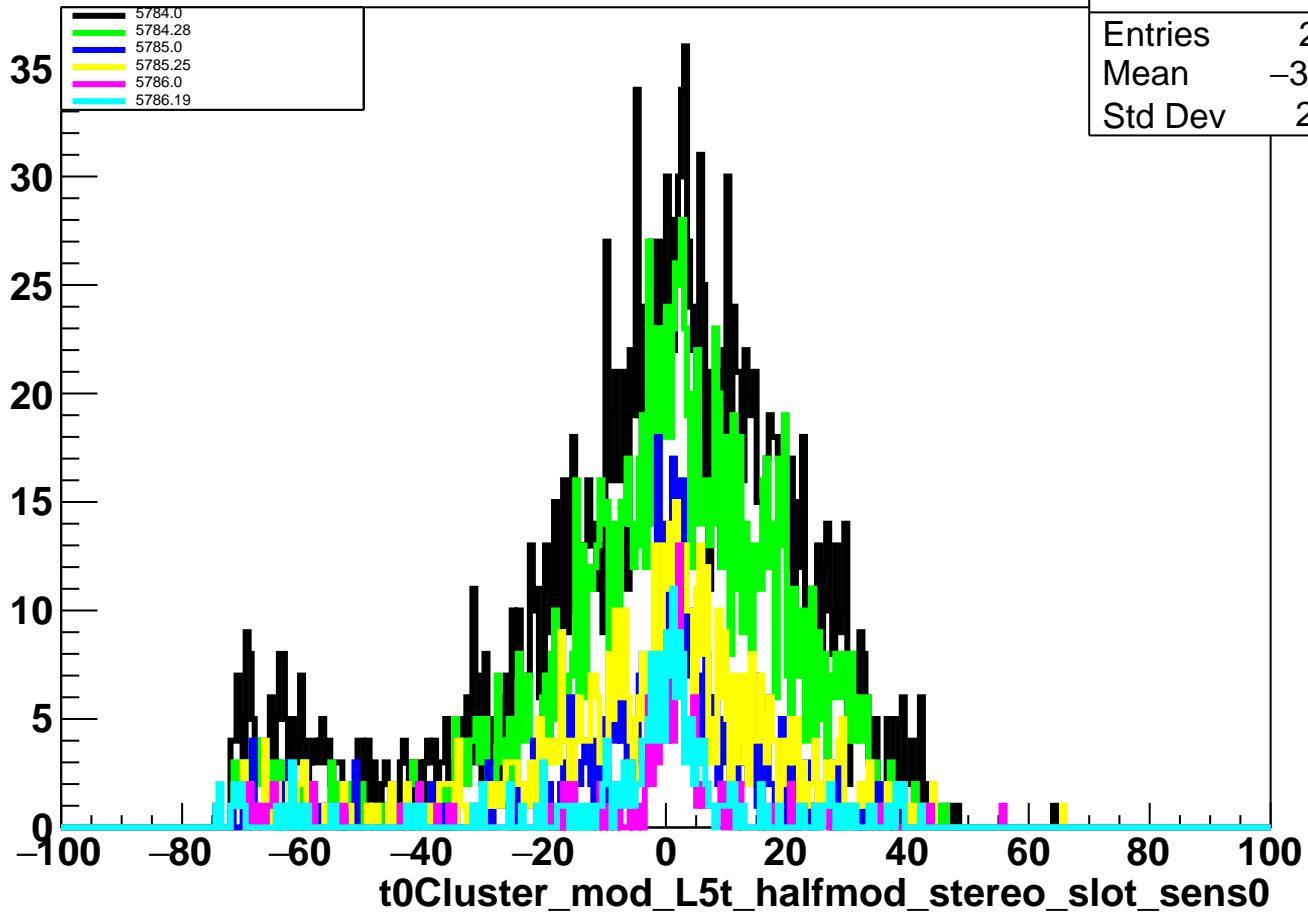
mod_L5t_halfmod_axial_slot_sens0



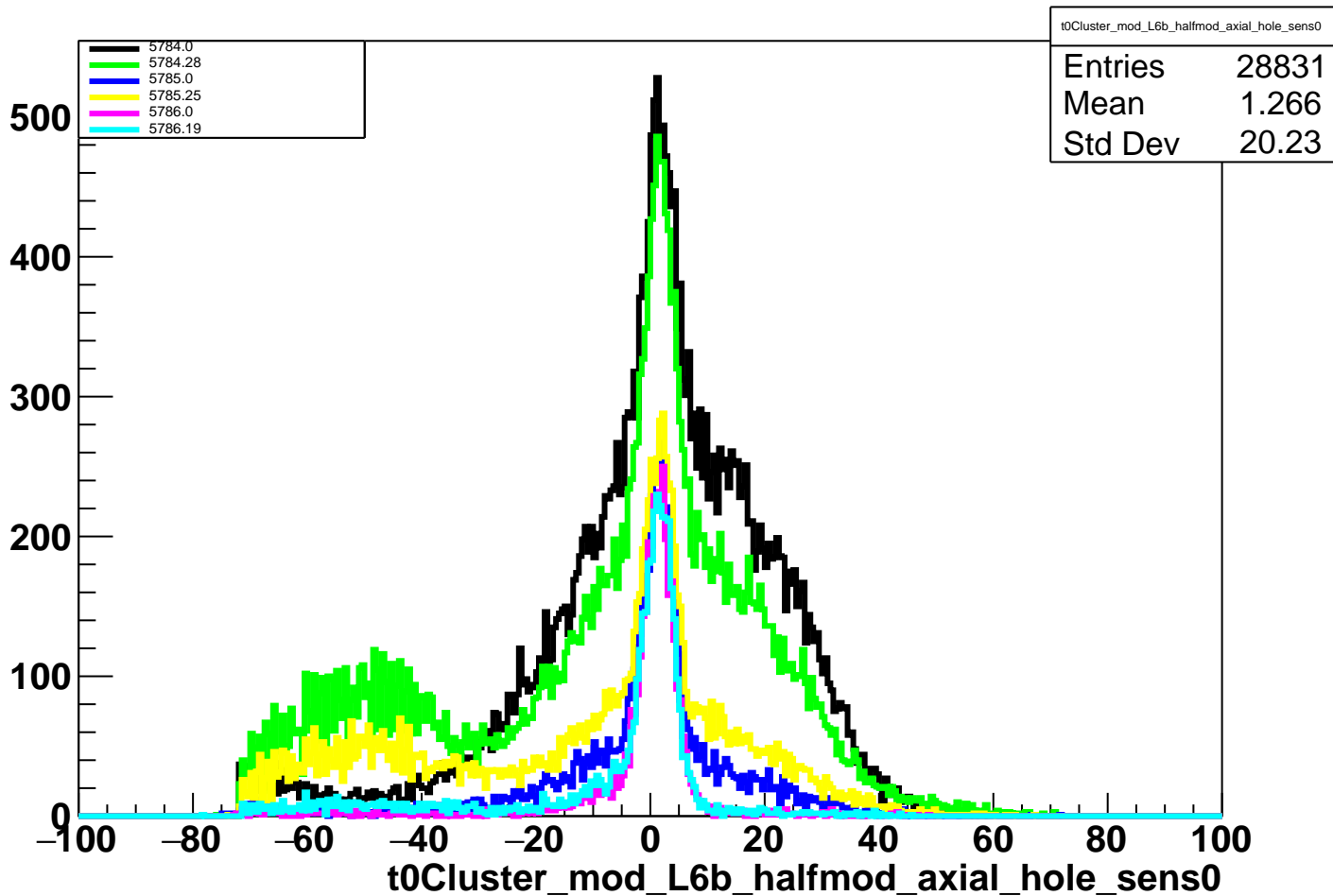
mod_L5t_halfmod_stereo_hole_sens0



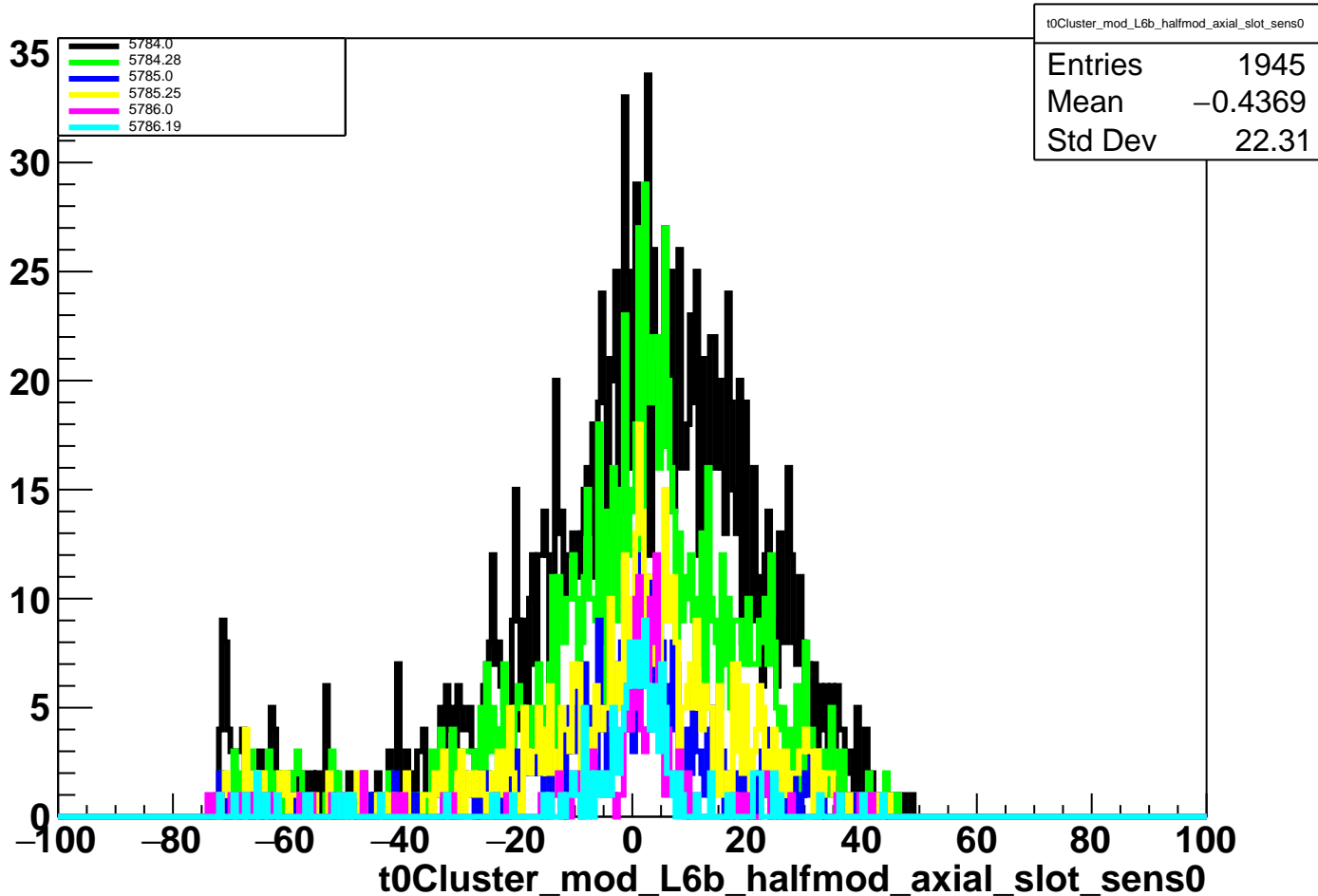
mod_L5t_halfmod_stereo_slot_sens0



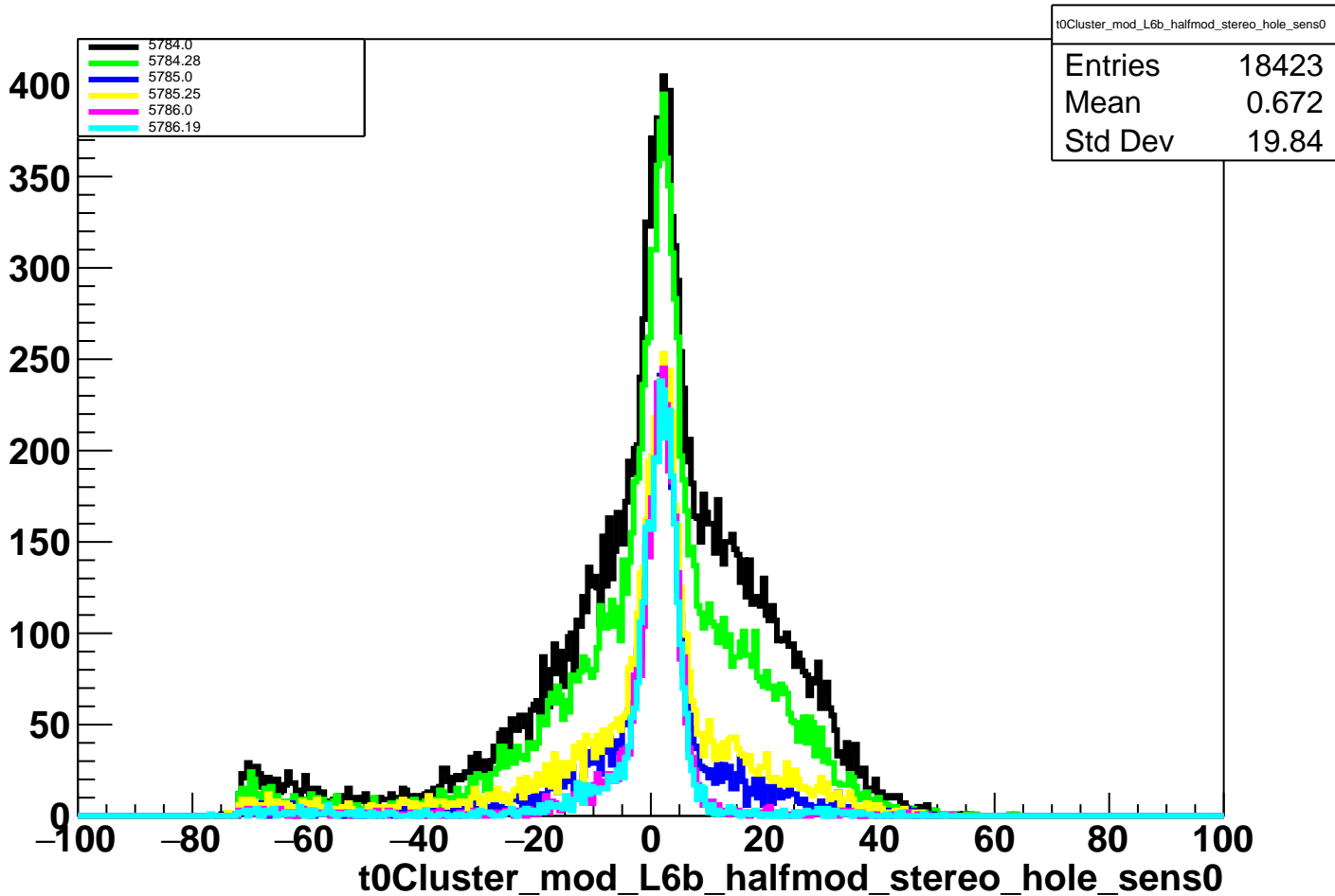
mod_L6b_halfmod_axial_hole_sens0



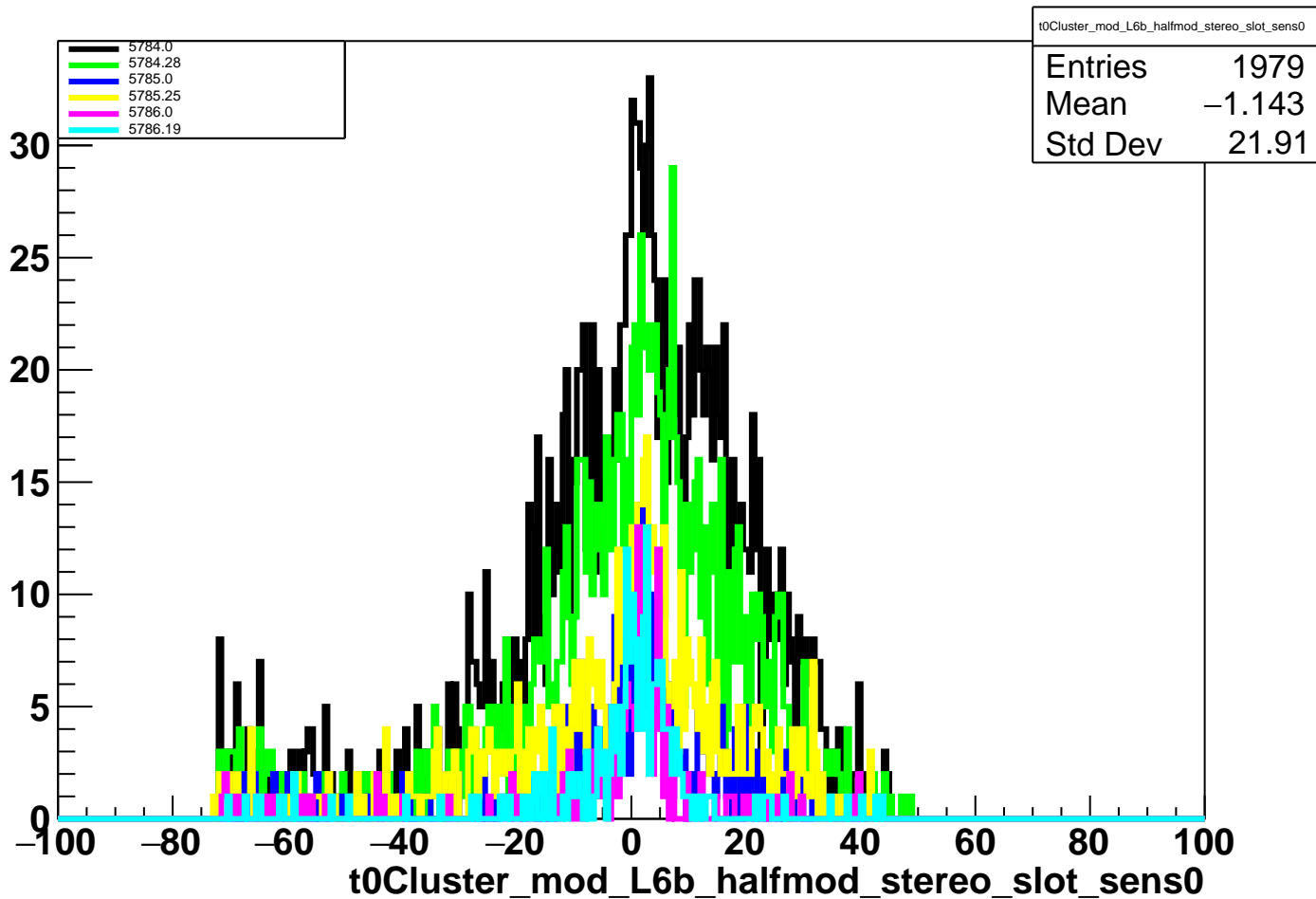
mod_L6b_halfmod_axial_slot_sens0



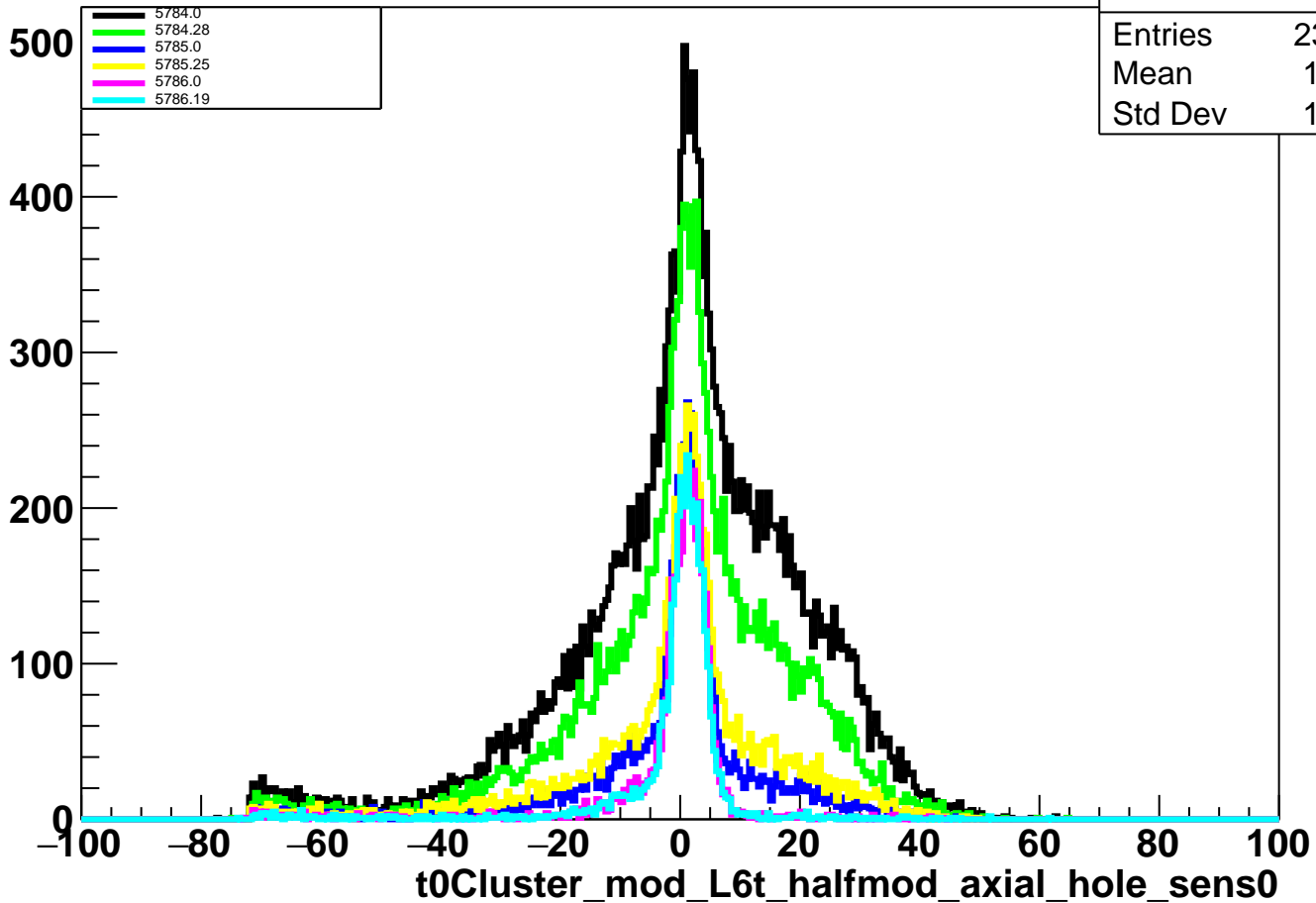
mod_L6b_halfmod_stereo_hole_sens0



mod_L6b_halfmod_stereo_slot_sens0



mod_L6t_halfmod_axial_hole_sens0



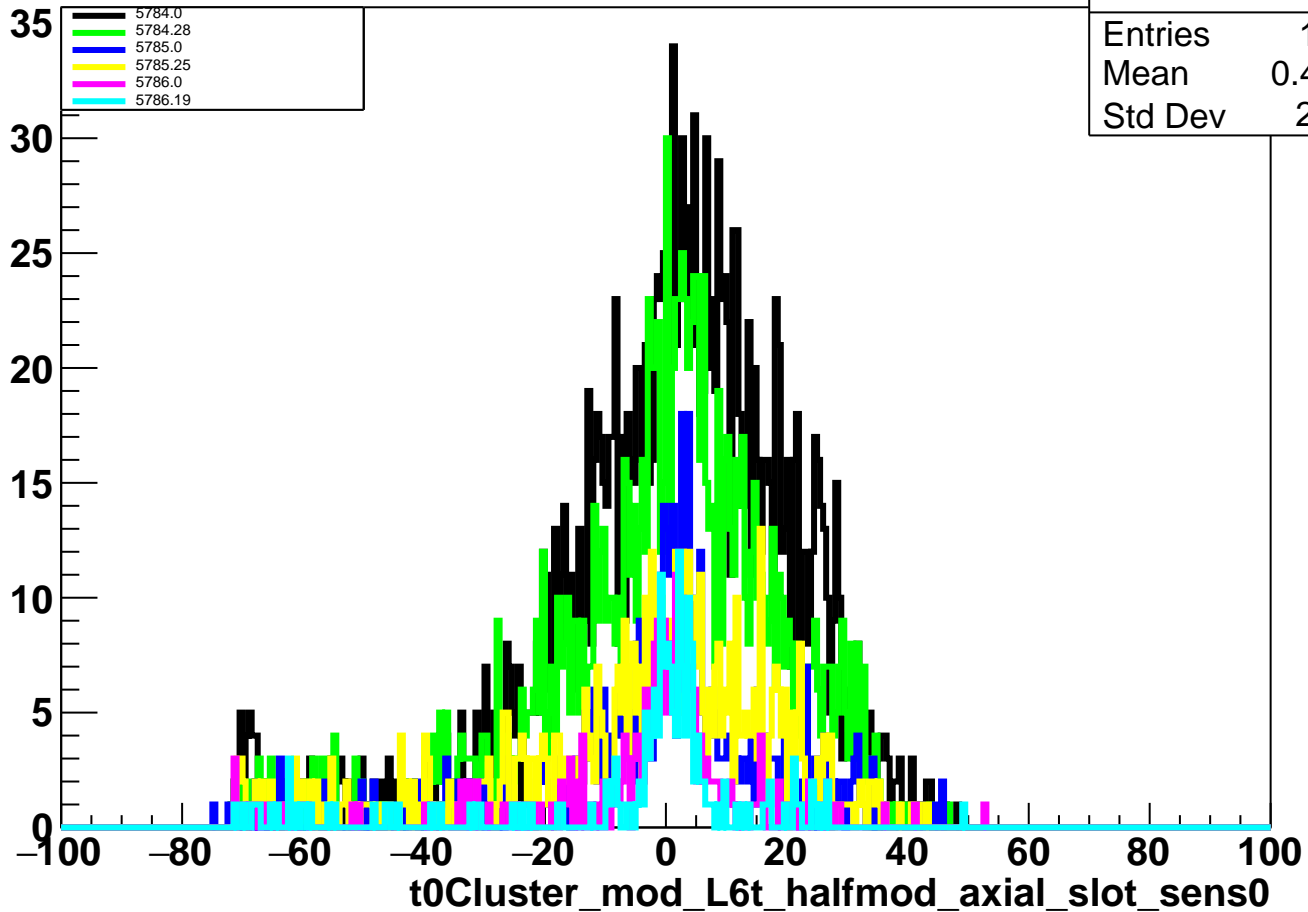
t0Cluster_mod_L6t_halfmod_axial_hole_sens0

Entries 23676

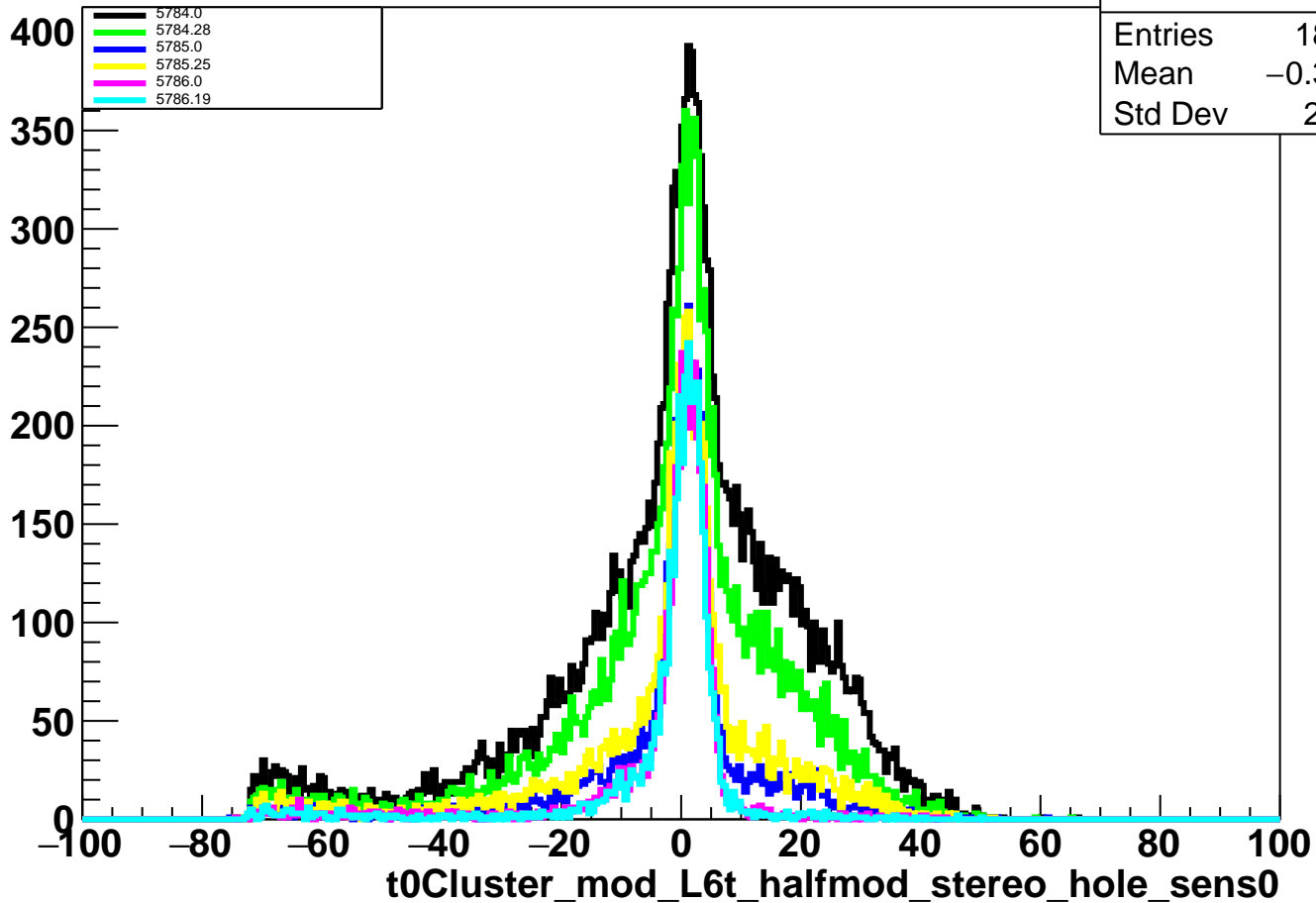
Mean 1.276

Std Dev 19.06

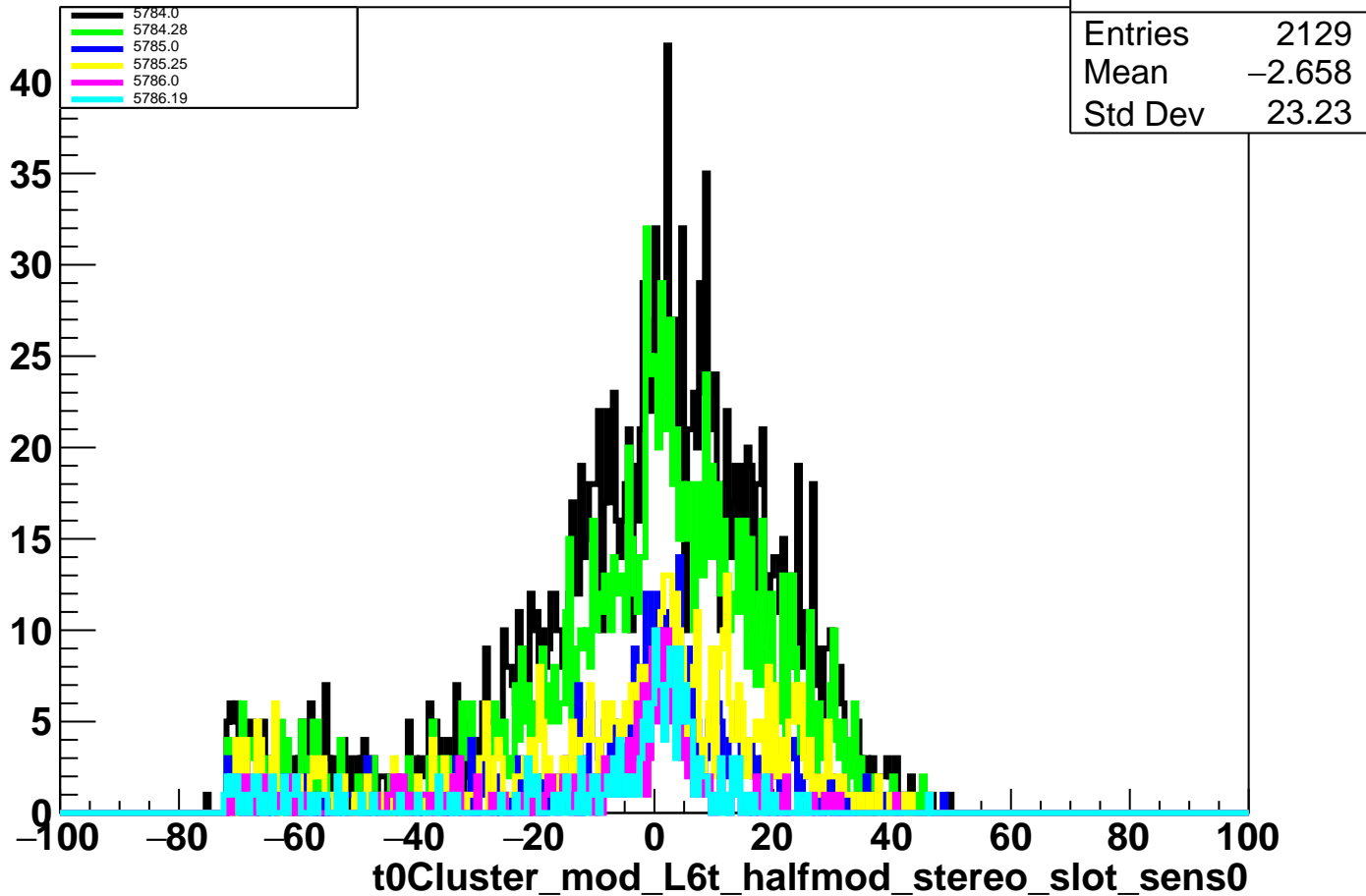
mod_L6t_halfmod_axial_slot_sens0



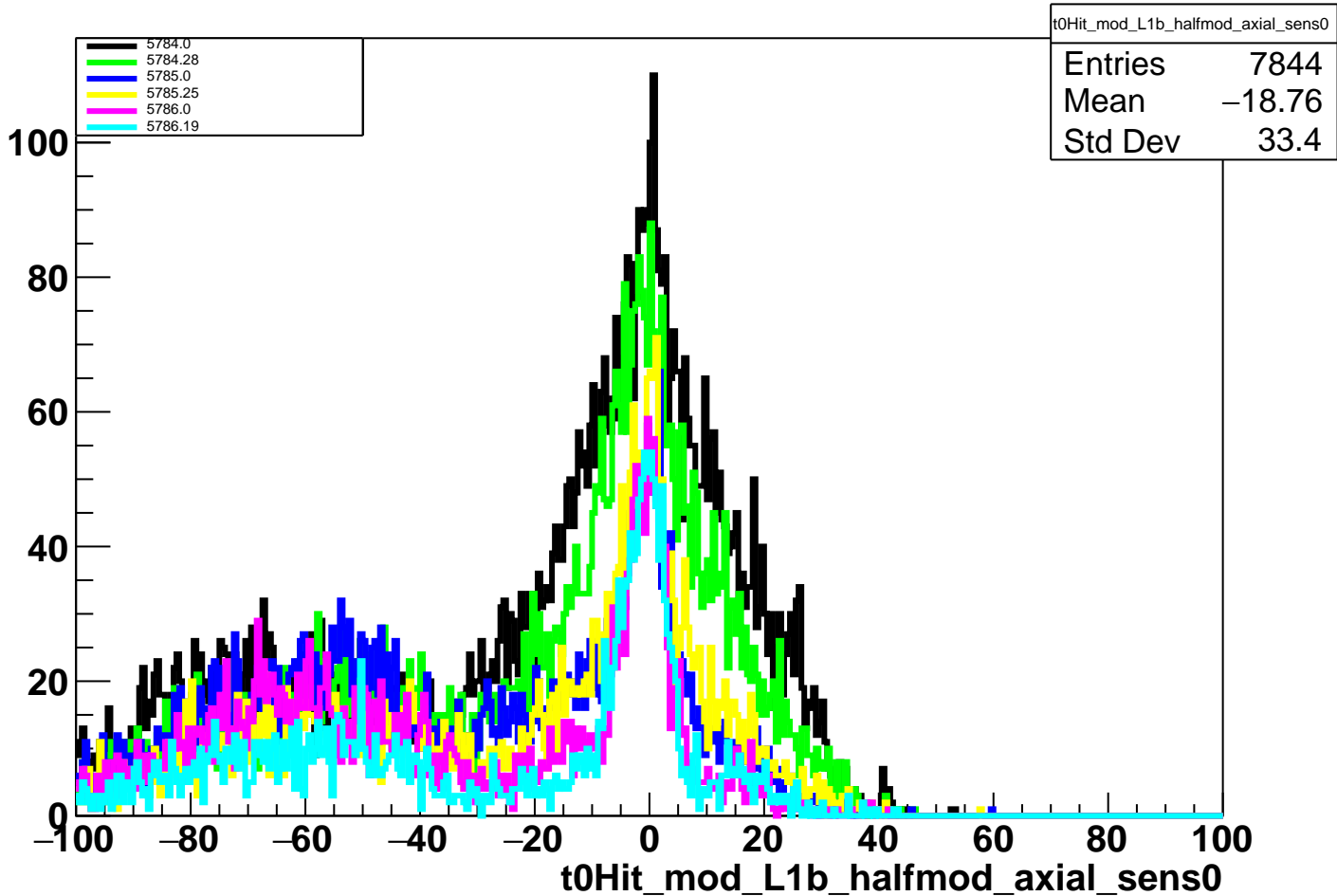
mod_L6t_halfmod_stereo_hole_sens0



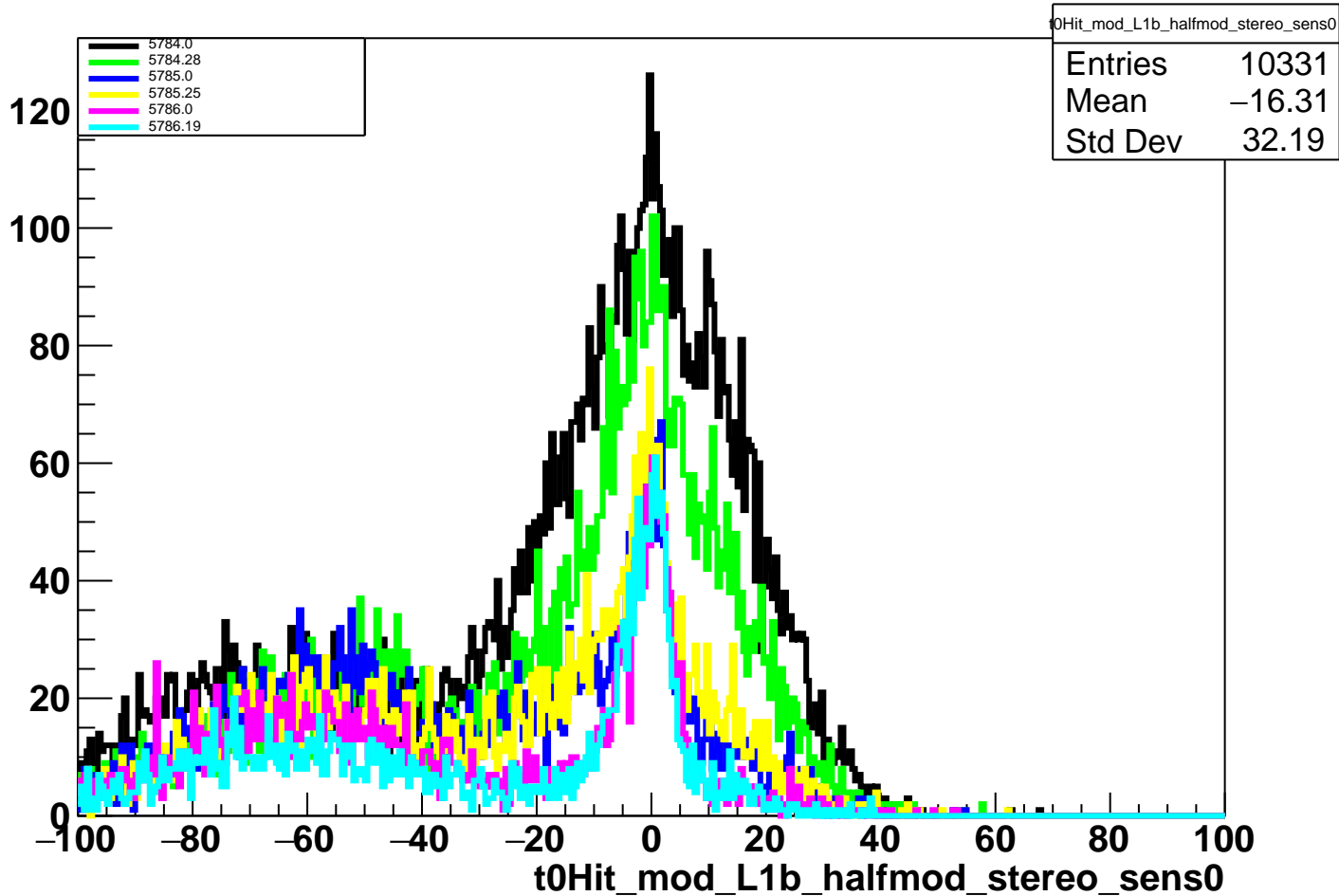
mod_L6t_halfmod_stereo_slot_sens0



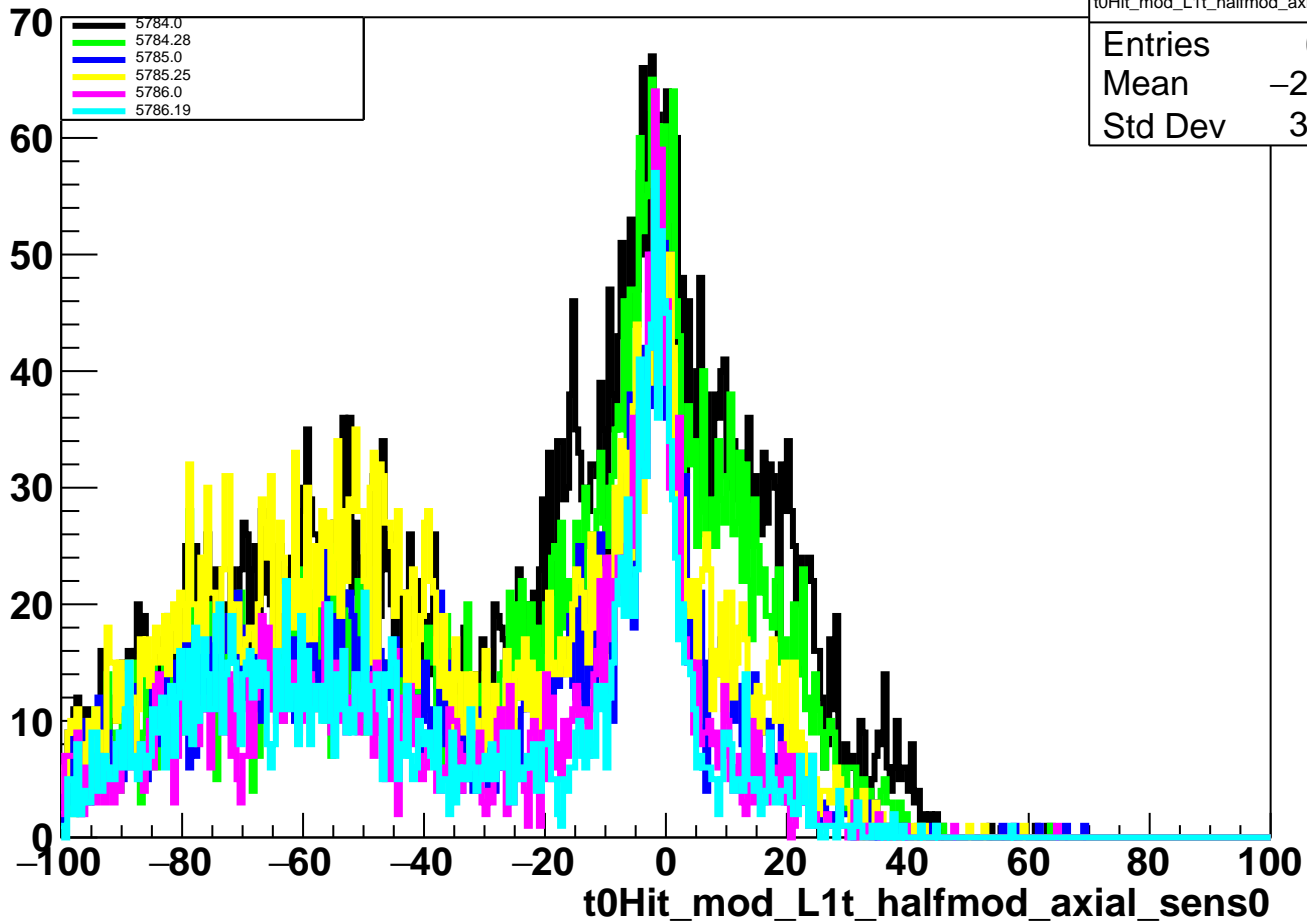
mod_L1b_halfmod_axial_sens0



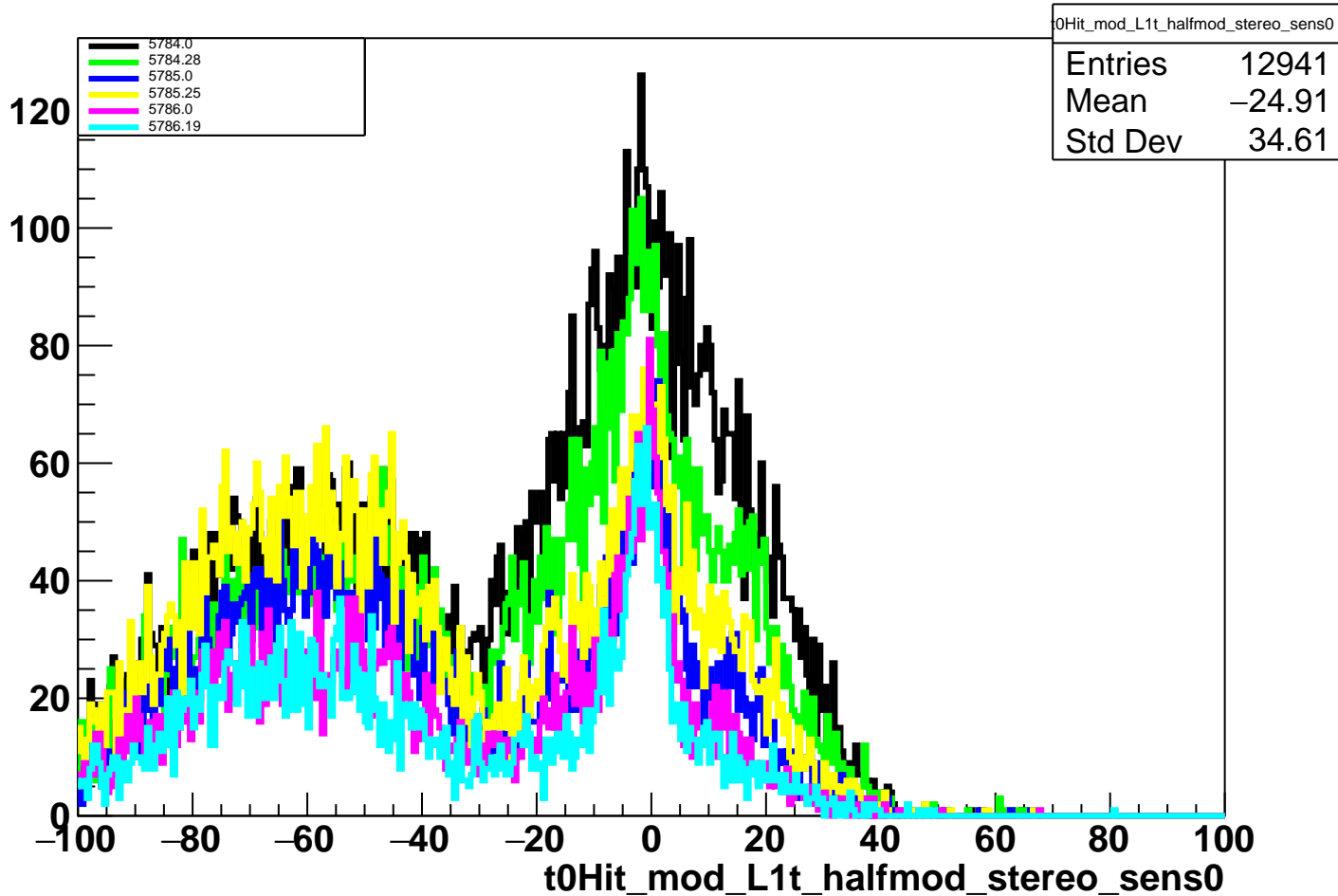
mod_L1b_halfmod_stereo_sens0



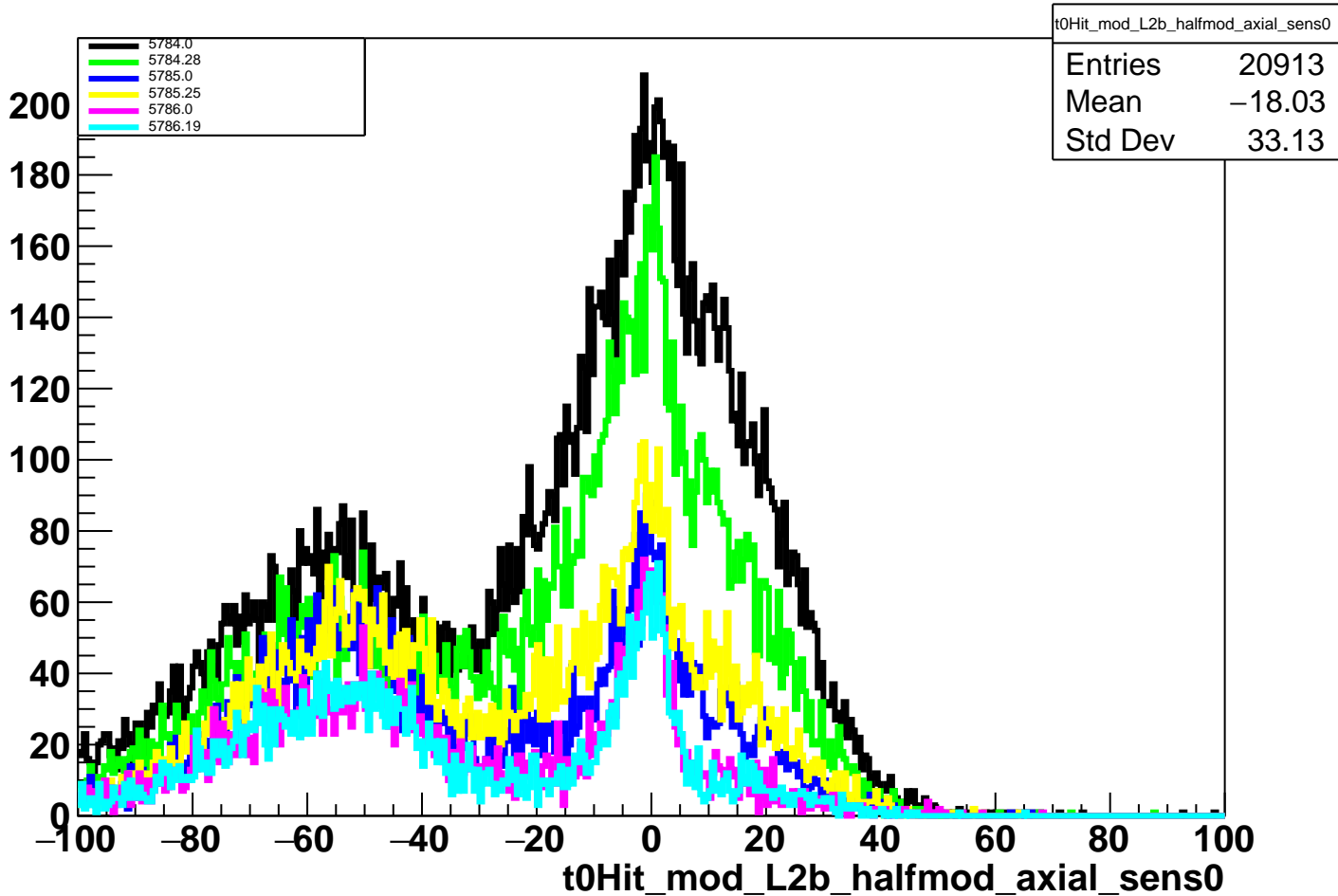
mod_L1t_halfmod_axial_sens0



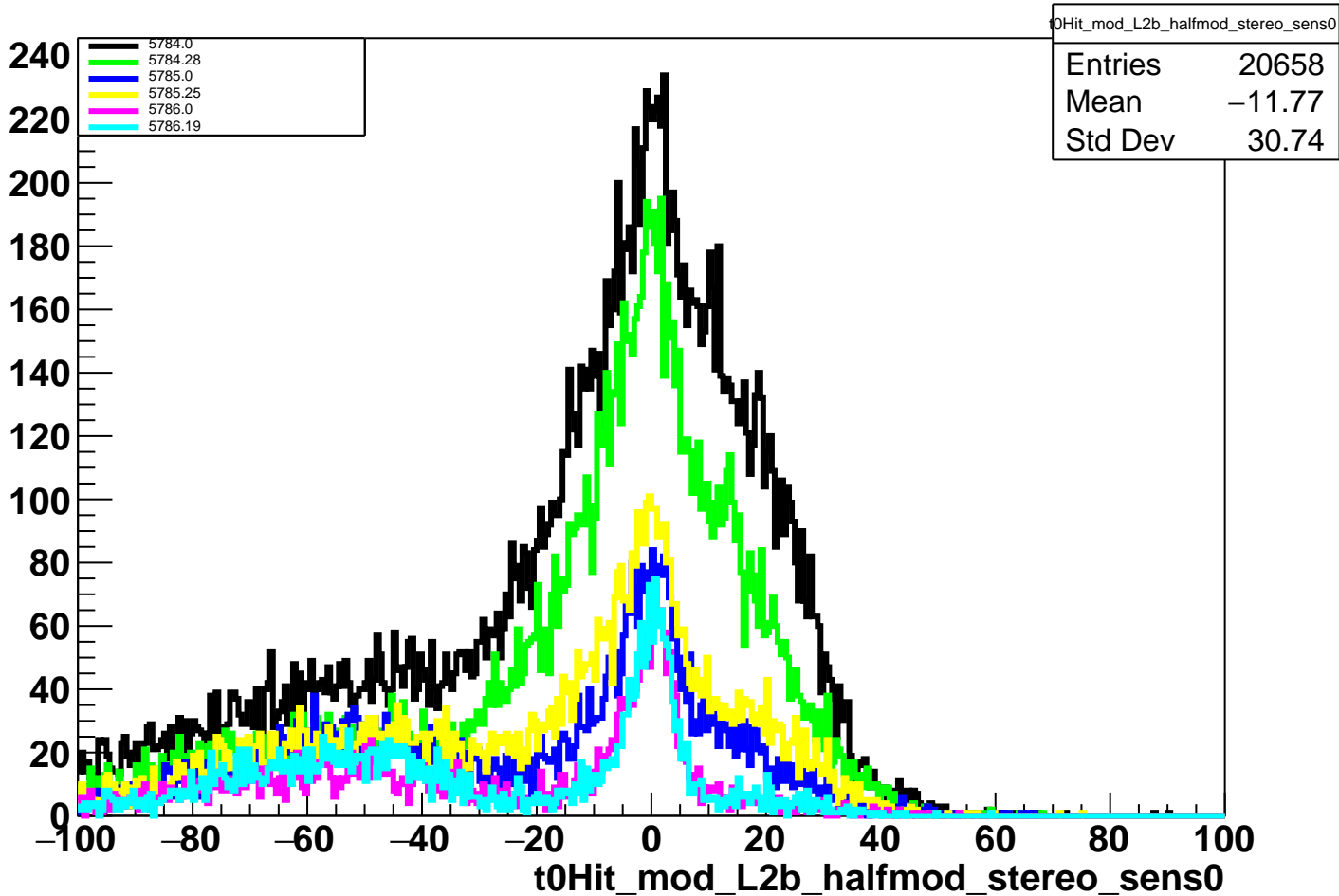
mod_L1t_halfmod_stereo_sens0



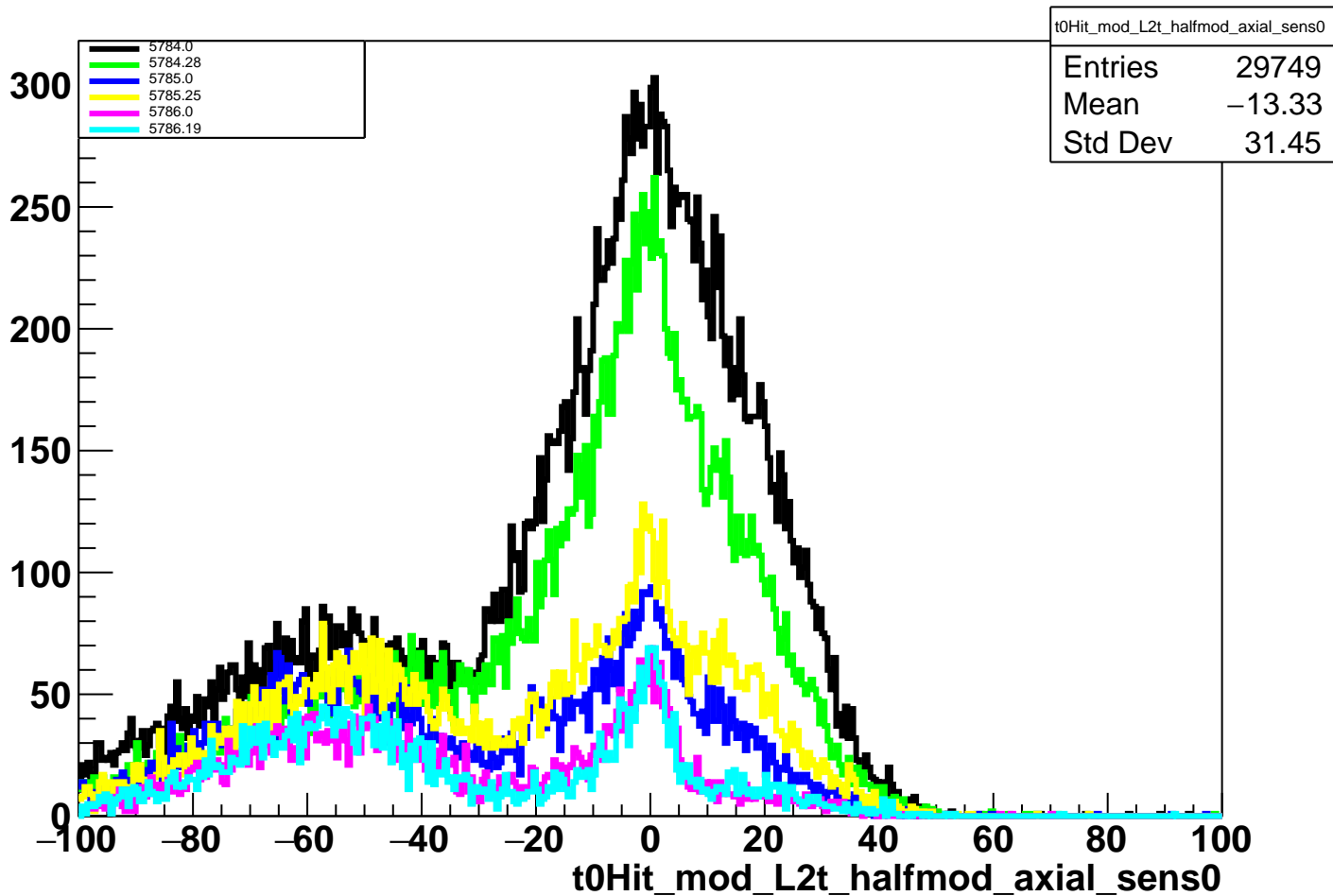
mod_L2b_halfmod_axial_sens0



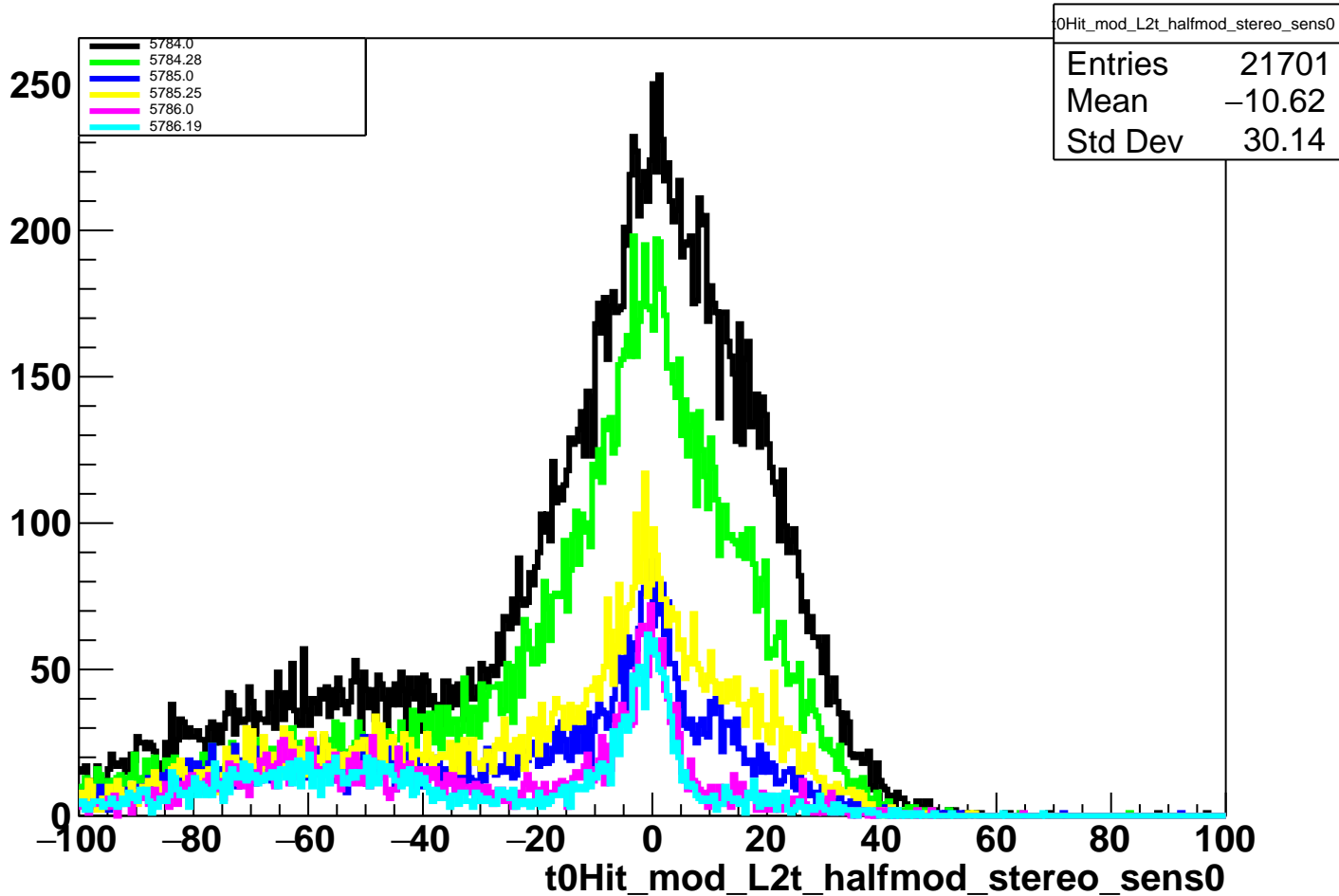
mod_L2b_halfmod_stereo_sens0



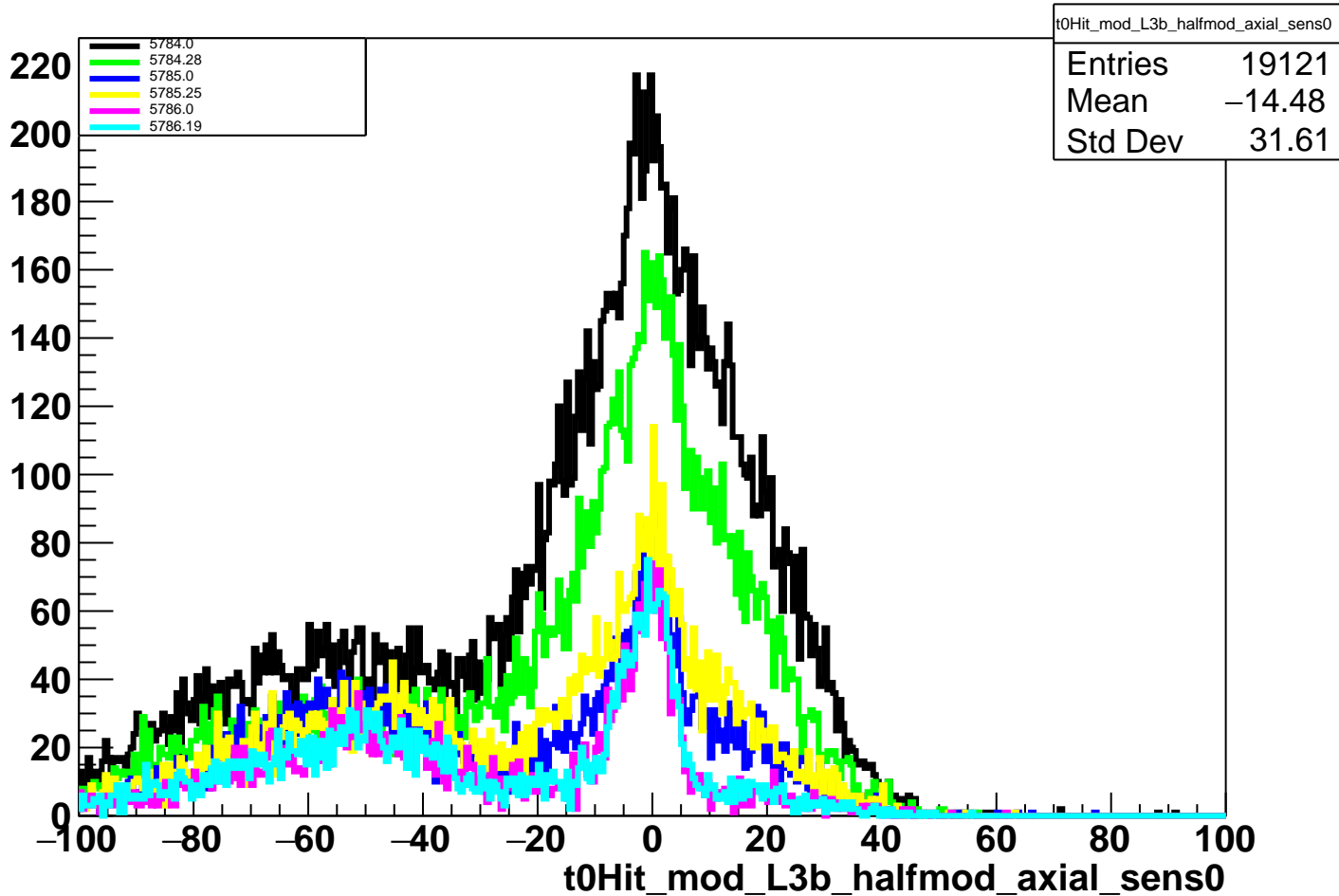
mod_L2t_halfmod_axial_sens0



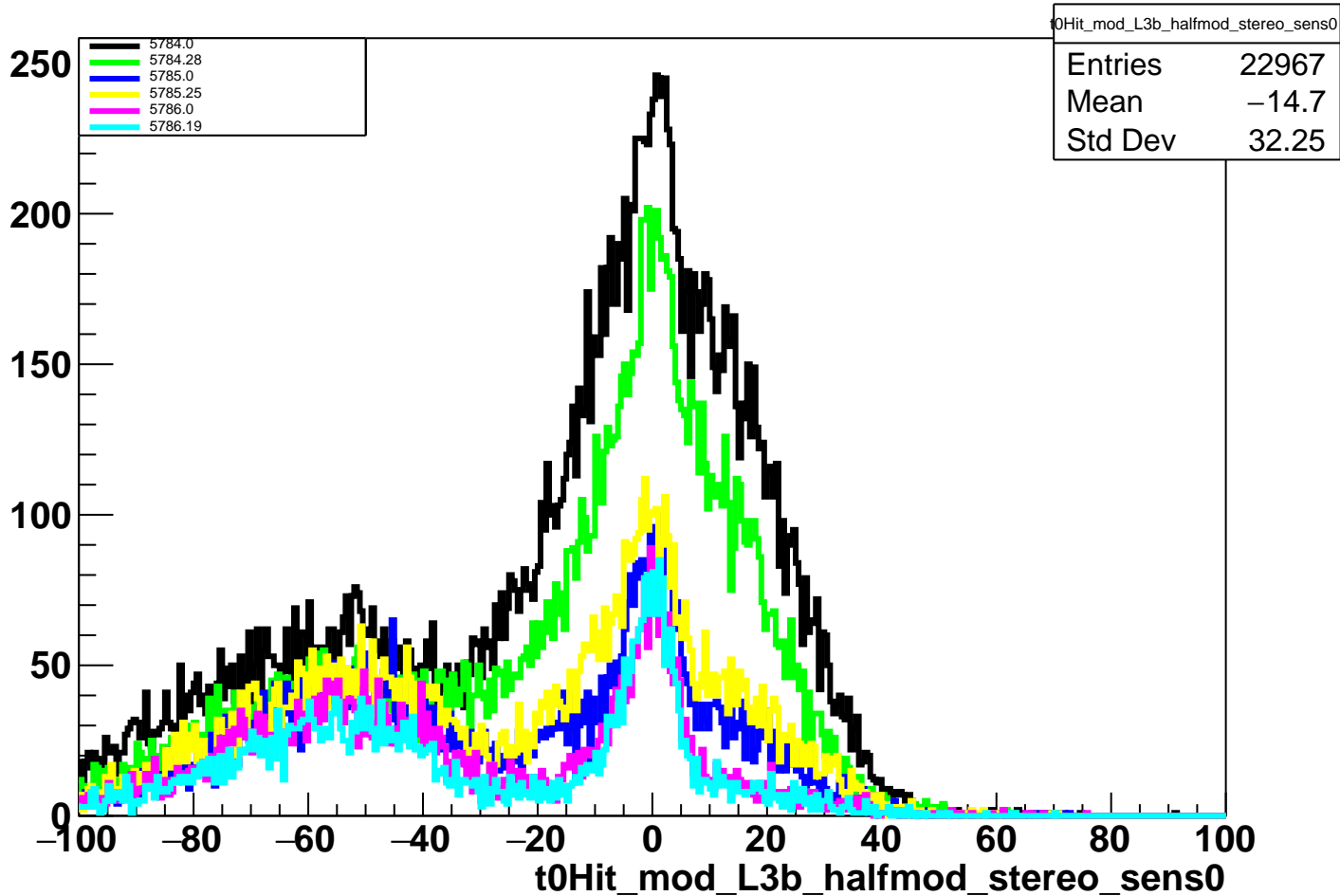
mod_L2t_halfmod_stereo_sens0



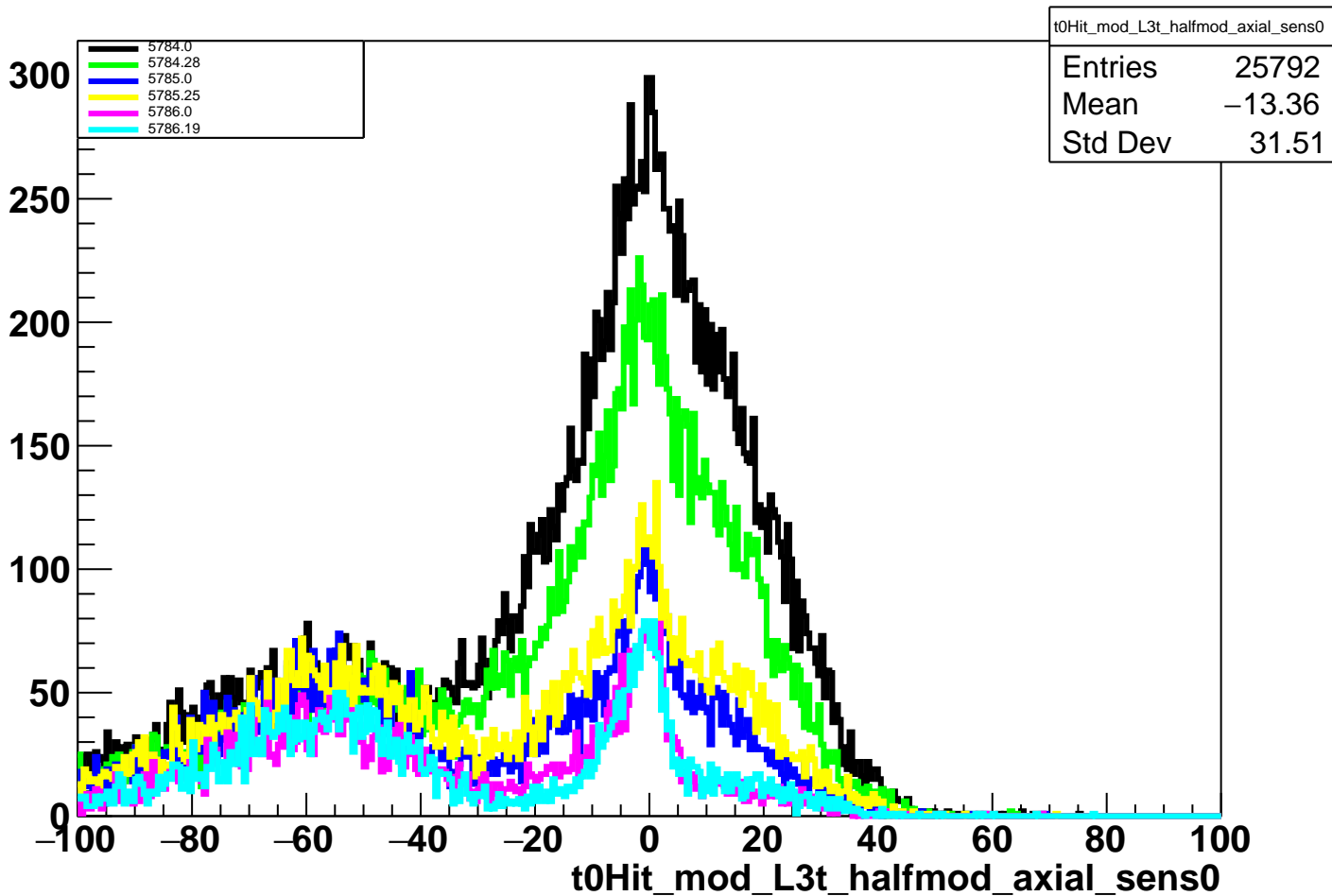
mod_L3b_halfmod_axial_sens0



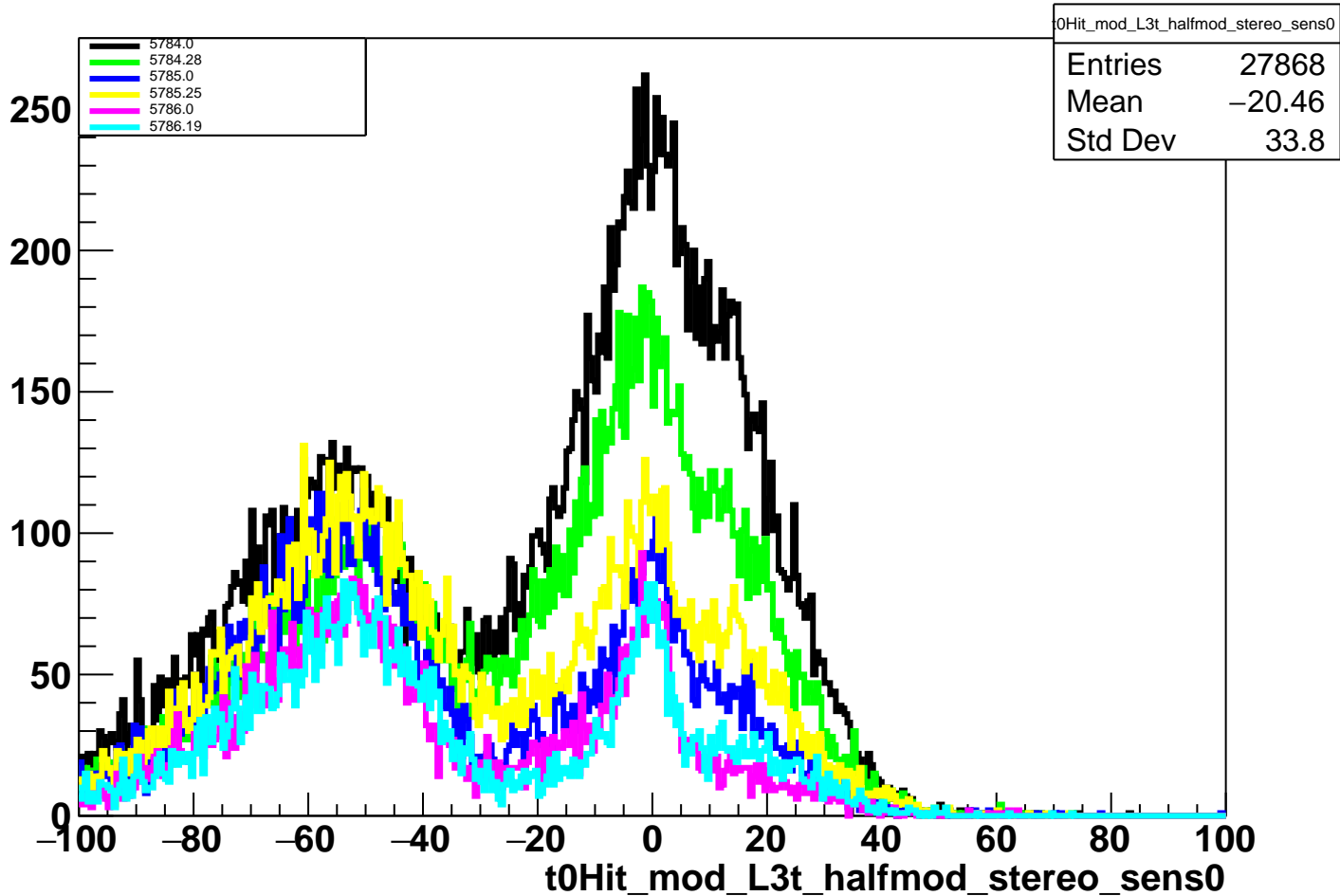
mod_L3b_halfmod_stereo_sens0



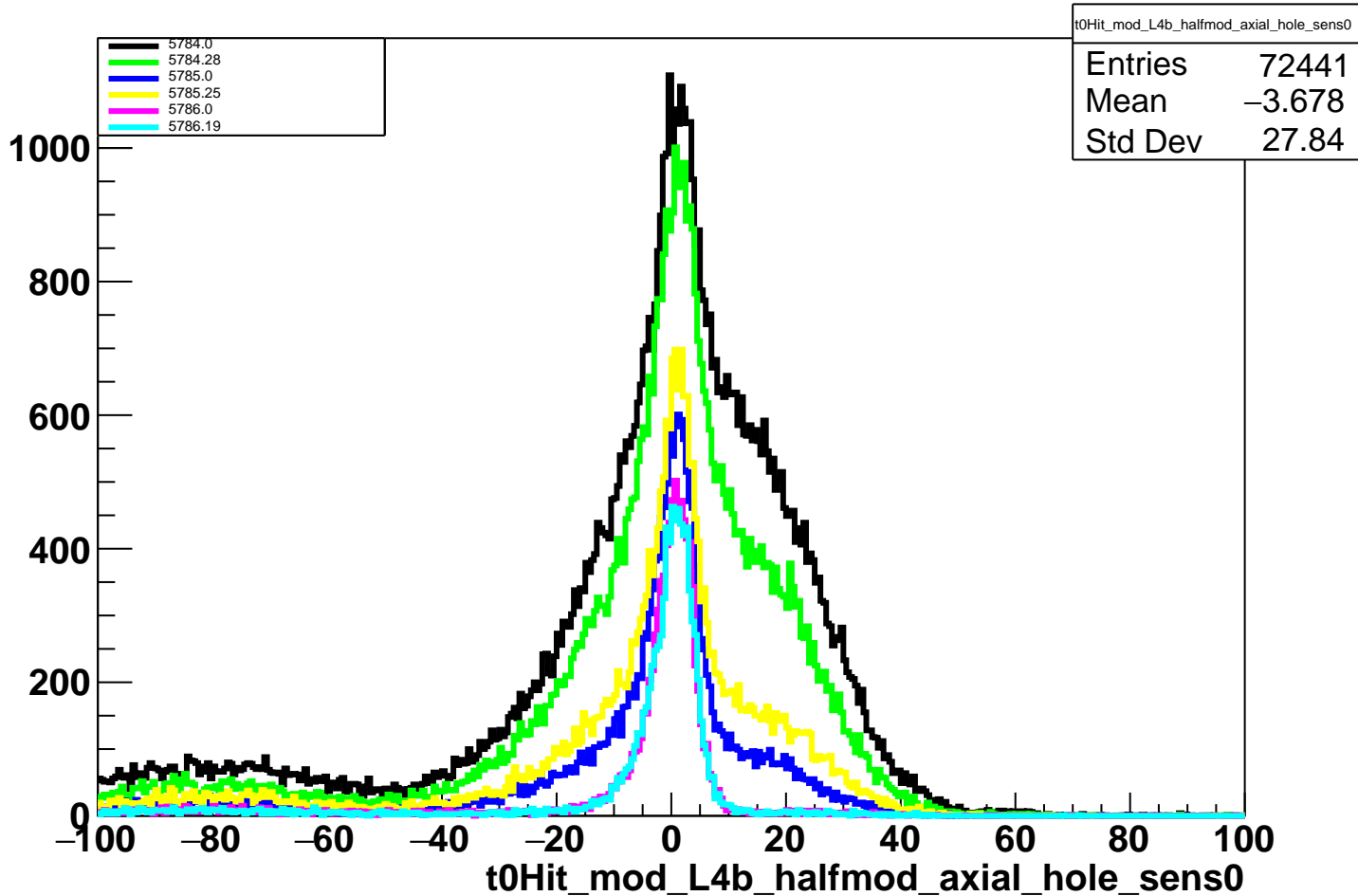
mod_L3t_halfmod_axial_sens0



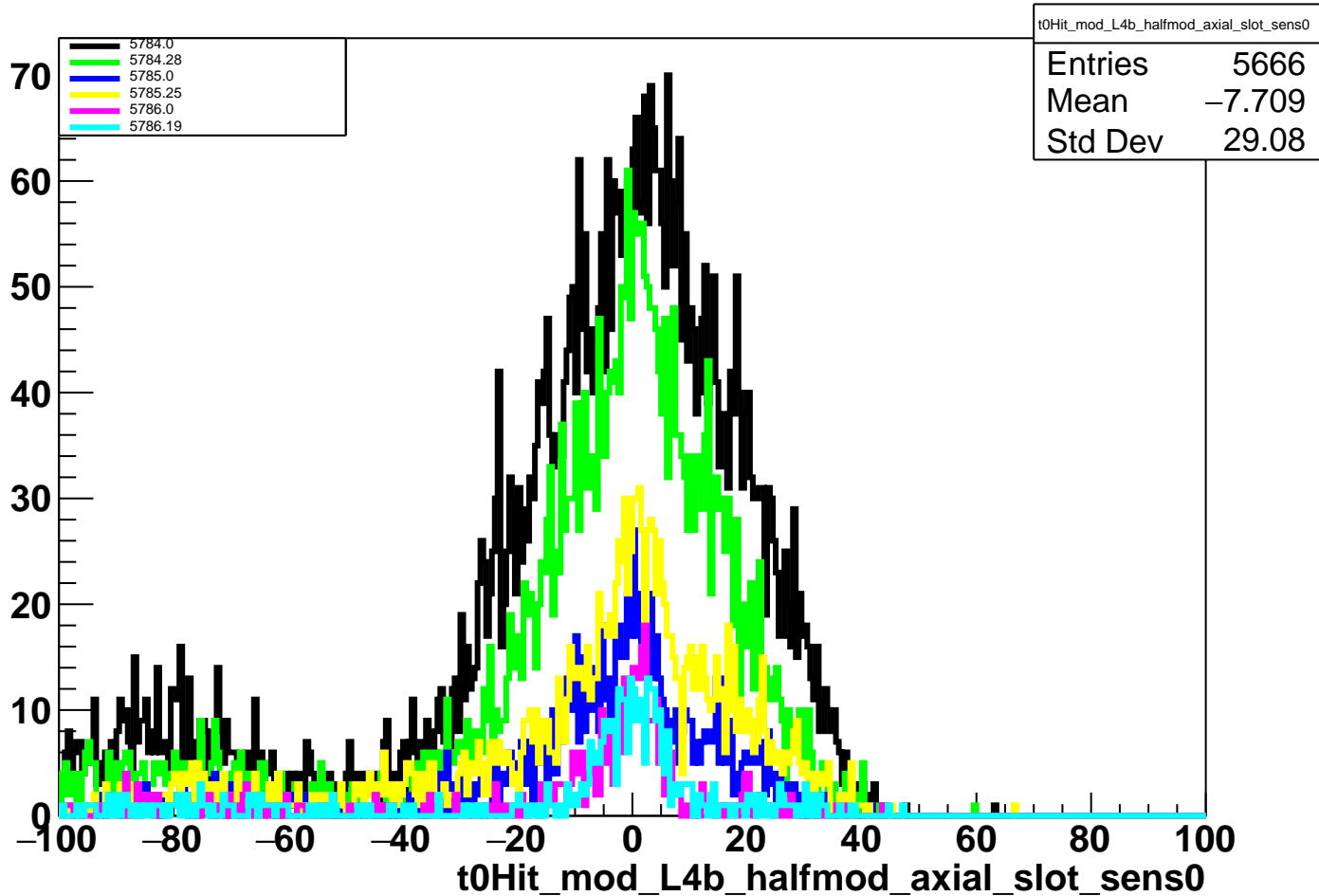
mod_L3t_halfmod_stereo_sens0



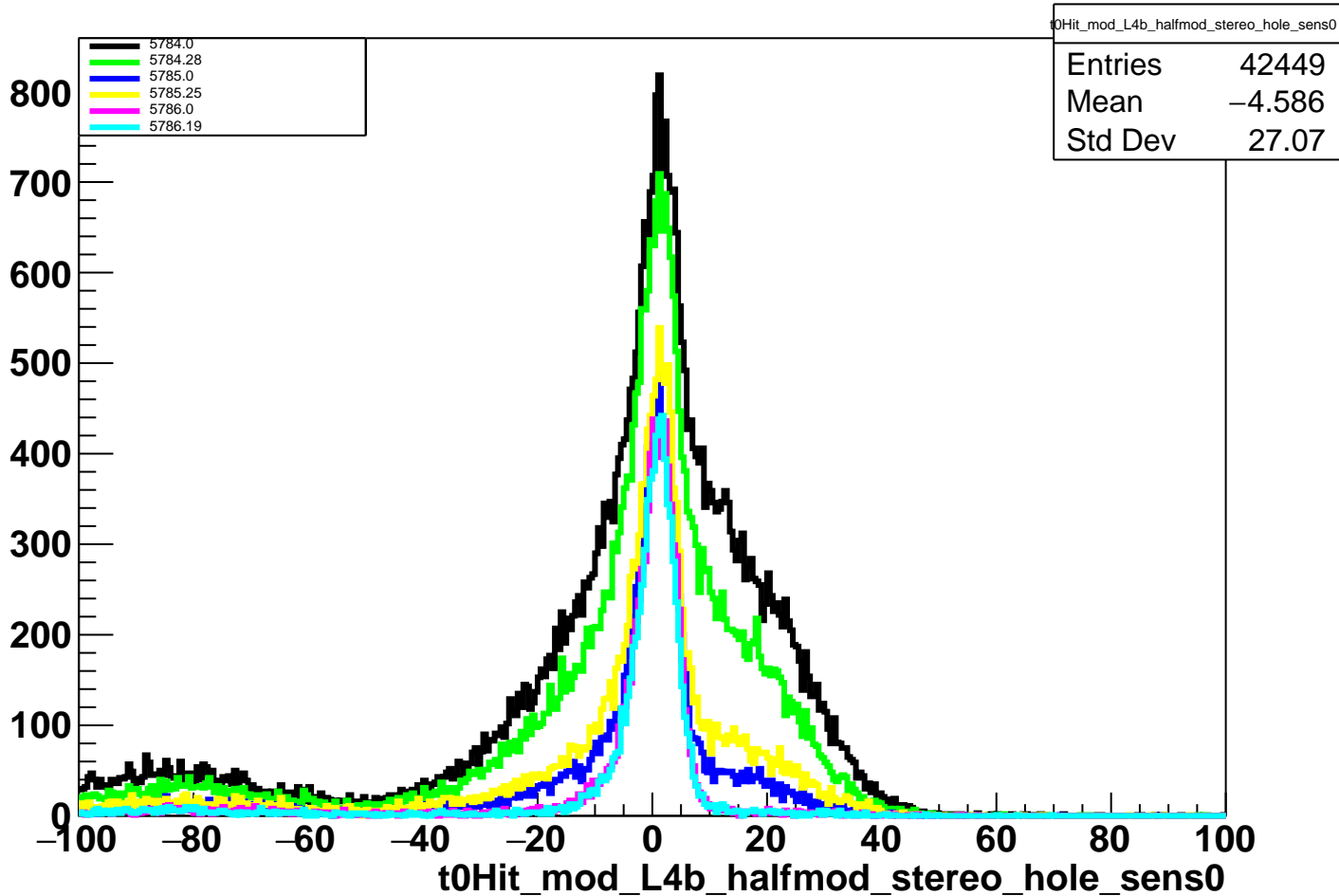
mod_L4b_halfmod_axial_hole_sens0



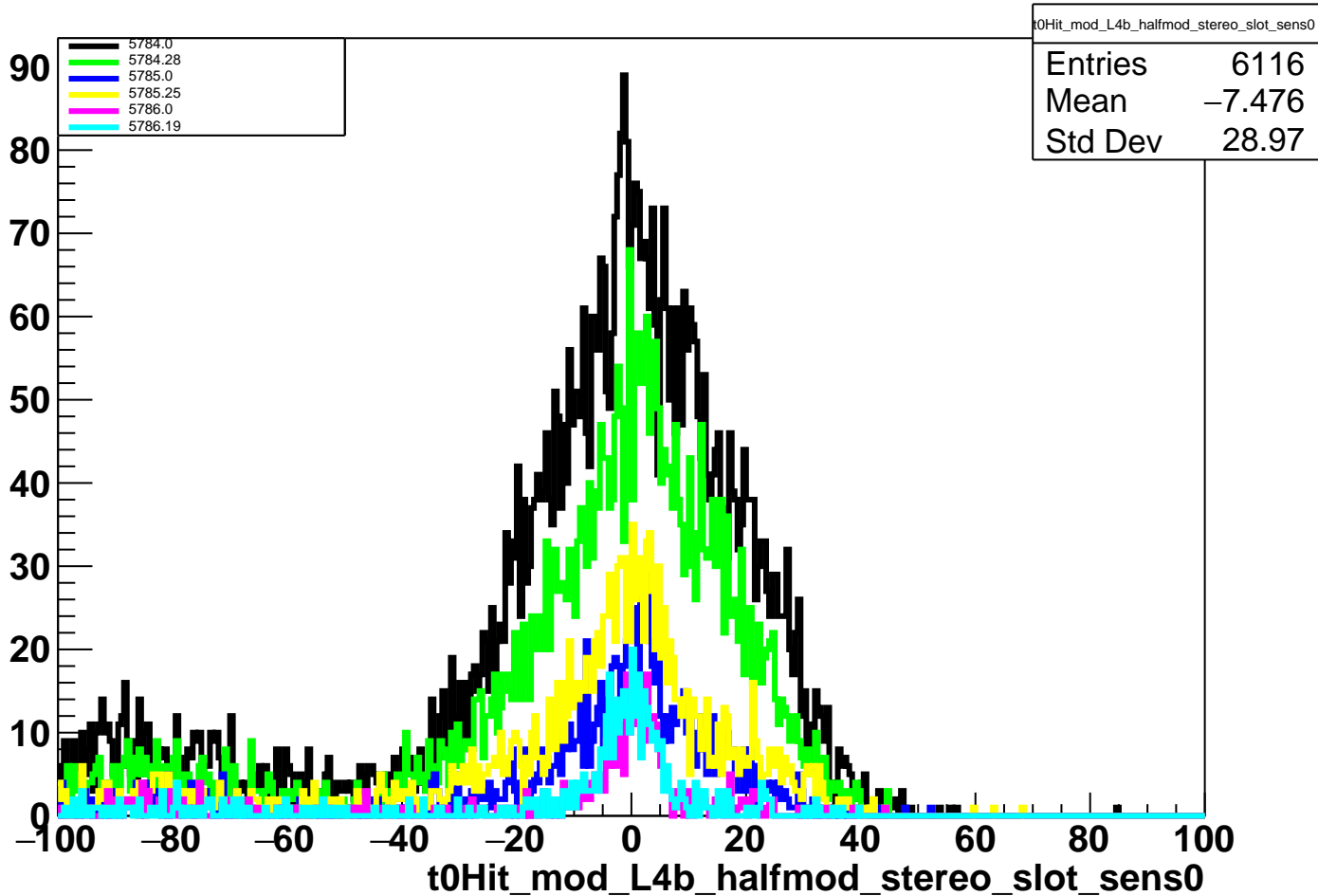
mod_L4b_halfmod_axial_slot_sens0



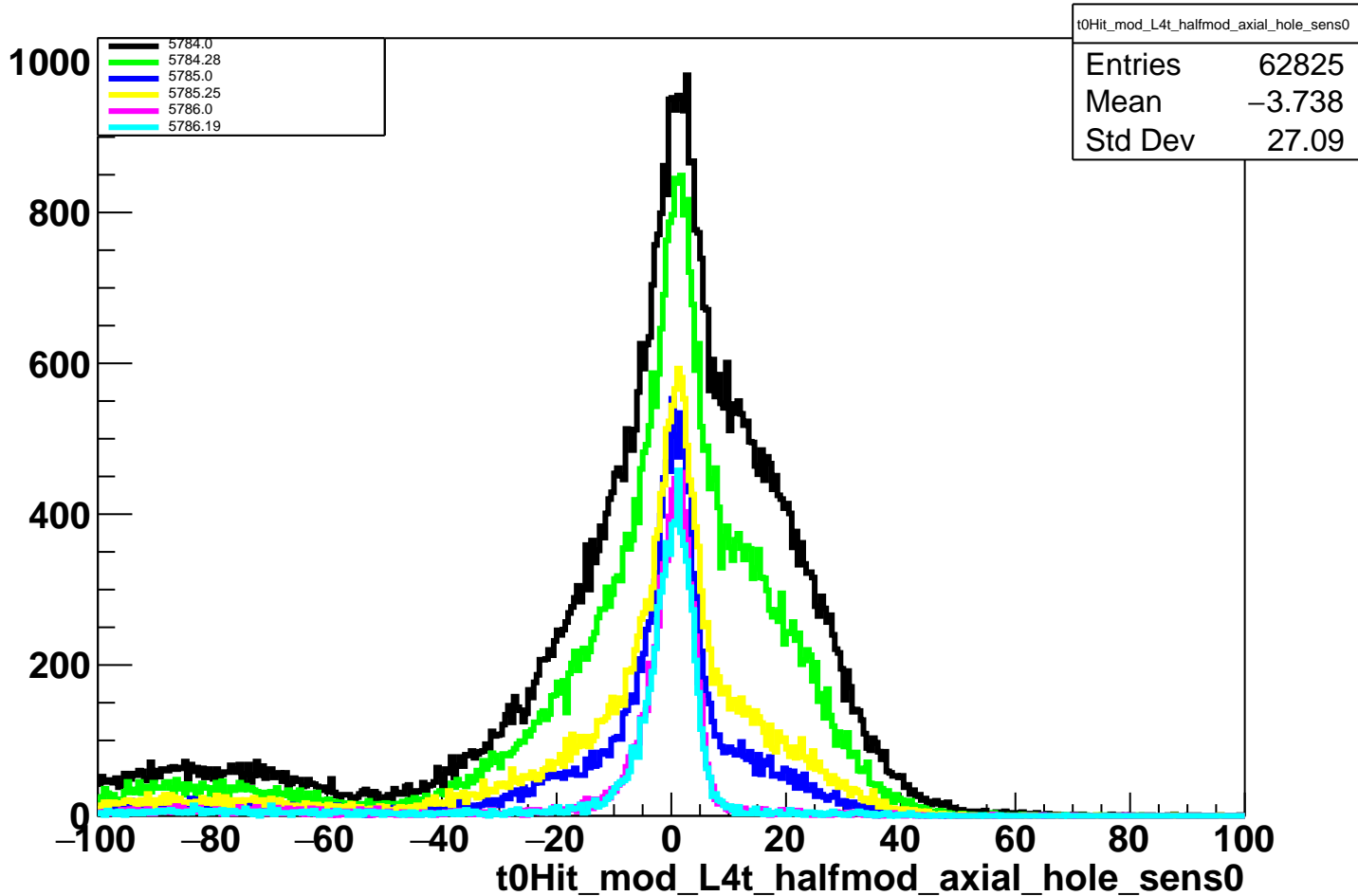
mod_L4b_halfmod_stereo_hole_sens0



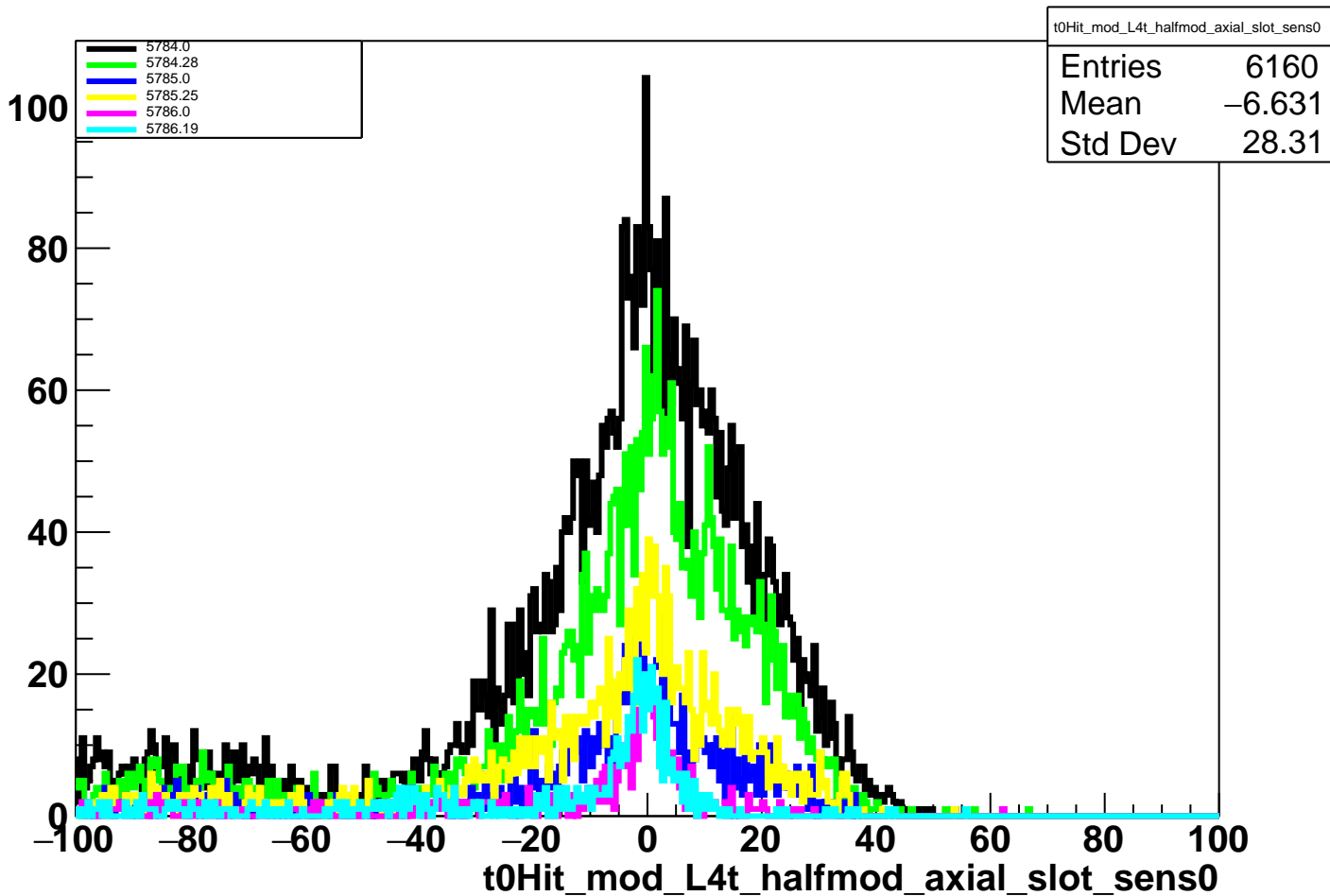
mod_L4b_halfmod_stereo_slot_sens0



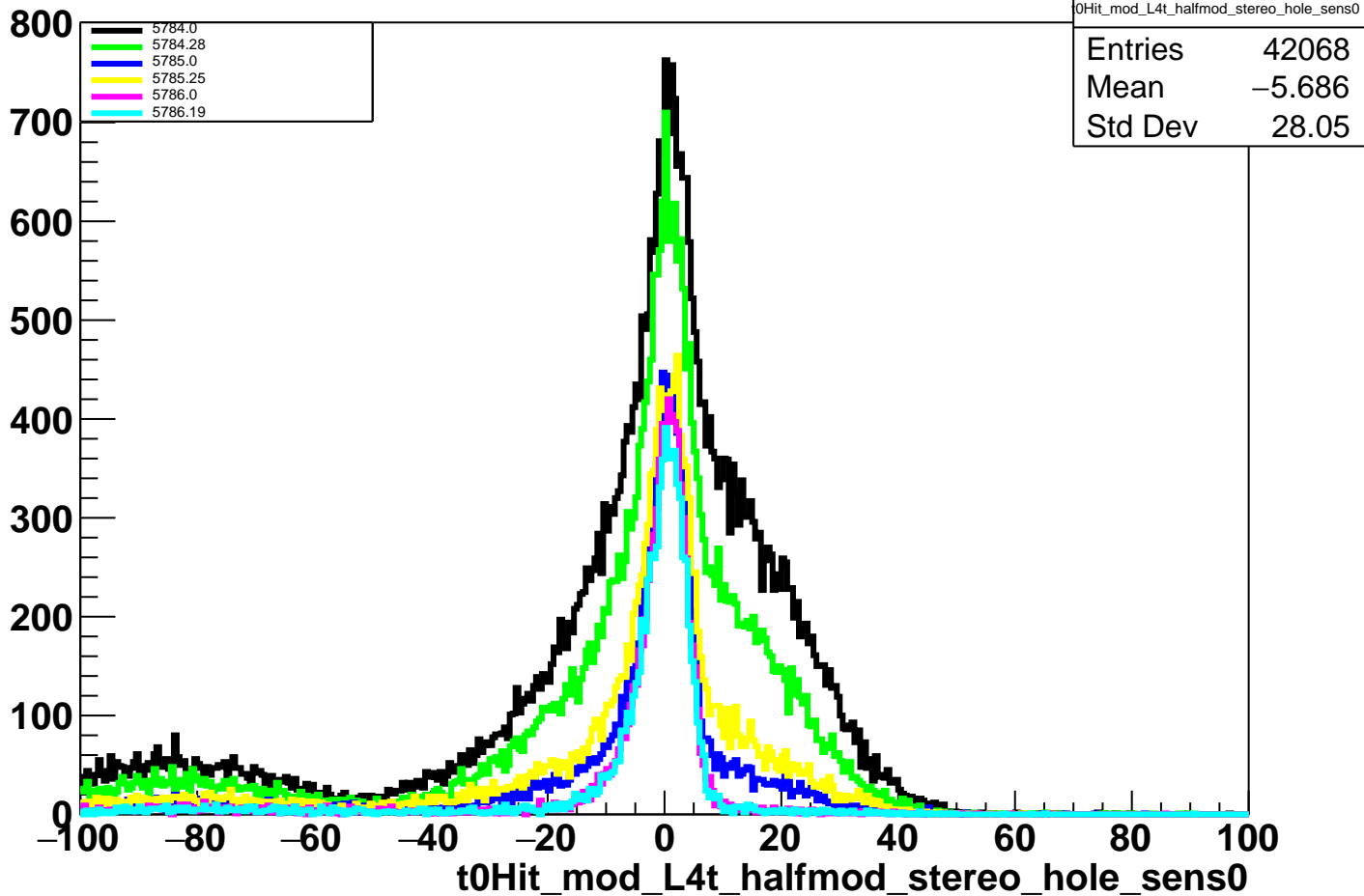
mod_L4t_halfmod_axial_hole_sens0



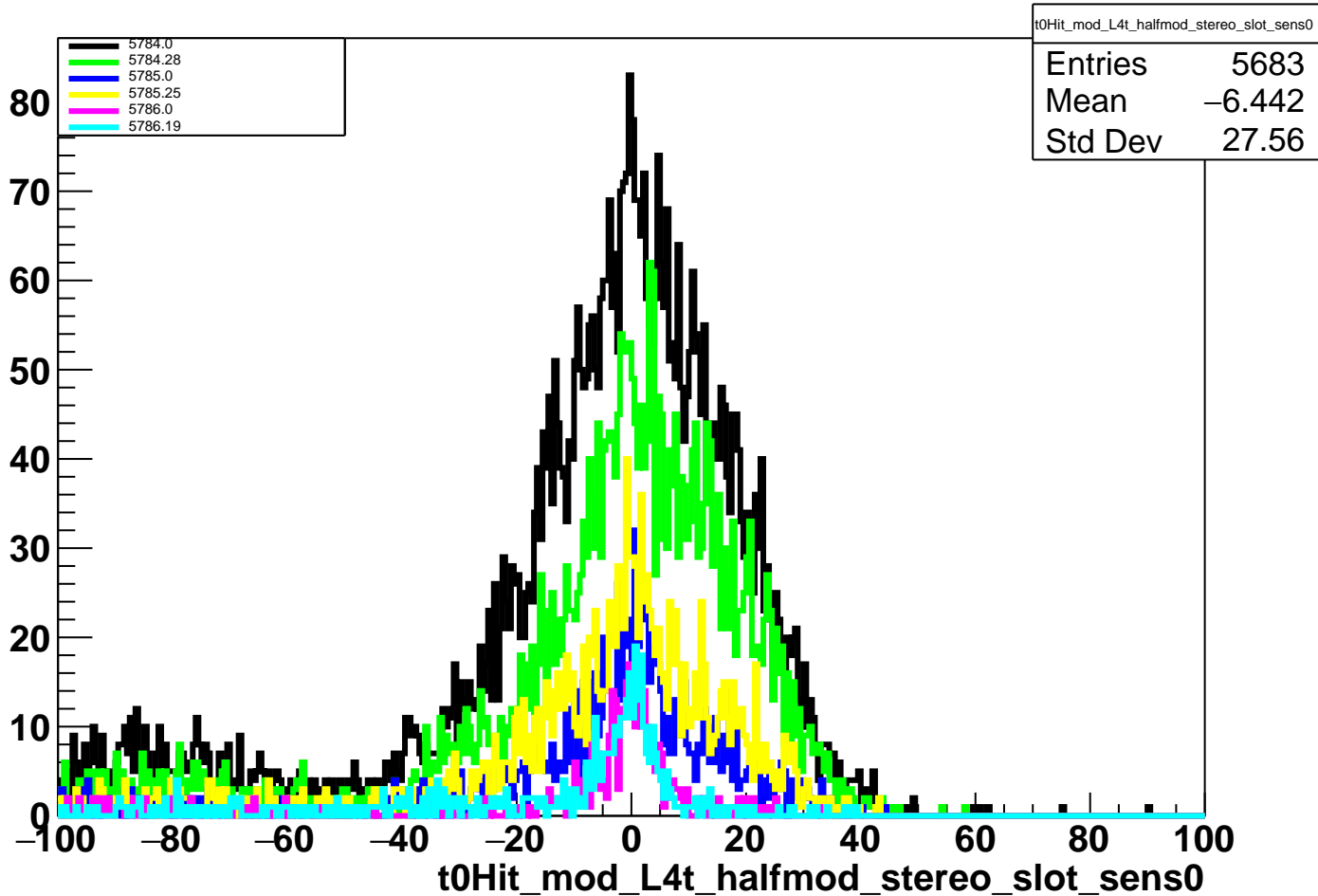
mod_L4t_halfmod_axial_slot_sens0



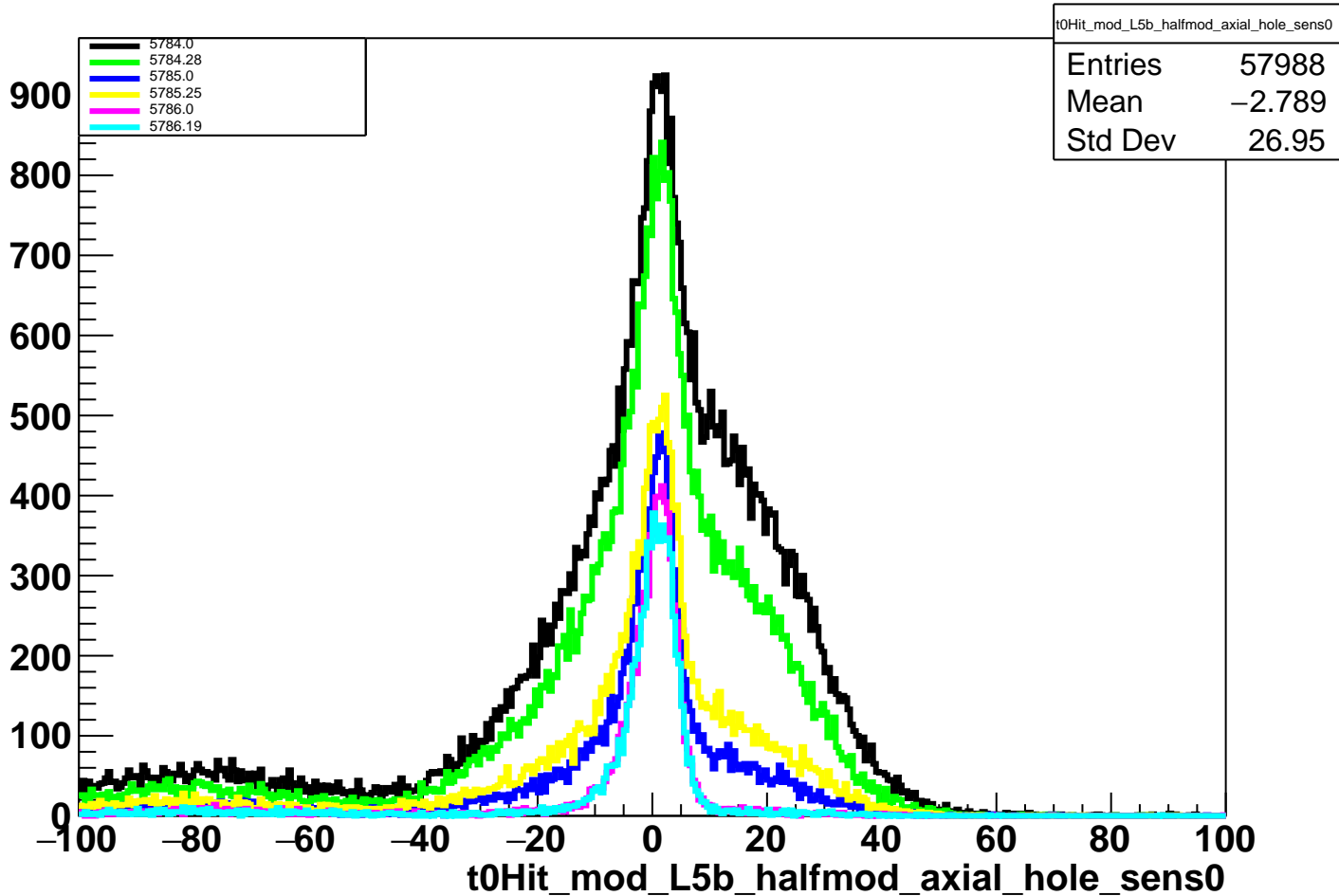
mod_L4t_halfmod_stereo_hole_sens0



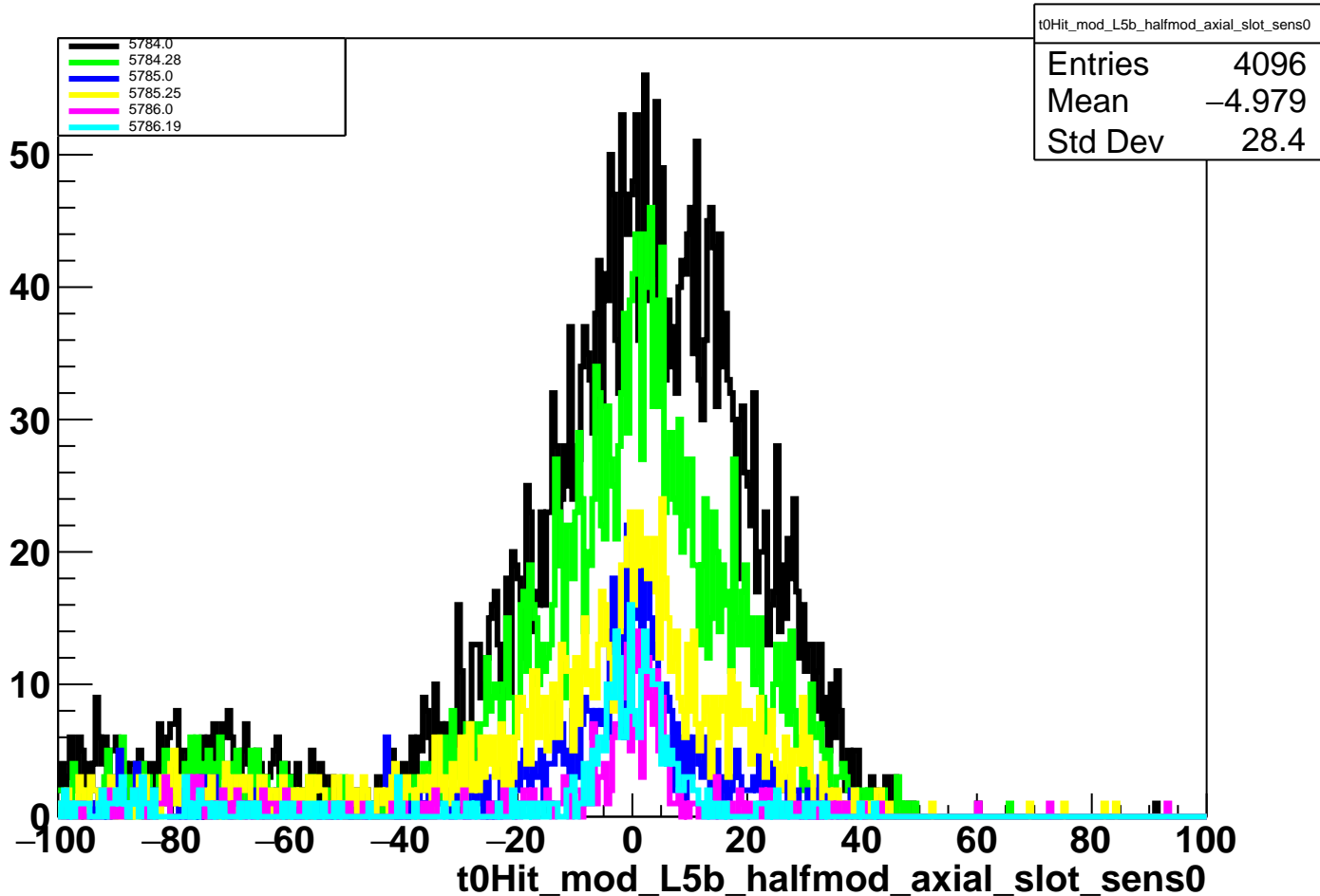
mod_L4t_halfmod_stereo_slot_sens0



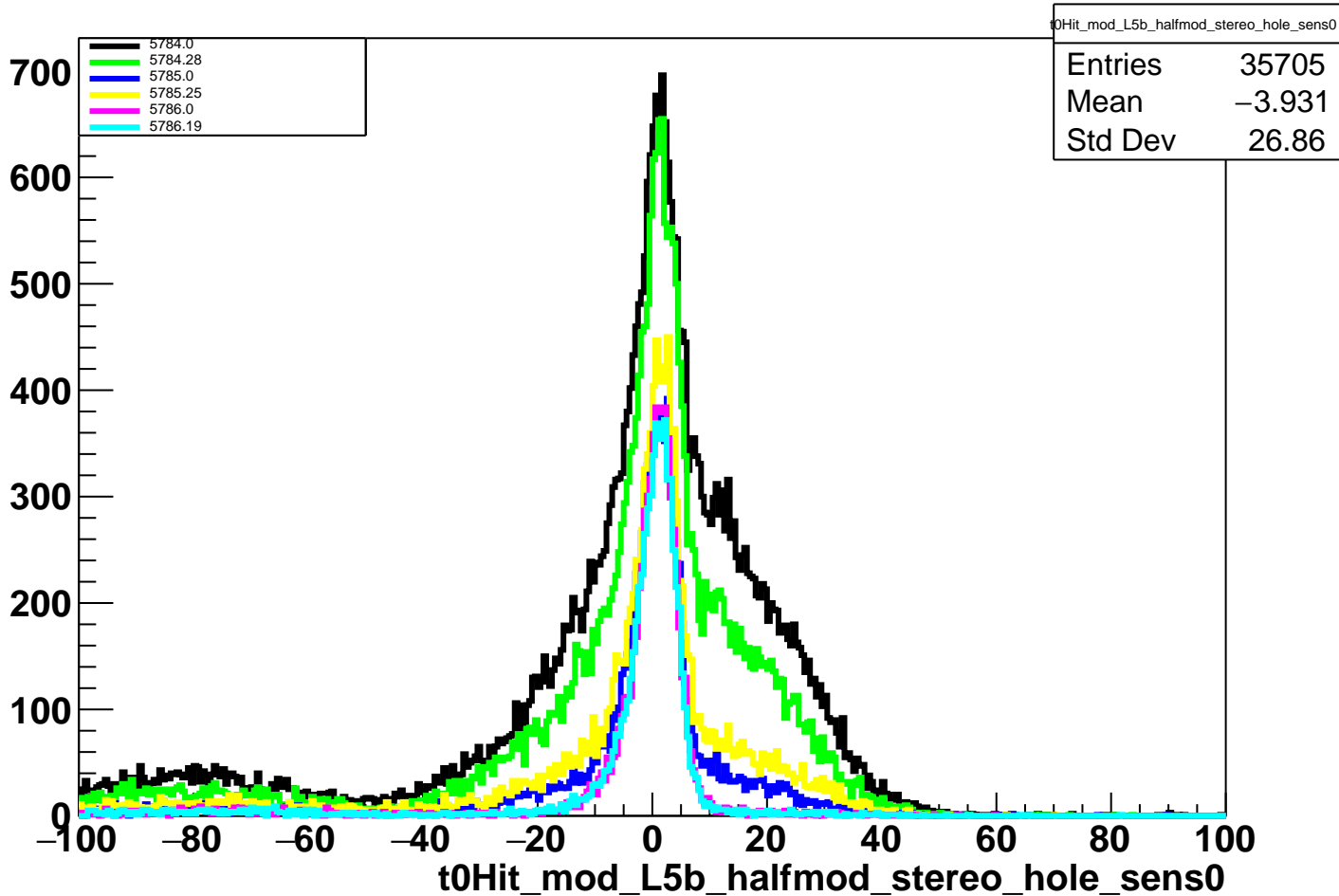
mod_L5b_halfmod_axial_hole_sens0



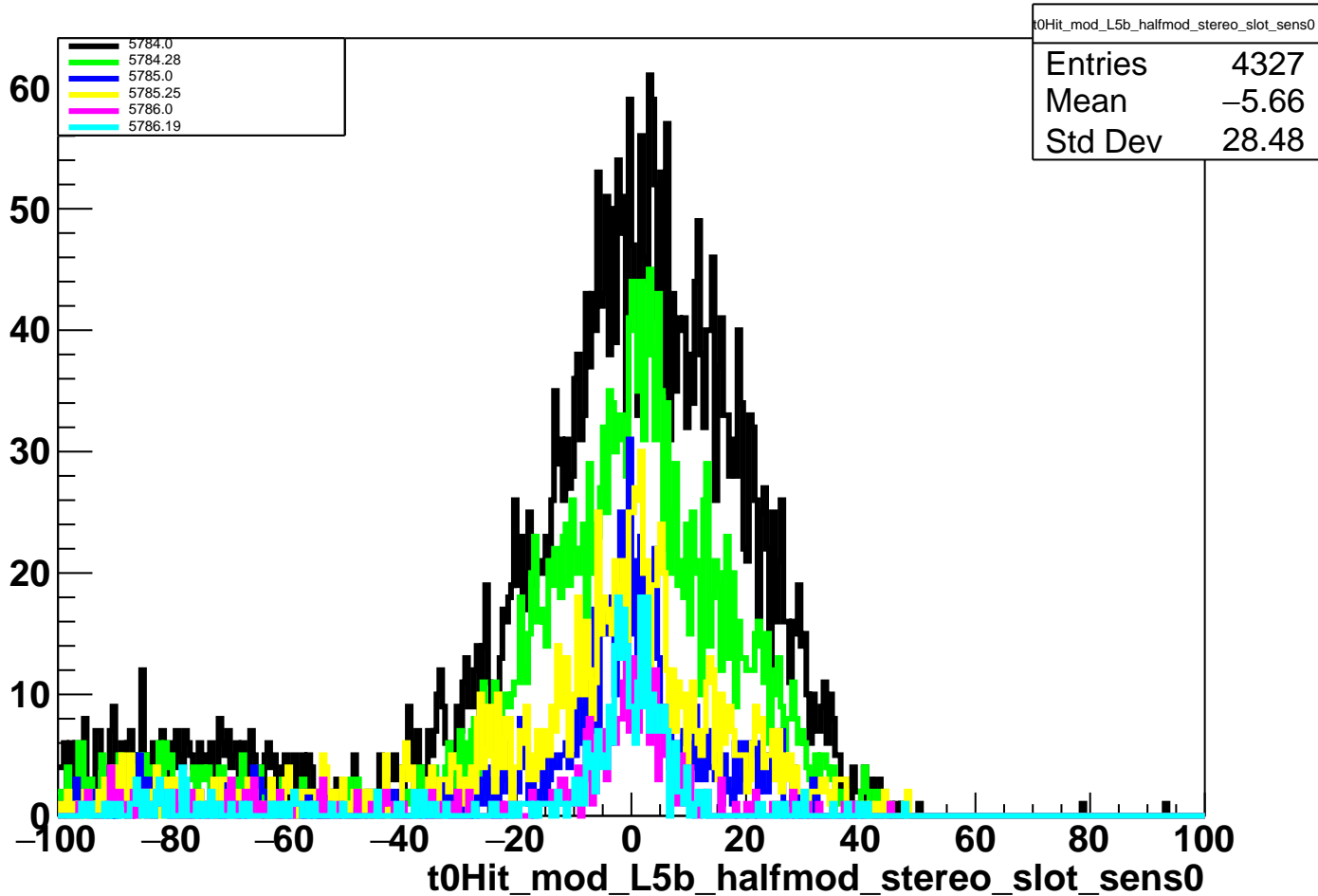
mod_L5b_halfmod_axial_slot_sens0



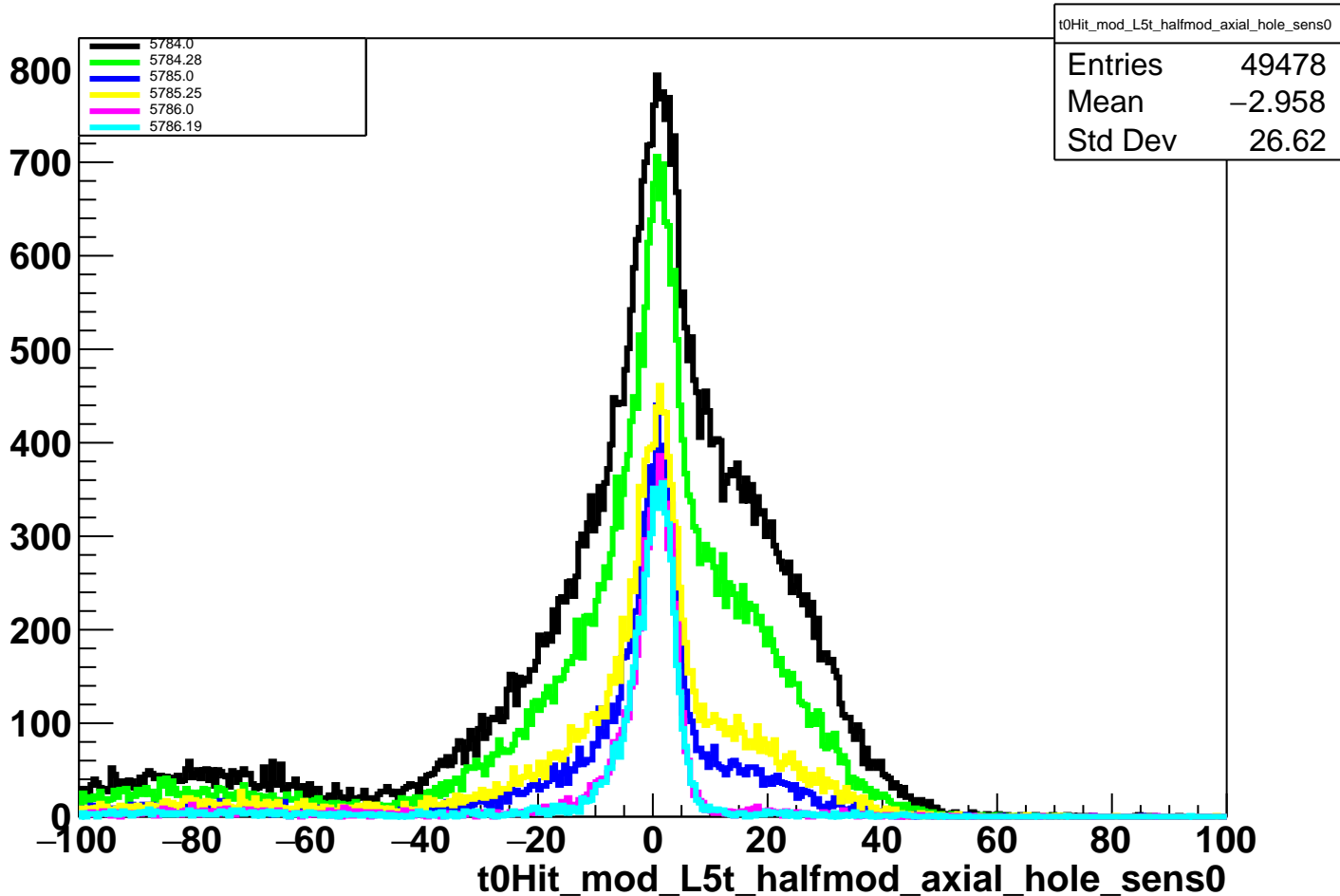
mod_L5b_halfmod_stereo_hole_sens0



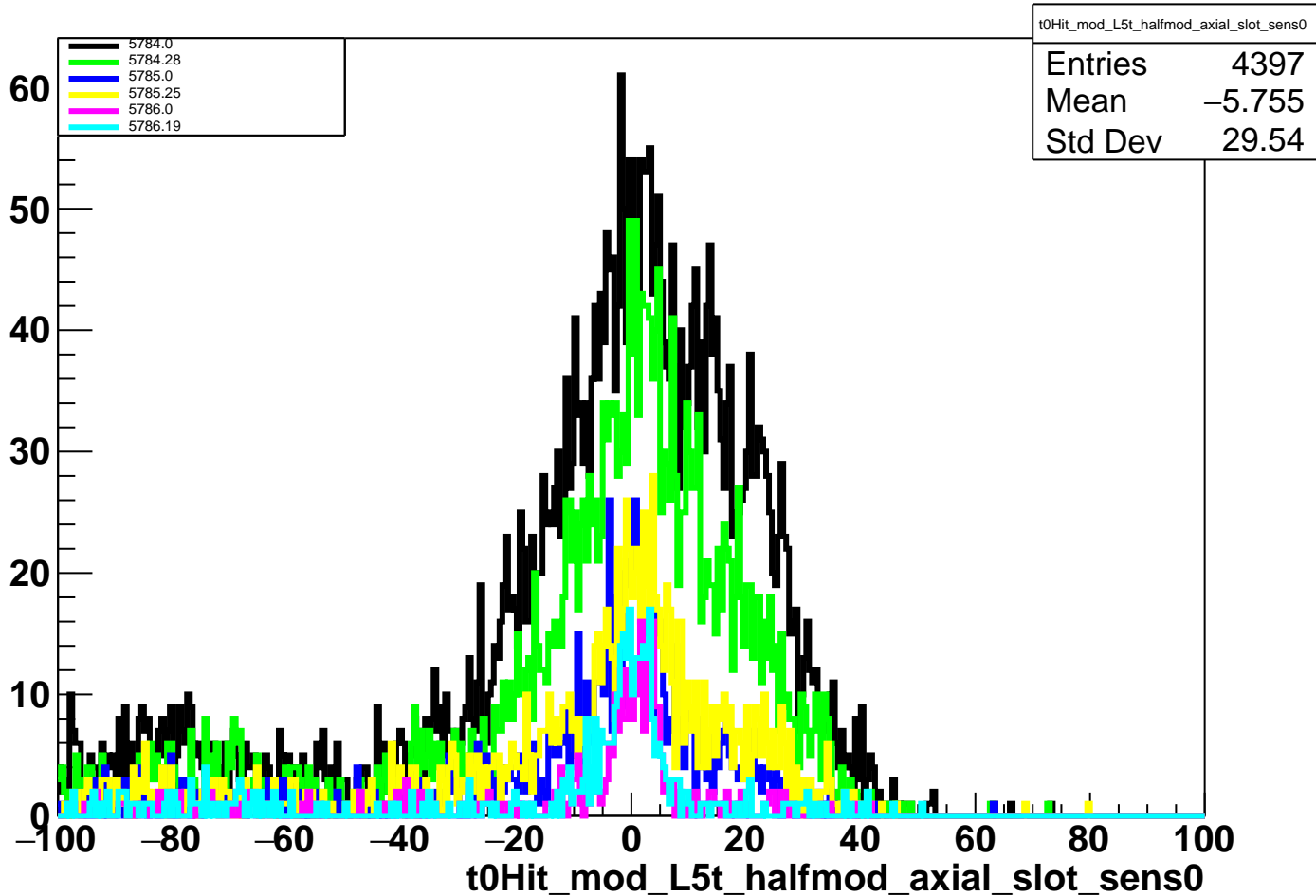
mod_L5b_halfmod_stereo_slot_sens0



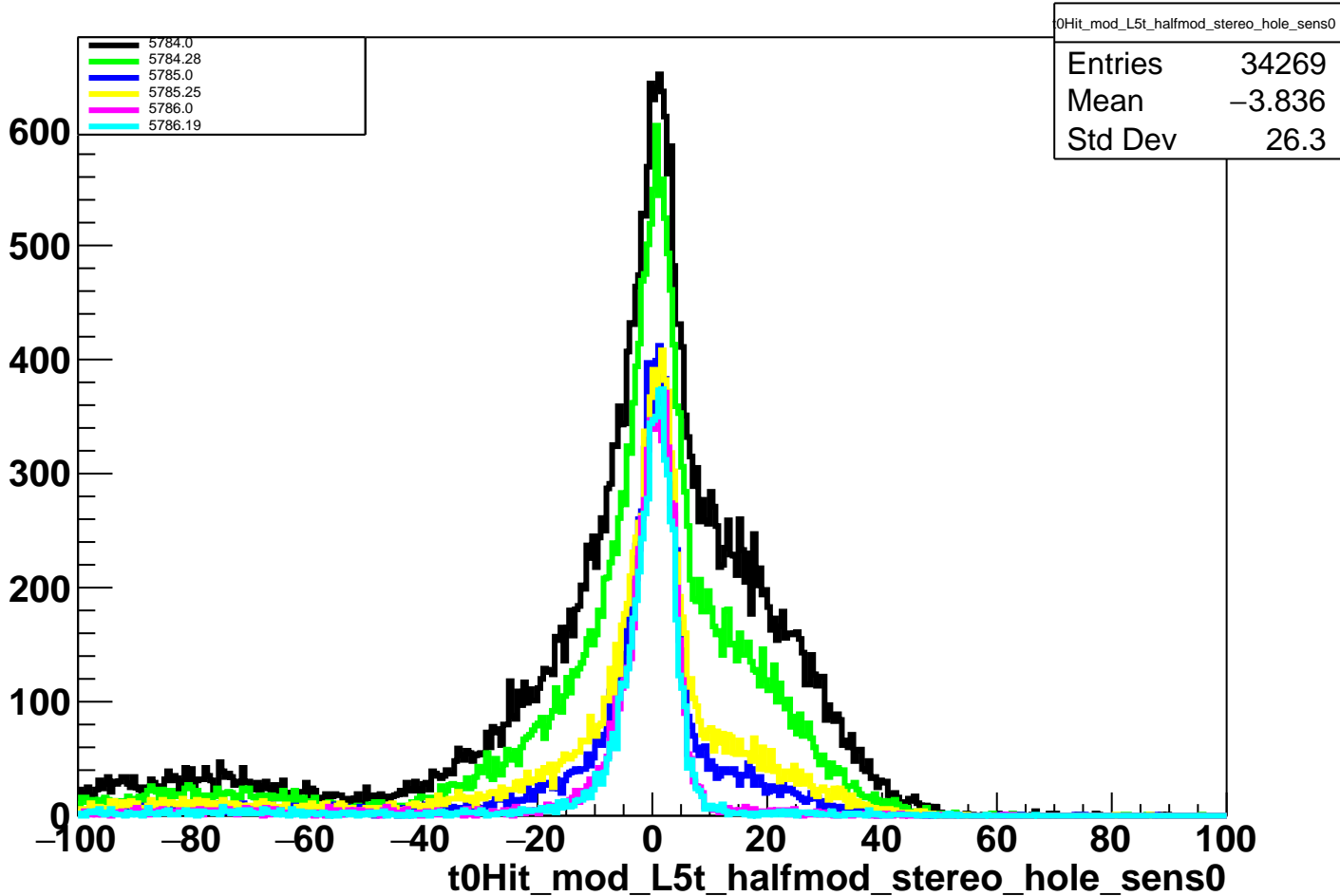
mod_L5t_halfmod_axial_hole_sens0



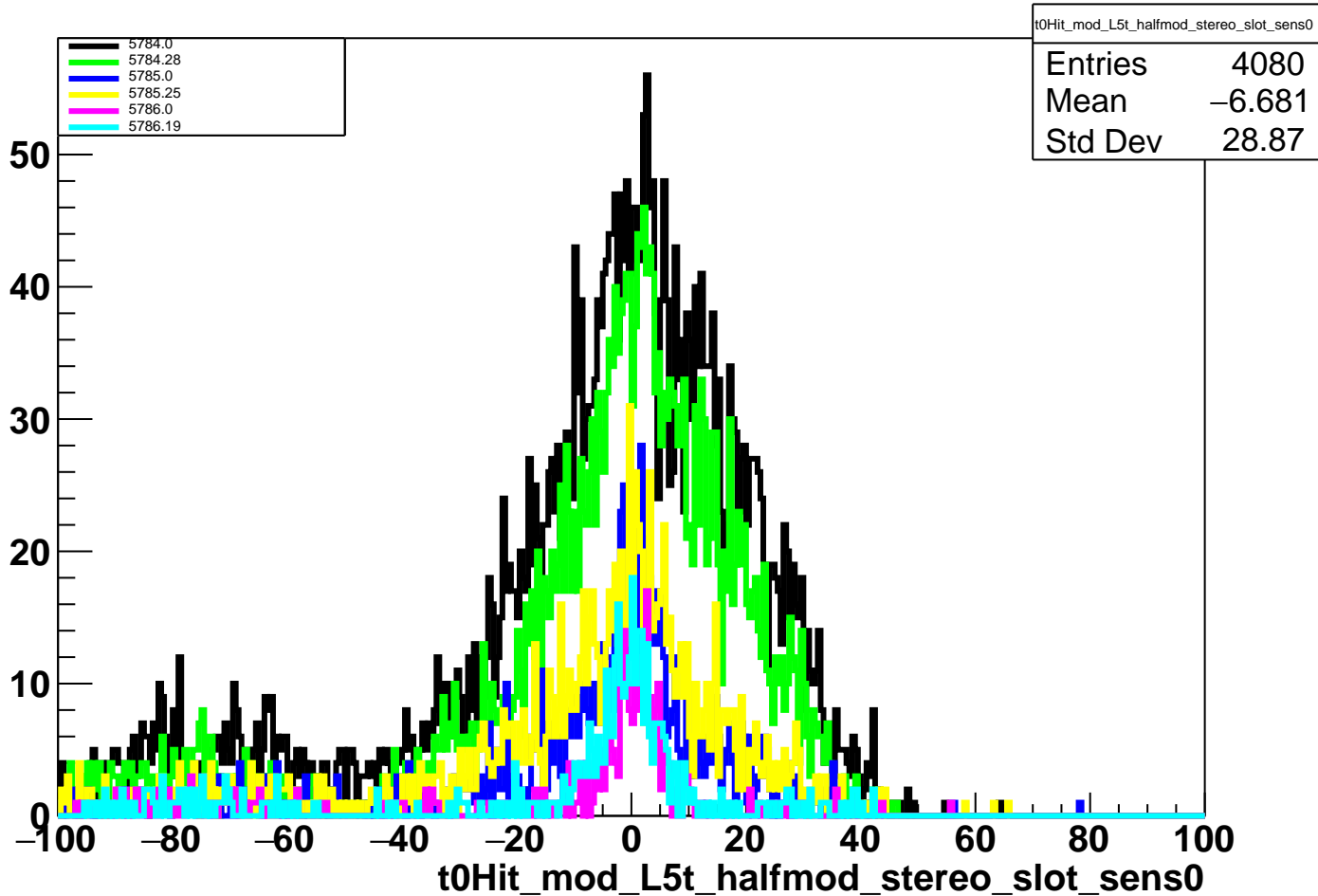
mod_L5t_halfmod_axial_slot_sens0



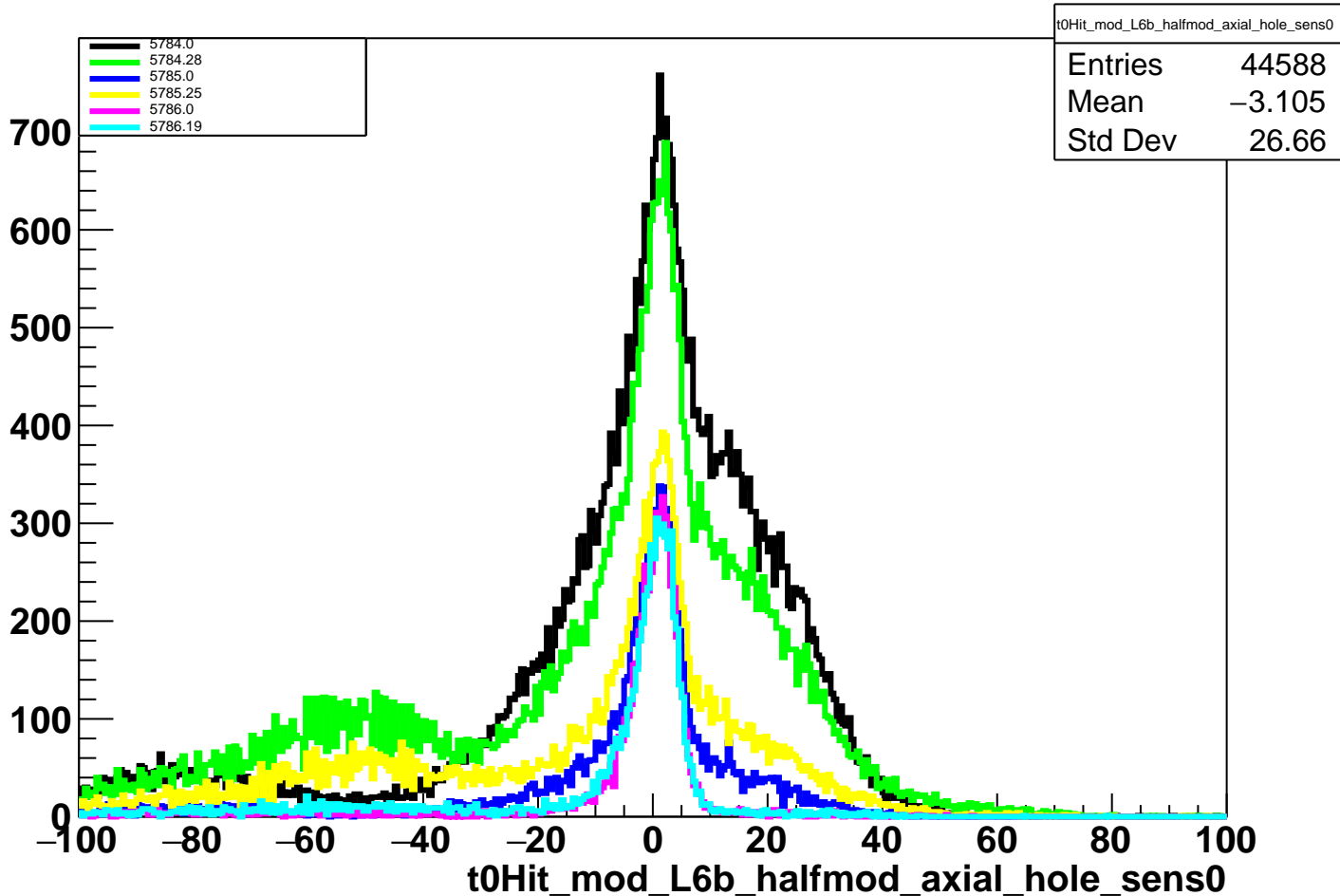
mod_L5t_halfmod_stereo_hole_sens0



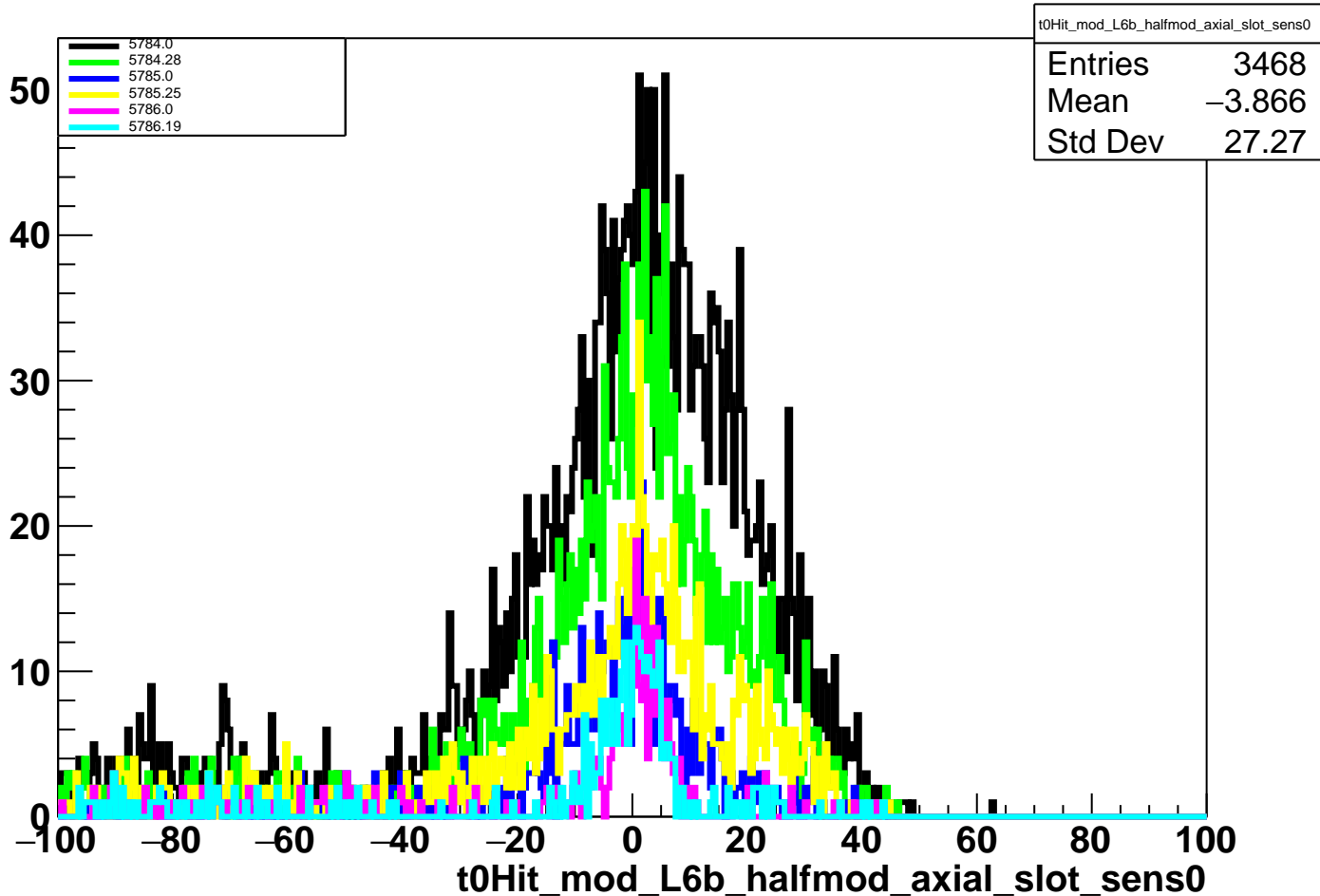
mod_L5t_halfmod_stereo_slot_sens0



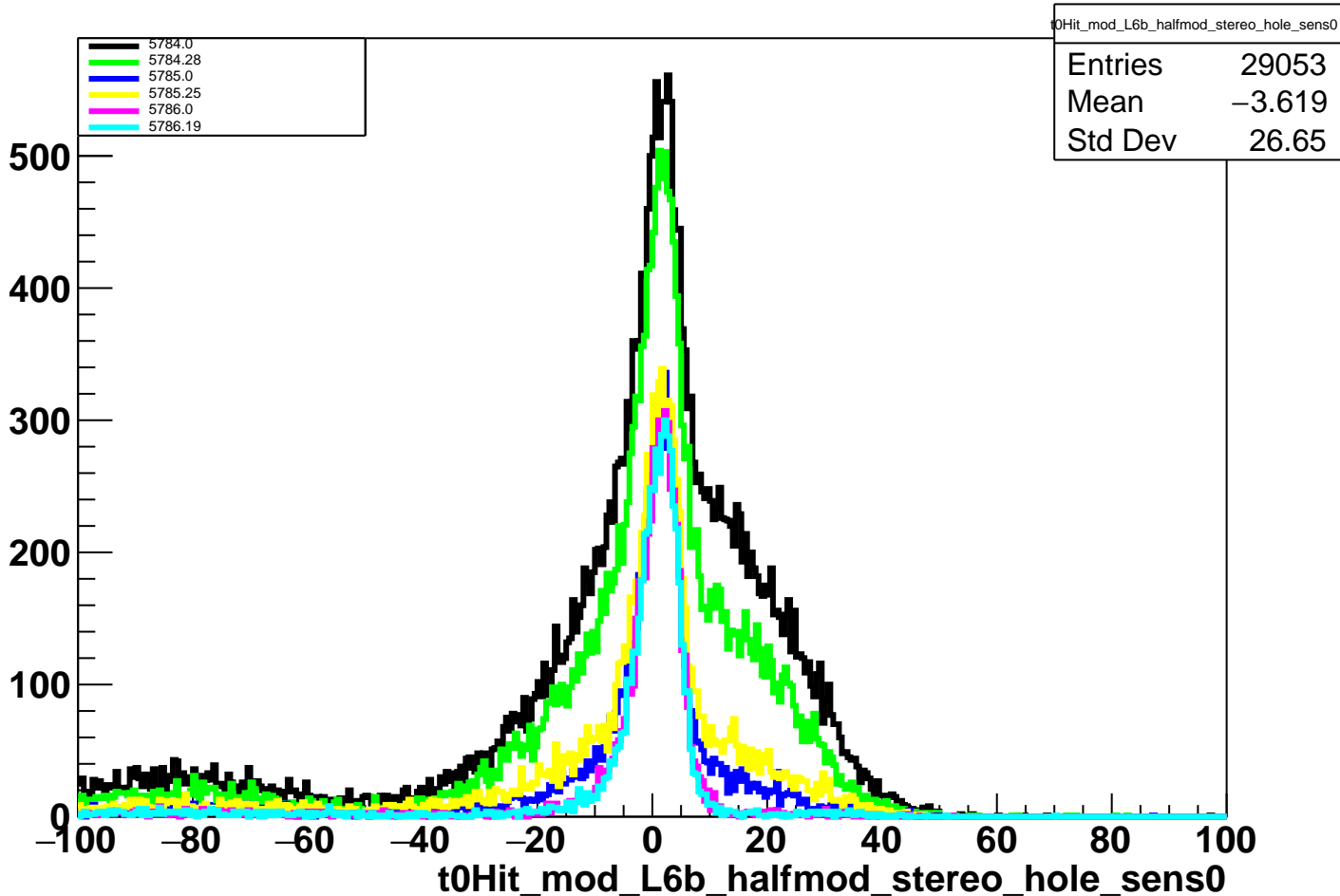
mod_L6b_halfmod_axial_hole_sens0



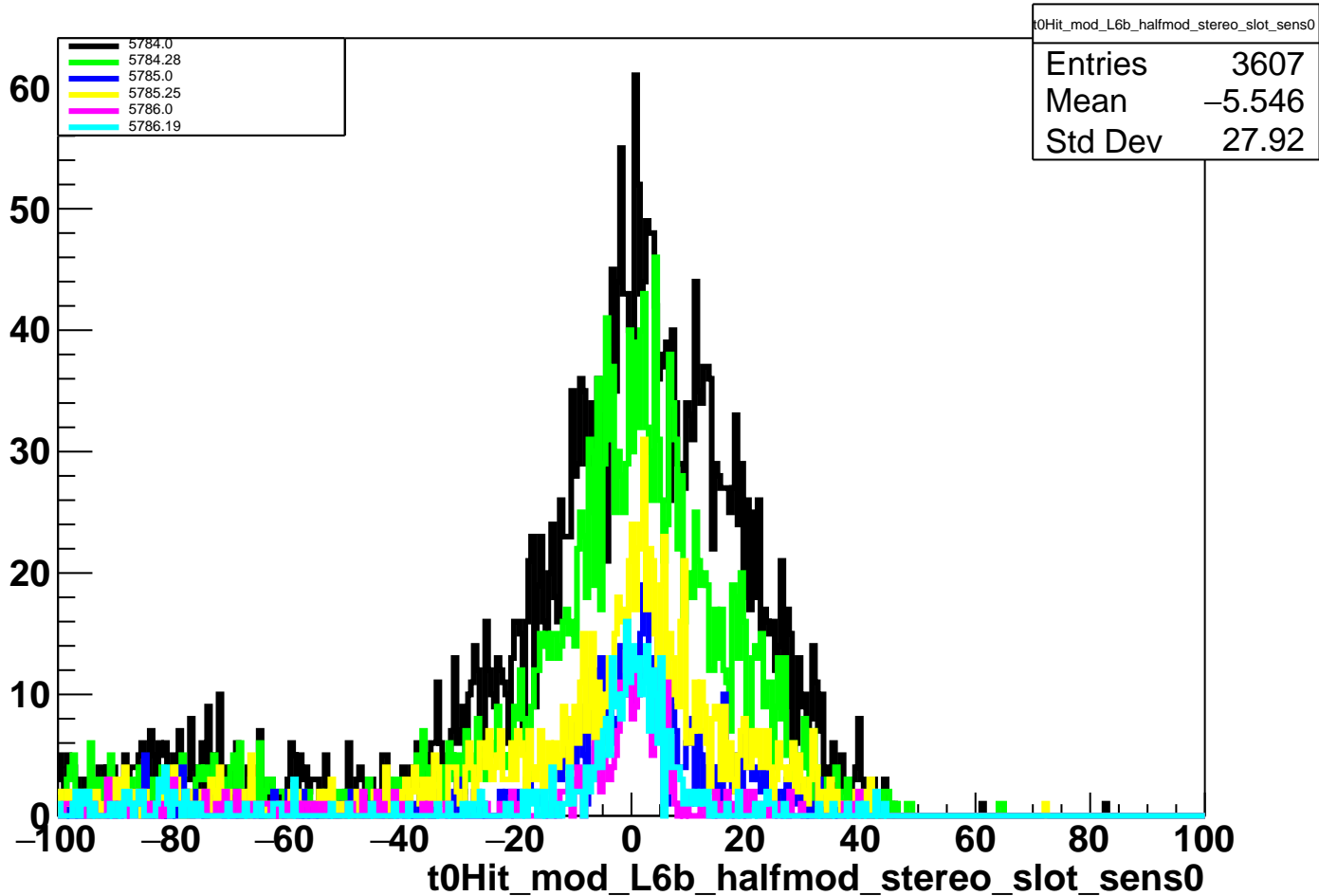
mod_L6b_halfmod_axial_slot_sens0



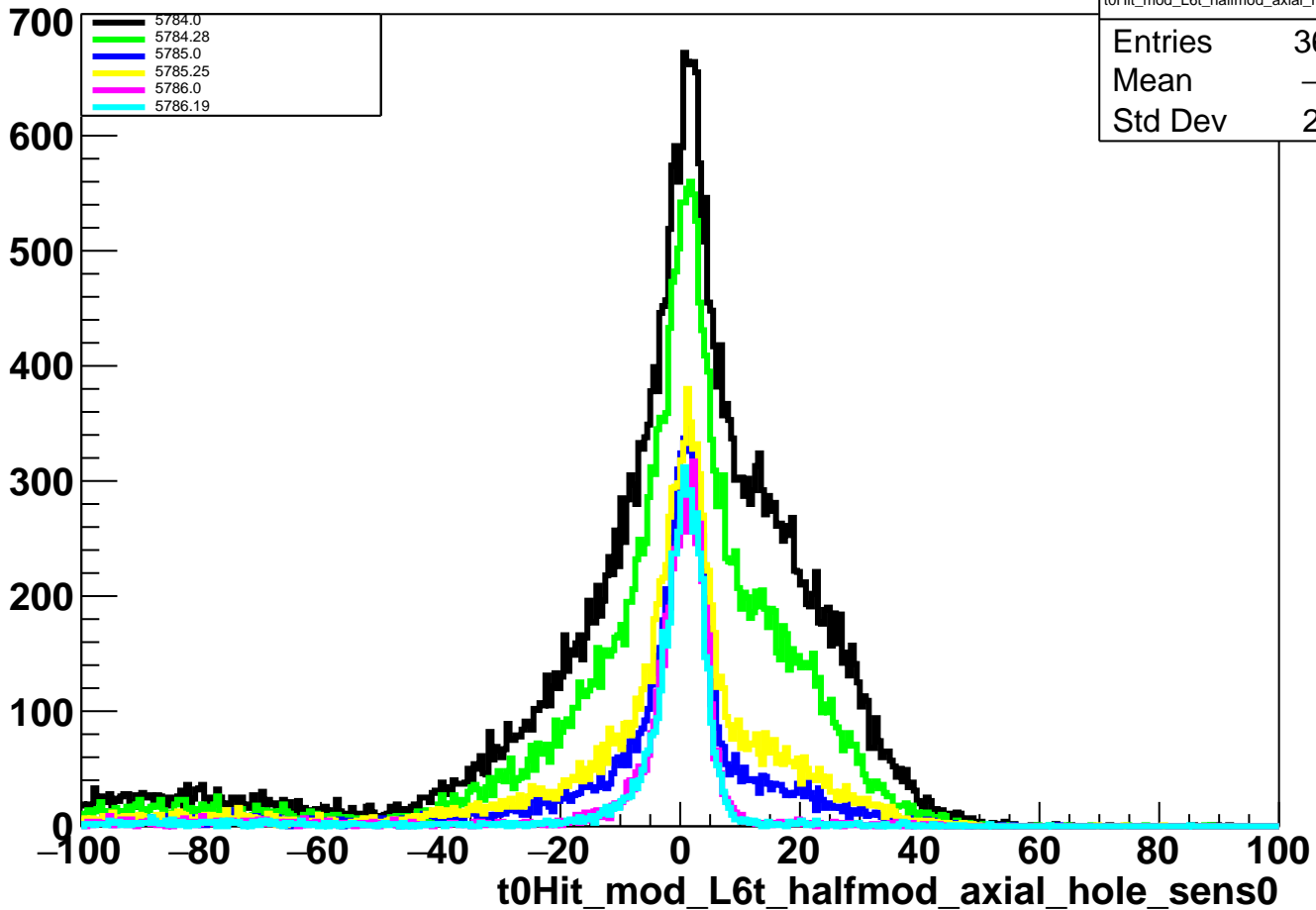
mod_L6b_halfmod_stereo_hole_sens0



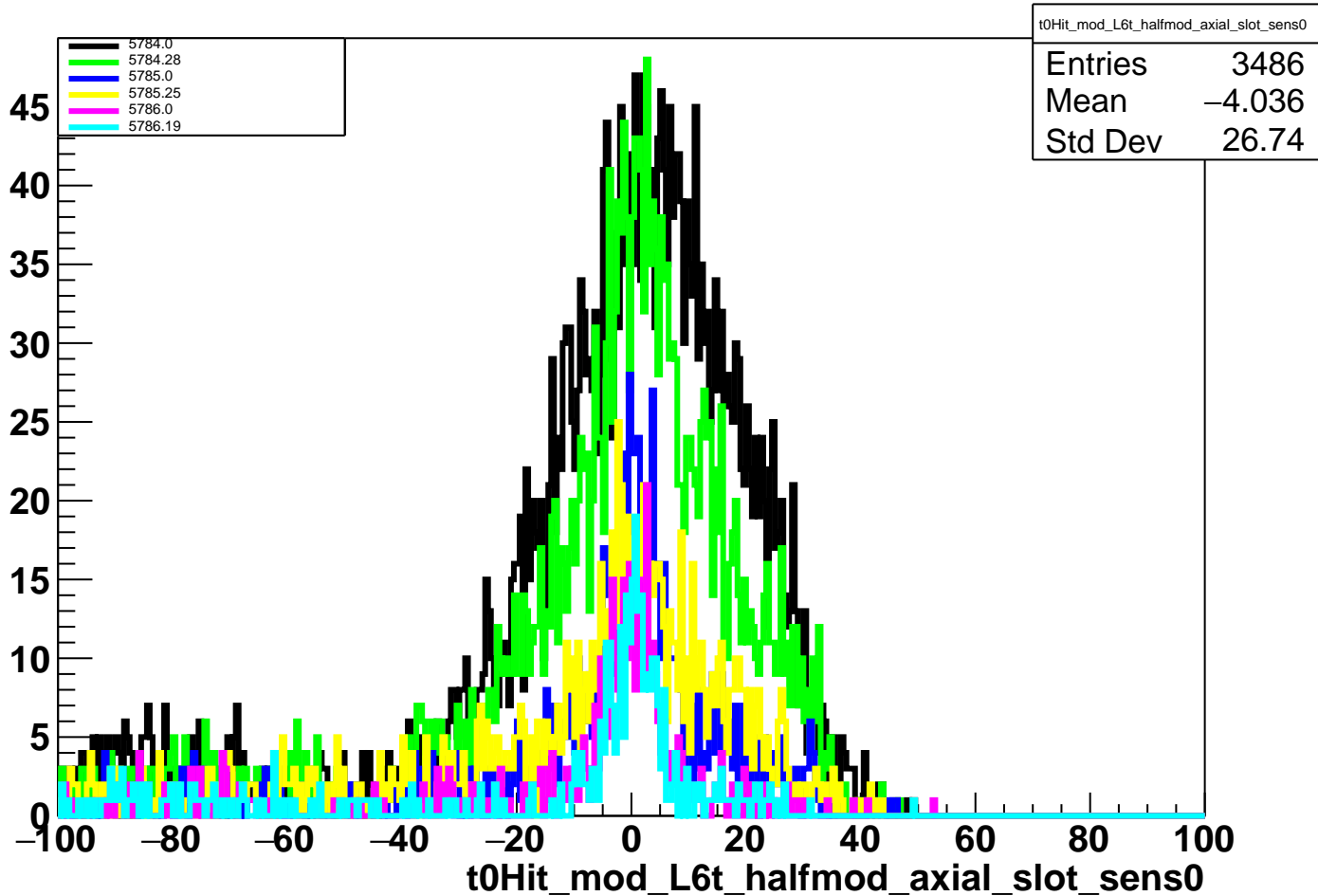
mod_L6b_halfmod_stereo_slot_sens0



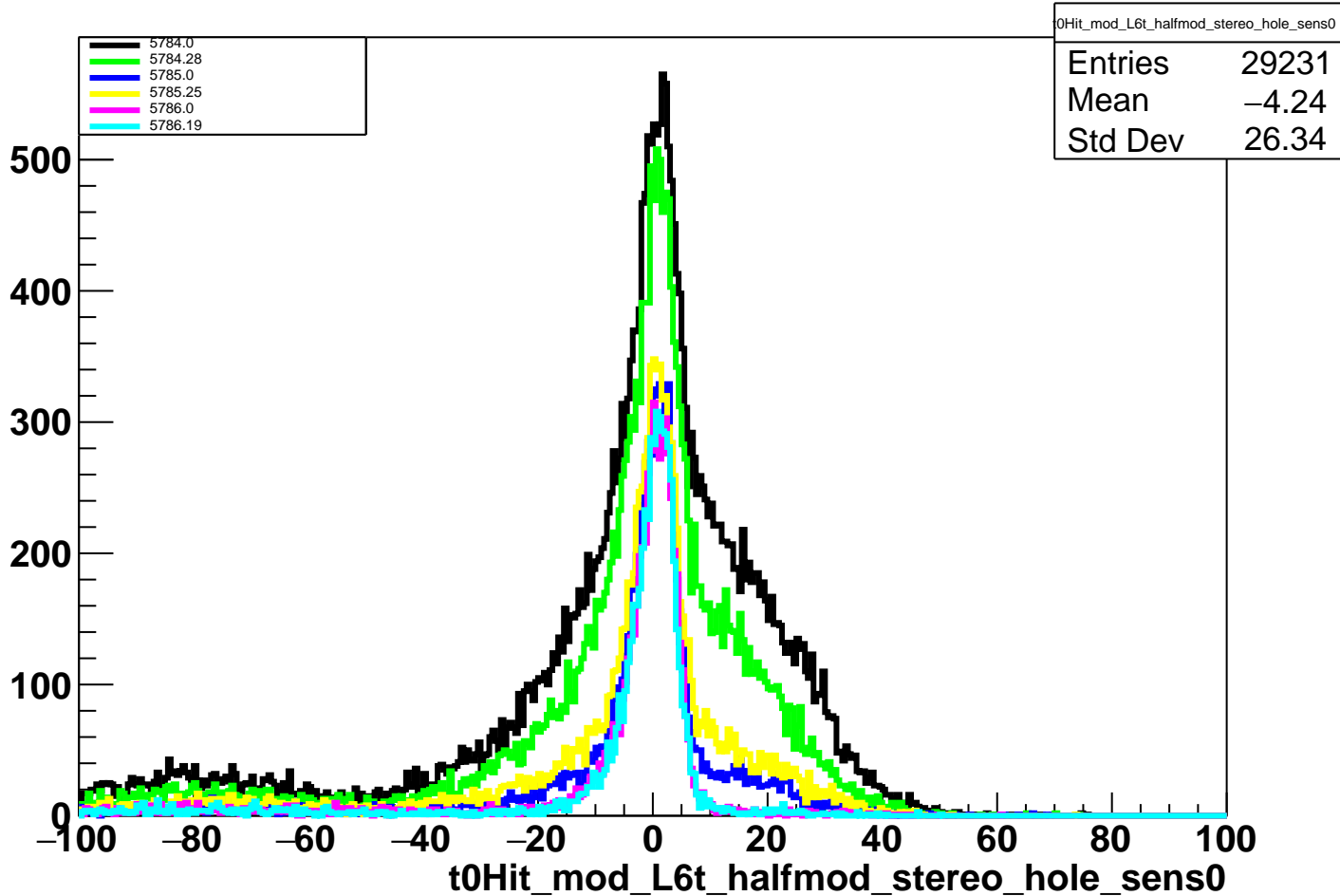
mod_L6t_halfmod_axial_hole_sens0



mod_L6t_halfmod_axial_slot_sens0



mod_L6t_halfmod_stereo_hole_sens0



mod_L6t_halfmod_stereo_slot_sens0

