

MPOD EPICS IOC status

- Borrowed an MPOD chassis for CLAS12 SVT group
 - 3 bias and 3 LV boards in the chassis
- Use GlueX-style code for MPOD IOC was installed on Hall B server
 - Reads the detector mapping from a SQLite database
 - Reads the existing boards/channels info from MPOD chassis
- Currently have hybrid bias voltages and LV for FE boards
 - Flange voltages are missing, easy to add, need LV boards
- Seems to work, with some noticeable problems
 - Slowly increases the memory size
 - Continuously increasing the CPU consumption to nearly 100%
 - Possibly a network issue
 - Needs to be investigated
- Bryan will be traveling to SLAC to help setup the IOC and voltage GUIs for may test.
 - SVT group will need to provide a RHEL6 computer with some specific packages installed to run this IOC.
 - Bryan needs to know the travel dates by April.
 - Can e-mail the requirements for the server to Pelle or Sho.