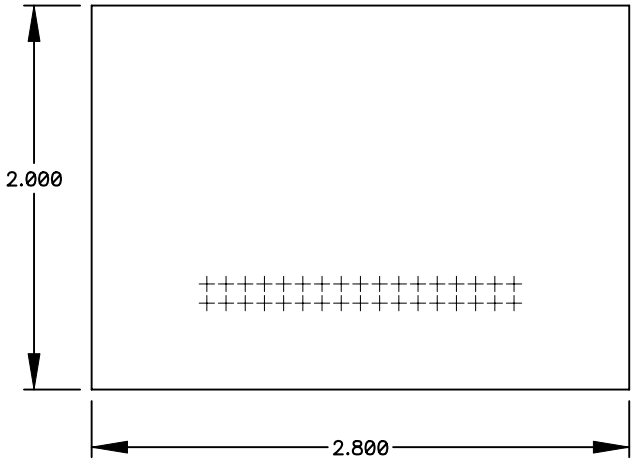
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



COMPONENT
SOLDER

DETAIL A
DOUBLE LAYER CONSTRUCTION
SCALE: NONE

Drill Table			
Hole Dia (inch)	Symbol	Quantity	Plated
0.046	+	34	Yes

NOTES:
1. BASE MATERIAL SHALL BE LAMINATED GLASS EPOXY, NEMA GRADE FR-4 (SHALL MEET UL 94V-0).
2. FINISHED BOARD THICKNESS TO BE .062 +/-.005" EXCLUDING COPPER FOIL.
3. TRACE LAYER TO BE 1 OZ. COPPER.
4. FABRICATE BOARDS IN ACCORDANCE WITH IPC-RB-6011 AND IPC-RB-6012, CLASS 2.
5. FABRICATION PROCESS SHALL BE UL APPROVED AND PCB SHALL BE IDENTIFIED WITH MANUFACTURERS APPROVED LOGO AND TYPE DESIGNATION IN ETCH.
6. CONSTRUCTION OF BOARD AS SHOWN IN DETAIL "A".
7. ALL HOLE SIZES SPECIFIED ARE PLATED.
8. FOR ARTWORK SEE DWG NO. 0073T0103.

UNLESS OTHERWISE SPECIFIED DIMENSIONS AND TOLERANCES ARE IN INCHES AND APPLY TO THE FINISHED PART

TOLERANCE ON:

2 PLACE DEC	3 PLACE DEC	ANGLES
±0.02	±0.005	±1/2°

MATL 1 2 3

FINISH ———

DRAWN J. BEHRENDT DATE 5/8/08

APPROVED M. HOEFERKAMP DATE

APPROVED ——— DATE

APPROVED ——— DATE

UNIVERSITY OF NEW MEXICO
DEPARTMENT OF PHYSICS AND ASTRONOMY
ELECTRONICS SHOP
ALBUQUERQUE, NEW MEXICO, USA 87131

TITLE
TEST ADAPTER FABRICATION

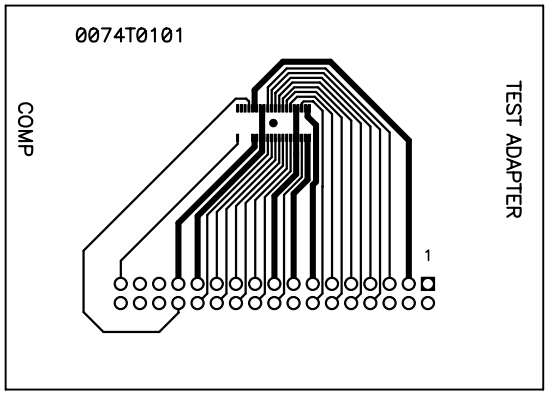
DWG FILENAME 0074t01.pcb

PLOTTED Thu May 08, 2008 11:05:08

SIZE A | DWG NO 0074T0102 | REV —

SCALE 1/1 | SHEET 1 OF 1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



TOP

UNLESS OTHERWISE SPECIFIED DIMENSIONS AND TOLERANCES ARE IN INCHES AND APPLY TO THE FINISHED PART

TOLERANCE ON:

2 PLACE DEC	3 PLACE DEC	ANGLES
±0.02	±0.005	±1/2°

MATL: ———

FINISH: ———

DRAWN: J. BEHRENDT — DATE: 5/8/08

APPROVED: M. HOEFERKAMP — DATE:

APPROVED: ——— — DATE:

APPROVED: ——— — DATE:

UNIVERSITY OF NEW MEXICO
DEPARTMENT OF PHYSICS AND ASTRONOMY
ELECTRONICS SHOP
ALBUQUERQUE, NEW MEXICO, USA 87131

TITLE

TEST ADAPTER ARTWORK

DWG FILENAME: 0074t01.pcb

PLOTTED: Thu May 08, 2008 11:05:08

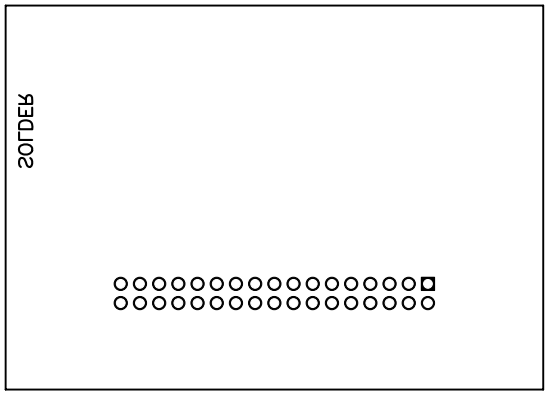
SIZE: A

DWG NO: 0074T0103

REV: —

SCALE: 1/1

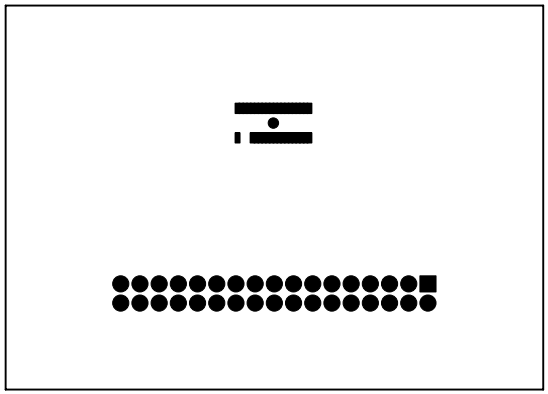
SHEET 1 OF 4



ARTWORK

SIZE		DWG NO	REV
A	———	0074T0103	—
SCALE 1/1	———	SHEET 2	

PLOTTED
Thu May 08, 2008 11:05:09

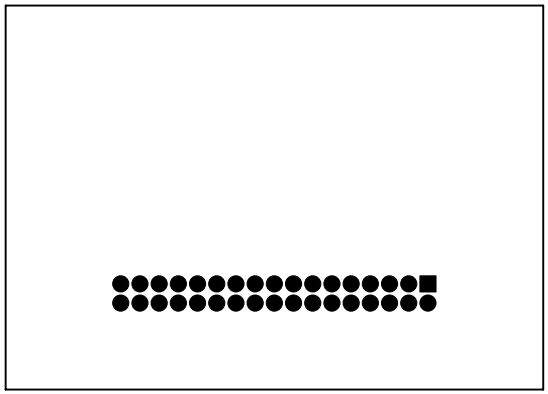


TOP MASK

ARTWORK

SIZE		DWG NO	REV
A	———	0074T0103	—
SCALE 1/1	———	SHEET 3	

PLOTTED
Thu May 08, 2008 11:05:09



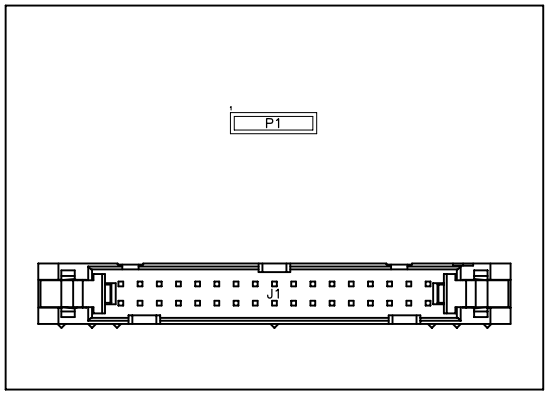
BOTTOM MASK

ARTWORK

SIZE		DWG NO	REV
A	———	0074T0103	—
SCALE 1/1	———	SHEET 4	

PLOTTED
Thu May 08, 2008 11:05:09

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



TOP ASSY

NOTES:
1. REFERENCE ELECTRICAL SCHEMATIC DRAWING, 0073T0101.
2. ASSEMBLE AND SOLDER PER IPC–A–610A AND IPC J–STD–001D.
3. OBSERVE POLARITY OF CAPACITORS, DIODES, ETC..
4. ELECTRICAL REFERENCE DESIGNATIONS ARE FOR REFERENCE ONLY AND NEED NOT APPEAR ON THE PARTS OR COMPONENTS UNLESS OTHERWISE SPECIFIED.

UNLESS OTHERWISE SPECIFIED DIMENSIONS AND TOLERANCES ARE IN INCHES AND APPLY TO THE FINISHED PART

TOLERANCE ON:

2 PLACE DEC	3 PLACE DEC	ANGLES
±0.02	±0.005	±1/2°

MATL: ———

FINISH: ———

DRAWN: J. BEHRENDT — DATE: 5/8/08

APPROVED: M. HOEFERKAMP — DATE:

APPROVED: ——— — DATE:

APPROVED: ——— — DATE:

UNIVERSITY OF NEW MEXICO
DEPARTMENT OF PHYSICS AND ASTRONOMY
ELECTRONICS SHOP
ALBUQUERQUE, NEW MEXICO, USA 87131

TITLE

TEST ADAPTER ASSEMBLY

DWG FILENAME: 0074t01.pcb

PLOTTED: Thu May 08, 2008 11:05:09

SIZE: A | DWG NO: 0074T0104 | REV: —

SCALE: 1/1 | SHEET 1 OF 1